



		15				
			VDD	7		
10	PTA0/SWD_CLK/TSI0_CH1/TPM0_CH5					
11	PTA1/TSI0_CH2/UART0_RX/TPM2_CH0					
12	PTA2/TSI0_CH3/UART0_TX/TPM2_CH1					
13	PTA3/SWD_DIO/TSI0_CH4/I2C1_SCL/TPM0_CH0					
14	PTA4/NMI/TSI0_CH5/I2C1_SDA/TPM0_CH1					PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI 32
17	PTA18/EXTAL0/UART1_RX/TPM_CLKIN0					PTD6/LLWU_P15/ADC0_SE7B/SPI1_MOSI/UART0_RX/SPI1_MISO 31
18	PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1					PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH5 30
19	PTA20/RESET					PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH4 29
20	PTB0/LLWU_P5/ADC0_SE8/TSI0_CH0/I2C0_SCL/TPM1_CH0					
21	PTB1/ADC0_SE9/TSI0_CH6/I2C0_SDA/TPM1_CH1					
22	PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/TSI0_CH14/I2C1_SCL/TPM0_CH0					PTE0/UART1_TX/RTC_CLKOUT/CMP0_OUT/I2C1_SDA 1
23	PTC2/ADC0_SE11/TSI0_CH15/I2C1_SDA/TPM0_CH1					PTE1/SPI1_MOSI/UART1_RX/SPI1_MISO/I2C1_SCL 2
24	PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT					PTE16/ADC0_DP1/ADC0_SE1/SPI0_PCS0/UART2_TX/TPM_CLKIN0 3
25	PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3					PTE17/ADC0_DM1/ADC0_SE5A/SPI0_SCK/UART2_RX/TPM_CLKIN1/LPTMR0_ALT3 4
26	PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT					PTE18/ADC0_DP2/ADC0_SE2/SPI0_MOSI/I2C0_SDA/SPI0_MISO 5
27	PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO					PTE19/ADC0_DM2/ADC0_SE6A/SPI0_MISO/I2C0_SCL/SPI0_MOSI 6
28	PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI					PTE30/DAC0_OUT/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN1 9
			VSS			
			VSSA			
			EX_PAD			
	<Value>	16		8		
				33		