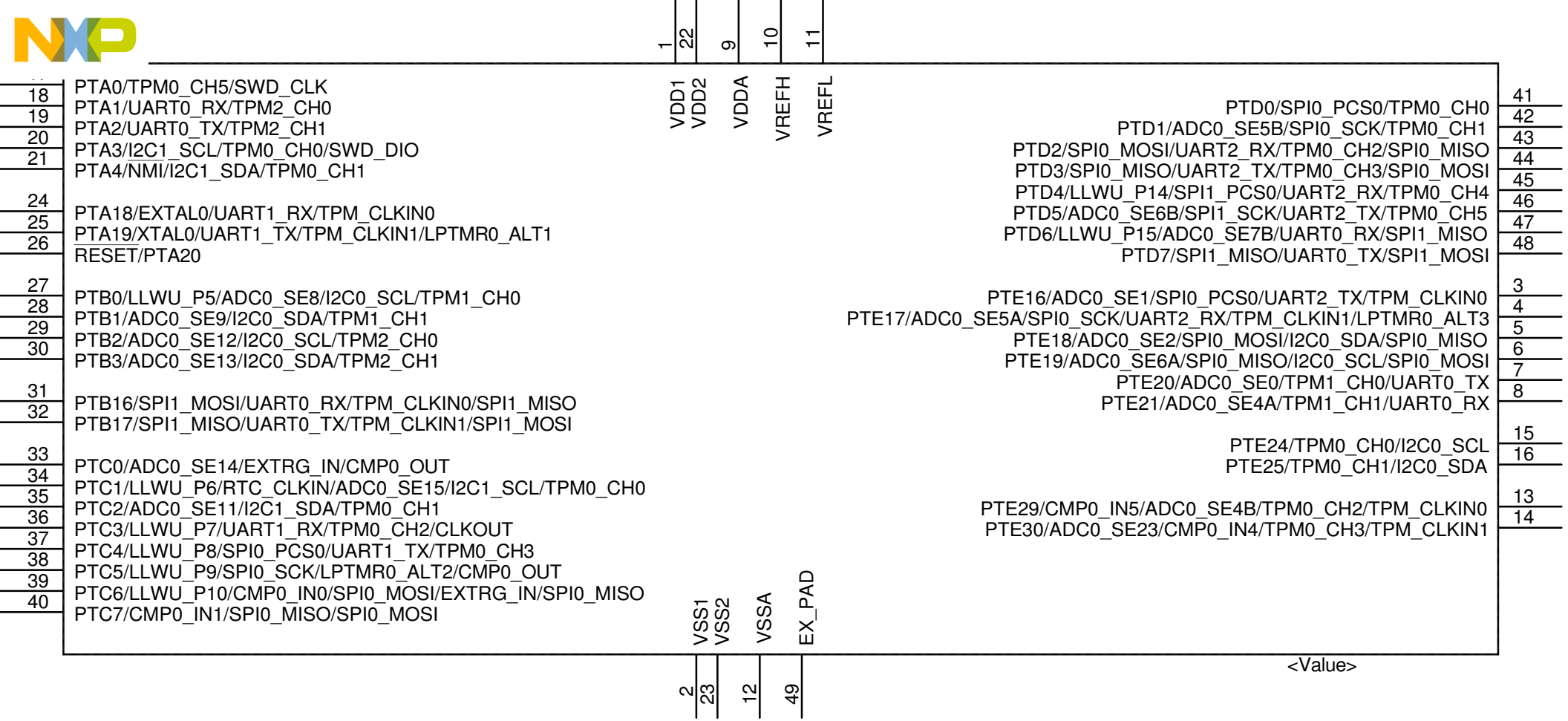
1
22
9
10
11
VDD1
VDD2
VDDA
VREFH
VREFL
18 PTA0/TPM0_CH5/SWD_CLK
19 PTA1/UART0_RX/TPM2_CH0
20 PTA2/UART0_TX/TPM2_CH1
21 PTA3/I2C1_SCL/TPM0_CH0/SWD_DIO
PTA4/NMI/I2C1_SDA/TPM0_CH1
24 PTA18/EXTAL0/UART1_RX/TPM_CLKIN0
25 PTA19/XTAL0/UART1_TX/TPM_CLKIN1/LPTMR0_ALT1
26 RESET/PTA20
27 PTB0/LLWU_P5/ADC0_SE8/I2C0_SCL/TPM1_CH0
28 PTB1/ADC0_SE9/I2C0_SDA/TPM1_CH1
29 PTB2/ADC0_SE12/I2C0_SCL/TPM2_CH0
30 PTB3/ADC0_SE13/I2C0_SDA/TPM2_CH1
31 PTB16/SPI1_MOSI/UART0_RX/TPM_CLKIN0/SPI1_MISO
32 PTB17/SPI1_MISO/UART0_TX/TPM_CLKIN1/SPI1_MOSI
33 PTC0/ADC0_SE14/EXTRG_IN/CMP0_OUT
34 PTC1/LLWU_P6/RTC_CLKIN/ADC0_SE15/I2C1_SCL/TPM0_CH0
35 PTC2/ADC0_SE11/I2C1_SDA/TPM0_CH1
36 PTC3/LLWU_P7/UART1_RX/TPM0_CH2/CLKOUT
37 PTC4/LLWU_P8/SPI0_PCS0/UART1_TX/TPM0_CH3
38 PTC5/LLWU_P9/SPI0_SCK/LPTMR0_ALT2/CMP0_OUT
39 PTC6/LLWU_P10/CMP0_IN0/SPI0_MOSI/EXTRG_IN/SPI0_MISO
40 PTC7/CMP0_IN1/SPI0_MISO/SPI0_MOSI
41 PTD0/SPI0_PCS0/TPM0_CH0
42 PTD1/ADC0_SE5B/SPI0_SCK/TPM0_CH1
43 PTD2/SPI0_MOSI/UART2_RX/TPM0_CH2/SPI0_MISO
44 PTD3/SPI0_MISO/UART2_TX/TPM0_CH3/SPI0_MOSI
45 PTD4/LLWU_P14/SPI1_PCS0/UART2_RX/TPM0_CH4
46 PTD5/ADC0_SE6B/SPI1_SCK/UART2_TX/TPM0_CH5
47 PTD6/LLWU_P15/ADC0_SE7B/UART0_RX/SPI1_MISO
48 PTD7/SPI1_MISO/UART0_TX/SPI1_MOSI
3 PTE16/ADC0_SE1/SPI0_PCS0/UART2_TX/TPM_CLKIN0
4 PTE17/ADC0_SE5A/SPI0_SCK/UART2_RX/TPM_CLKIN1/LPTMR0_ALT3
5 PTE18/ADC0_SE2/SPI0_MOSI/I2C0_SDA/SPI0_MISO
6 PTE19/ADC0_SE6A/SPI0_MISO/I2C0_SCL/SPI0_MOSI
7 PTE20/ADC0_SE0/TPM1_CH0/UART0_TX
8 PTE21/ADC0_SE4A/TPM1_CH1/UART0_RX
15 PTE24/TPM0_CH0/I2C0_SCL
16 PTE25/TPM0_CH1/I2C0_SDA
13 PTE29/CMP0_IN5/ADC0_SE4B/TPM0_CH2/TPM_CLKIN0
14 PTE30/ADC0_SE23/CMP0_IN4/TPM0_CH3/TPM_CLKIN1
VSS1
VSS2
VSSA
EX_PAD
2
23
12
49
<Value>