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Revisions			
Rev	Description	Date	Approved
X1	Prototype initial	2020/3/17	Philippe Rousselle
A	Prototype release	2020/3/26	Philippe Rousselle

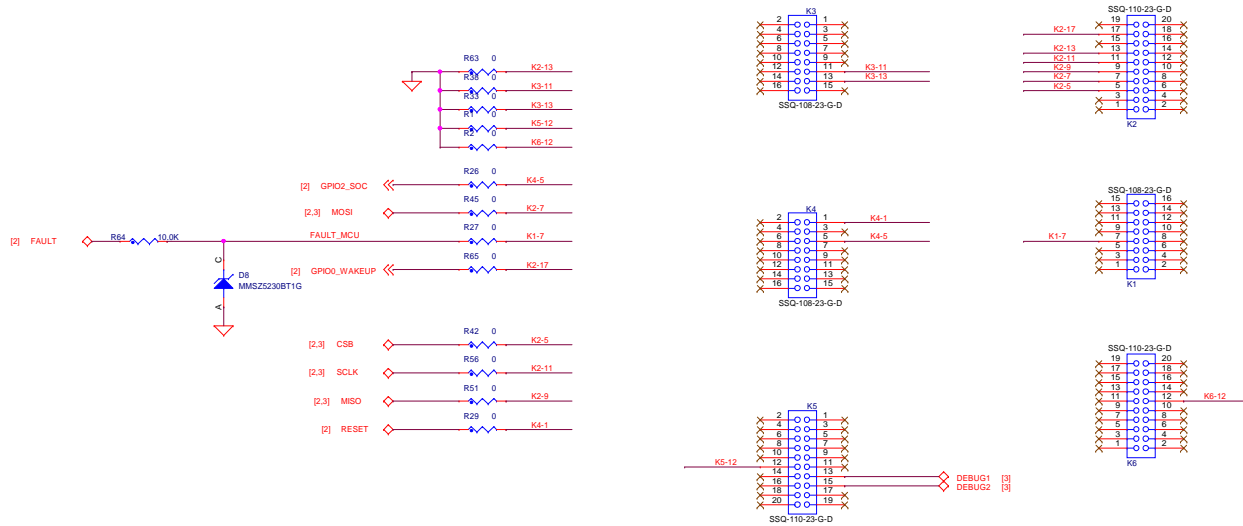
# FRDM33771CSPIEBV

## EVB MC33771C with SPI Interface

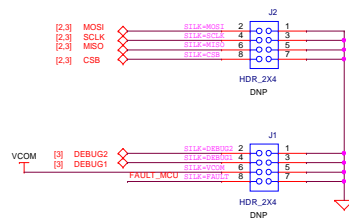
REF DES	ASSY_OPT	PAGE NAME
R41	DNP	02 - MC33771
J2_J1	DNP	03 - SPI interface

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## SPI interface with S32K144EVb



## SPI Analyzer Interface

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