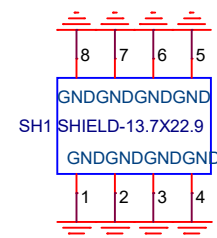
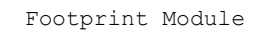


M16

M16



1
X BUBBLE-01
CI

X FID1
NC

X FID2
NC



This document contains information proprietary to NXP and shall not be used for engineering design, procurement or manufacture in whole or in part without the express written permission of NXP Semiconductors.

© NXP SEMICONDUCTORS Classification: Public

Designer:
SR-ASCO

DRAWING TITLE:	
----------------	--

OM15072-K32W

Drawn by:
SR-ASCO

Page Title:

HIGH POWER MODULE

Approved:
M.BAUDRY

	Size
--	------

Document Number

SCH-46122 PDF: SPF-46122

Rev

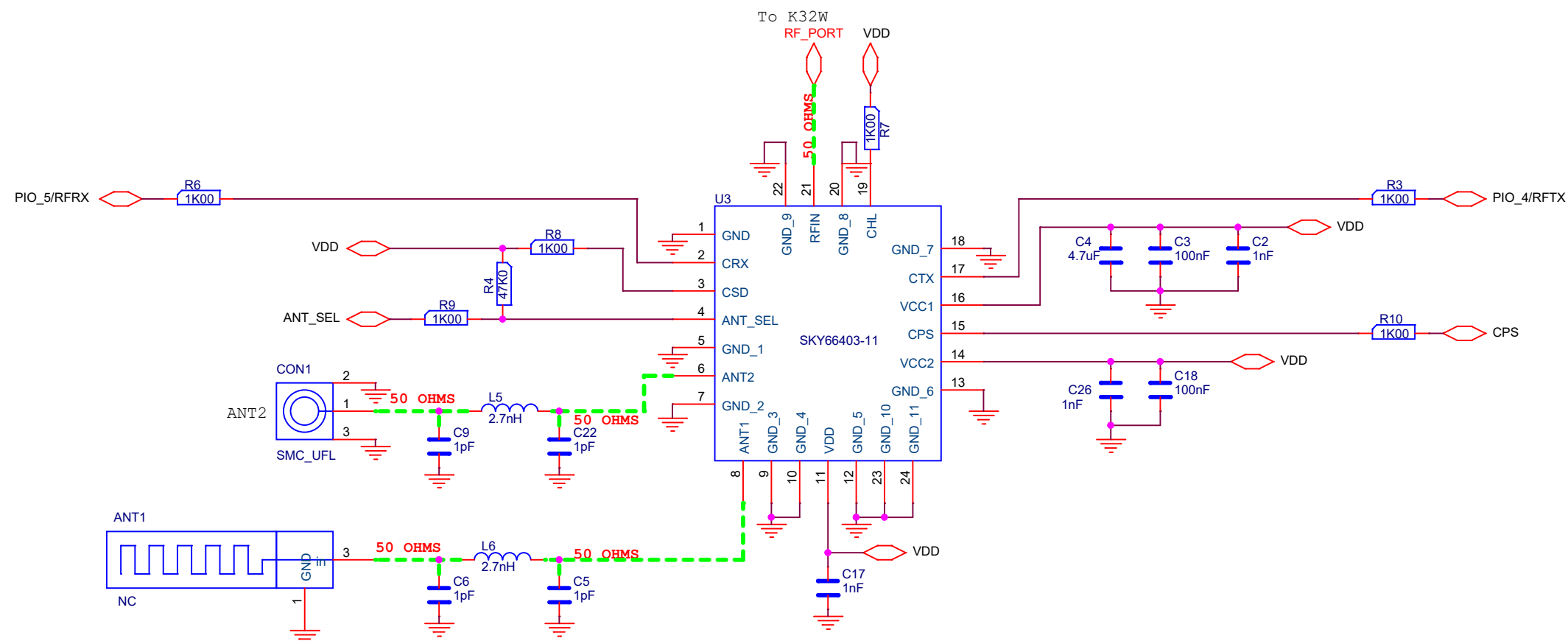
Date:

Monday, November 18, 2019

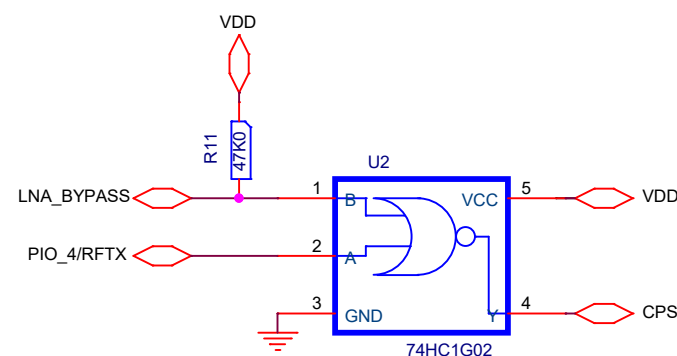
Sheet 1 of 2

Sheet 1 of 2

Front End Module



LOGIC



LOGIC INPUTS			LOGIC OUTPUTS				FEM STATE
LNA_Bypass	PIO_5/RFRX	PIO_4/RFTX	CSD Tie high	CRX =PIO_5/RFRX	CTX =PIO_4/RFTX	CPS =LNA_BYPASS NOR PIO_4/RFTX	
0	0	0	1	0	0	1	SLEEP
0	0	1	1	0	1	0	TX ON
0	1	0	1	1	0	1	RX BYP
0	1	1	1	1	1	0	N/A
1	0	0	1	0	0	0	SLEEP
1	0	1	1	0	1	0	TX ON
1	1	0	1	1	0	0	RX LNA
1	1	1	1	1	1	0	N/A

