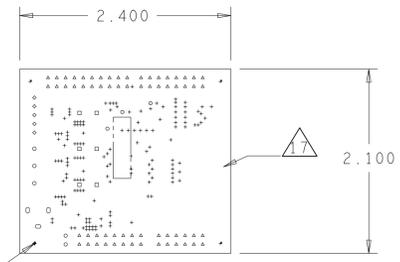


NOTES (UNLESS OTHERWISE SPECIFIED):

- THIS DRAWING SPECIFIES THE REQUIREMENTS FOR A PRINTED WIRING BOARD IN ACCORDANCE WITH SPECIFICATION IPC-A-600 CLASS 2 (LATEST REVISION).
- THE PWB MUST BE LEAD FREE ASSEMBLY PROCESS COMPATIBLE AND MUST BE ABLE TO HANDLE A MINIMUM OF 5 CYCLES AT 260 DEGREES CELSIUS FOR 10 SECONDS.
- BASE MATERIAL - LAMINATE AND PREPREG SHALL MEET IPC-4101B-26, 83 or 98
Tg - MUST BE GREATER THAN OR EQUAL TO 150 DEGREES CELSIUS.
Td - MUST BE GREATER THAN OR EQUAL TO 330 DEGREES CELSIUS.
- COPPER FOIL WEIGHT - SEE STACKUP DETAIL 'A'
- CHARACTERISTIC IMPEDANCE - NONE
- MINIMUM CONDUCTIVE WIDTH/SPACING TO BE .005"/.004"
- PLATING FINISH - BOTH SIDES ENIG (ELECTROLESS NICKEL IMMERSION GOLD):
.05080 - .232 MICRON (22 - 8 MICROINCH) OF GOLD OVER
2.540 - 6.350 MICRON (100 - 250 MICROINCH) OF NICKEL.

8. ALL THROUGH HOLE VIAS MAY BE PLATED SHUT.
9. SOLDERMASK - ORANGE COLOR LPI SOLDERMASK BOTH SIDES.
MODIFICATION OF SOLDERMASK IS NOT ALLOWED WITHOUT WRITTEN PERMISSION FROM FREESCALE.
10. SILKSCREEN - WHITE EPOXY INK, BOTH SIDES. NO SILK ON PADS.
11. ELECTRICAL TEST - 100% IPCD356.
12. PRINTED WIRING BOARD IS TO BE INDIVIDUALLY BAGGED.
13. DRC'S MUST BE RUN ON THE GERBER BEFORE BUILDING BOARDS.
UNLESS PRIOR APPROVAL IS GIVEN IN WRITING BY FREESCALE.
14. TEARDROPS MAYBE ADDED AT THE FAB HOUSE TO ALL SIGNAL LAYERS.
15. 2 SOLDER SAMPLES TO BE PROVIDED.
16. BASIC GRID INCREMENT AT 1:1 IS .0001.
17. SUPPLIER MARKINGS - ON SOLDER SIDE ONLY, WHERE SHOWN.
- MUST BE UL RECOGNIZED AND MUST HAVE AN ID THAT CONFORMS TO UL94V-0
18. THE PWB WILL BE MARKED AS LEAD FREE BY USE OF AN INK STAMP (LF)
19. THE PWB WILL BE MARKED AS LEAD FREE PROCESS COMPATIBLE BY USE OF AN INK STAMP (260°C)
20. ALL PLATED AND NON-PLATED THROUGH HOLES ARE TO BE DRILLED AT PRIMARY DRILL STEP.
ALL HOLE LOCATION TOLERANCES ARE TO BE +/- .002 IN REFERENCE TO THE PRIMARY DATUM.
21. FINISHED PCB MUST BE PANELIZED FOR ASSEMBLY ACCORDING TO CONTRACT MANUFACTURERS REQUIREMENTS.
THE ADDITION OF RAILS AND .125" NON-PLATED TOOLING HOLES ARE AT THE DISCRETION OF CONTRACT MANUFACTURER. PANELIZATION MUST BE APPROVED BY CONTRACT MANUFACTURER.
22. DO NOT PROVIDE SOLDERMASK CLEARANCES FOR THE SPECIFIC LOCATIONS
LEAVE SOLDERMASK AS THEY APPEAR ON CAD.
23. INTENTIONAL SHORT AT SH1, SH2, AND SH3 BETWEEN NETS GND AND GNDM.

REVISIONS				
ZONE	REV	DESCRIPTION	DATE	APPROVED
	A	ORIGINAL RELEASE	07/20/15	



.063 THICK +/- 10%

DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
8	12.0	+2.0/-12.0	PLATED	155
o	30.0	+3.0/-3.0	PLATED	6
o	40.0	+2.0/-2.0	PLATED	5
o	40.0	+3.0/-3.0	PLATED	5
+	42.0	+3.0/-3.0	PLATED	64
o	52.0	+3.0/-3.0	PLATED	3
+	125.0	+3.0/-3.0	NON-PLATED	4
o	63.0x40.0	+3.0/-3.0	PLATED	2
o	63.0x40.0	+3.0/-3.0	PLATED	1

16 PRIMARY DATUM GRID ORIGIN

FINISHED Cu WEIGHT

LAYER 1	COMPONENT SIDE	1 oz.
LAYER 2	SOLDER SIDE	1 oz.

DETAIL A
LAYER STACKUP
SCALE: NONE

PART NO. 170-28719		FREESCALE SEMICONDUCTOR	
THIS DOCUMENT CONTAINS INFORMATION PROPRIETARY TO FREESCALE AND SHALL NOT BE USED FOR ENGINEERING DESIGN PROCEDURE OR MANUFACTURE IN WHOLE OR IN PART WITHOUT THE CONSENT OF FREESCALE.		6501 WILLIAM CANNON DRIVE WEST AUSTIN, TEXAS 78735 USA	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: DECIMALS ANGLES .XX .01 .0-30' .XXX .005	APPROVALS DRAWN AVID TECH. CHECKED DESIGN ENGINEER	DATE 07/20/15	TITLE: PRINTED WIRING BOARD FRDM-MC-LVBLDC
SCALE 1/1	DO NOT SCALE DRAWING	SHEET 1 OF 1	REV A