

SOT1780-9

WLCSP36, wafer level chip-scale package, 36 terminals, 0.4 mm pitch, 2.8 mm x 2.52 mm x 0.525 mm body (backside coating included)

23 March 2020

Package information

1 Package summary

Terminal position code B (bottom)

Package type descriptive code WLCSP36

Package style descriptive code WLCSP (wafer level chip-size package)

Mounting method type S (surface mount)

Issue date01-11-2018Manufacturer package code98ASA01326D

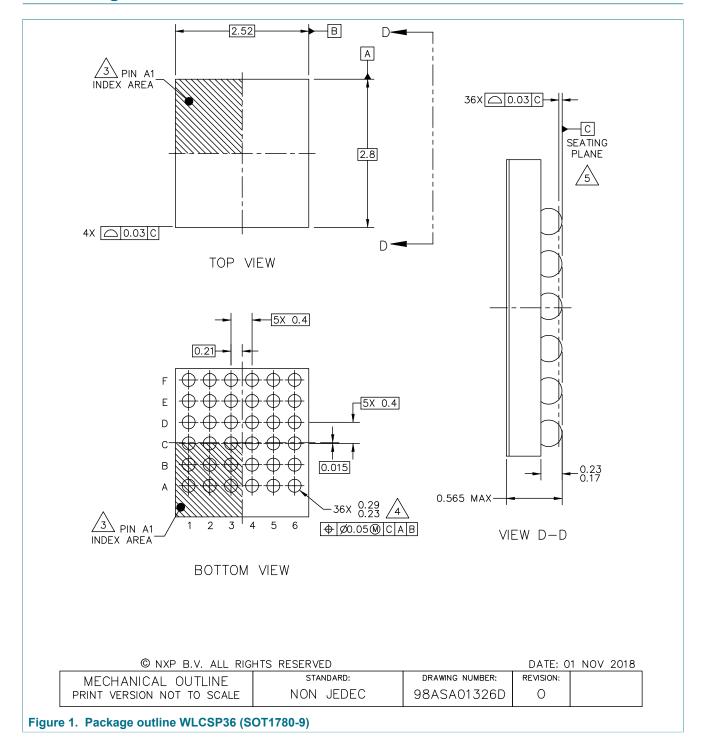
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	2.77	2.8	2.83	mm
package width	2.49	2.52	2.55	mm
package height	-	0.525	0.565	mm
nominal pitch	-	0.4	-	mm
actual quantity of termination	-	36	-	



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2 Package outline



WLCSP36, wafer level chip-scale package, 36 terminals, 0.4 mm pitch, 2.8 mm x 2.52 mm x 0.525 mm body (backside coating included)

3 Soldering

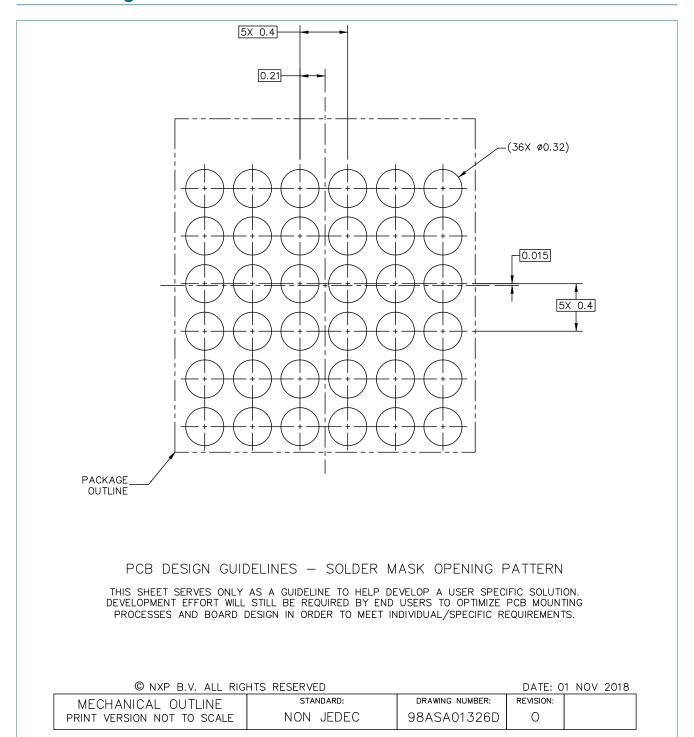


Figure 2. Reflow soldering footprint part1 for WLCSP36 (SOT1780-9)

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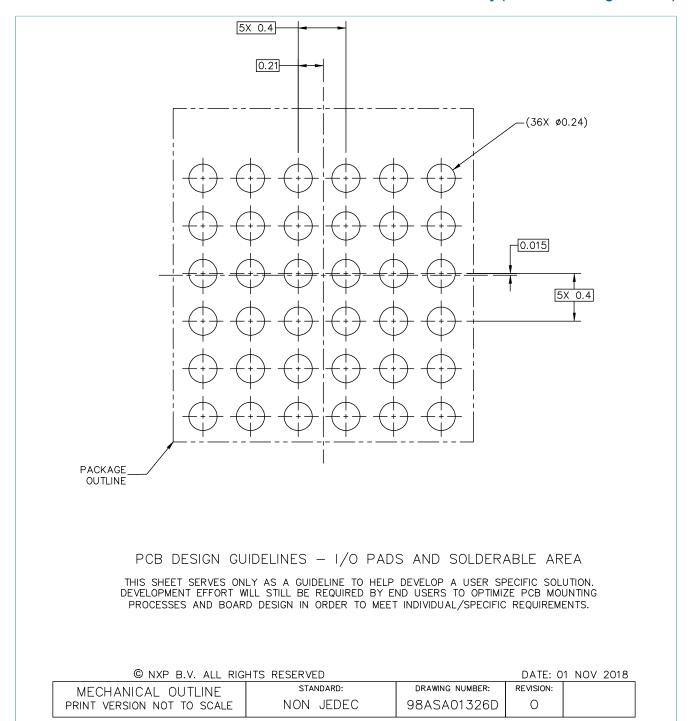
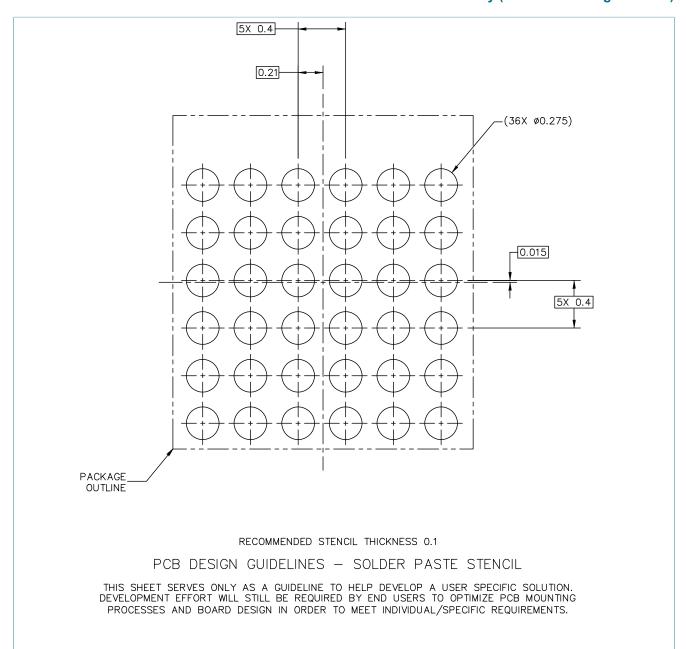


Figure 3. Reflow soldering footprint part2 for WLCSP36 (SOT1780-9)

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Figure 4. Reflow soldering footprint part3 for WLCSP36 (SOT1780-9)

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NOTES:

- 1. ALL DIMENSIONS IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

3. PIN A1 FEATURE SHAPE, SIZE AND LOCATION MAY VARY.

4. MAXIMUM SOLDER BALL DIAMETER MEASURED PARALLEL TO DATUM C.

 $\sqrt{5}$ \ DATUM C, THE SEATING PLANE, IS DETERMINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.

6. THIS PACKAGE HAS A BACK SIDE COATING THICKNESS OF 0.025.

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Figure 5. Package outline note WLCSP36 (SOT1780-9)

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