

# SOT1453-1

wafer level chip-scale package, 8 bumps

21 April 2016

Package information

## 1. Package summary

<b>Terminal position code</b>	B (bottom)
<b>Package type descriptive code</b>	WLCSP8
<b>Package type industry code</b>	WLCSP8
<b>Package style descriptive code</b>	WLCSP (wafer level chip-size package)
<b>Package style suffix code</b>	NA (not applicable)
<b>Mounting method type</b>	S (surface mount)
<b>Issue date</b>	2-3-2016

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	0.62	-	0.65	0.68	mm
E	package width	0.62	-	0.65	0.68	mm
A	seated height	0.28	-	0.305	0.33	mm
A <sub>2</sub>	package height	0.205	-	0.225	0.245	mm
e	nominal pitch	-	-	0.44	-	mm
n <sub>2</sub>	actual quantity of termination	-	-	8	-	



## 2. Package outline

WL CSP8: wafer level chip-scale package;  
8 bumps; 0.65 x 0.65 x 0.305 mm (Backside coating included)

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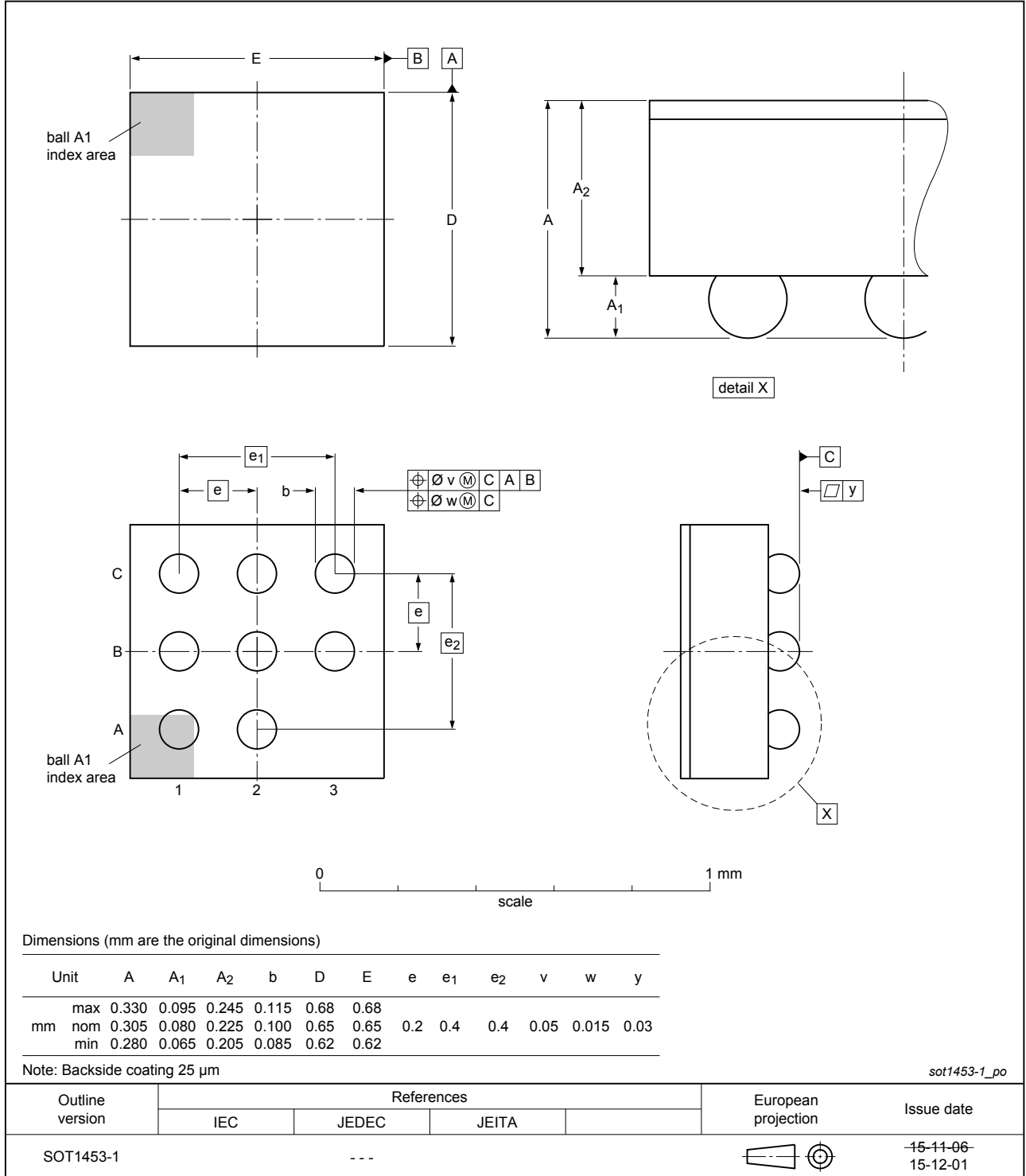


Fig. 1. Package outline WL CSP8 (SOT1453-1)

### 3. Legal information

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