

WLCSP6, , wafer level chip-size package; 6 terminals; 0.4 mm pitch; 1.18 mm x 0.84 mm x 0.455 mm body (backside coating included)

24 February 2021

Package information

1 Package summary

Terminal position code	B (bottom)
Package type descriptive code	WLCSP6
Package type industry code	WLCSP6
Package style descriptive code	WLCSP (wafer level chip-size package)
Mounting method type	S (surface mount)
Issue date	04-12-2020
Manufacturer package code	98ASA01666D

Table 1. Package summary

Parameter	Min	Nom	Мах	Unit
package length	1.15	1.18	1.21	mm
package width	0.81	0.84	0.87	mm
package height	-	0.455	0.485	mm
nominal pitch	-	0.4	-	mm
actual quantity of termination	-	6	-	

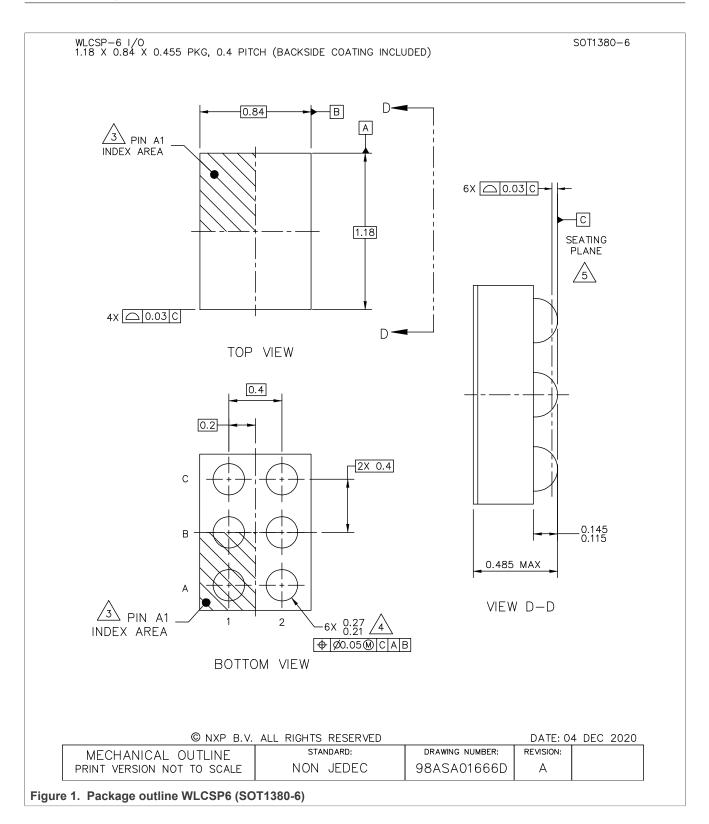


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SOT1380-6

WLCSP6, , wafer level chip-size package; 6 terminals; 0.4 mm pitch; 1.18 mm x 0.84 mm x 0.455 mm body (backside coating included)

2 Package outline

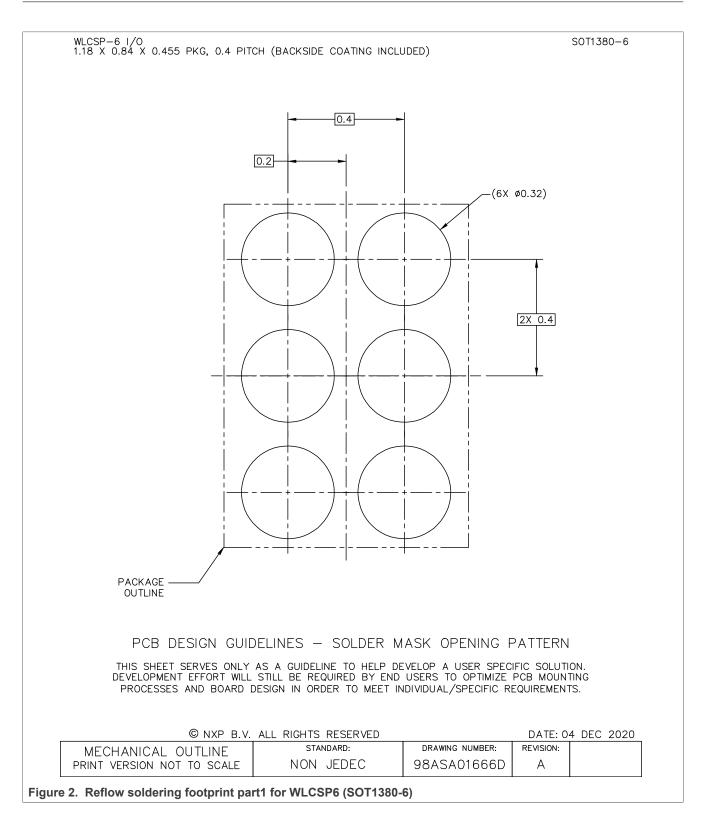


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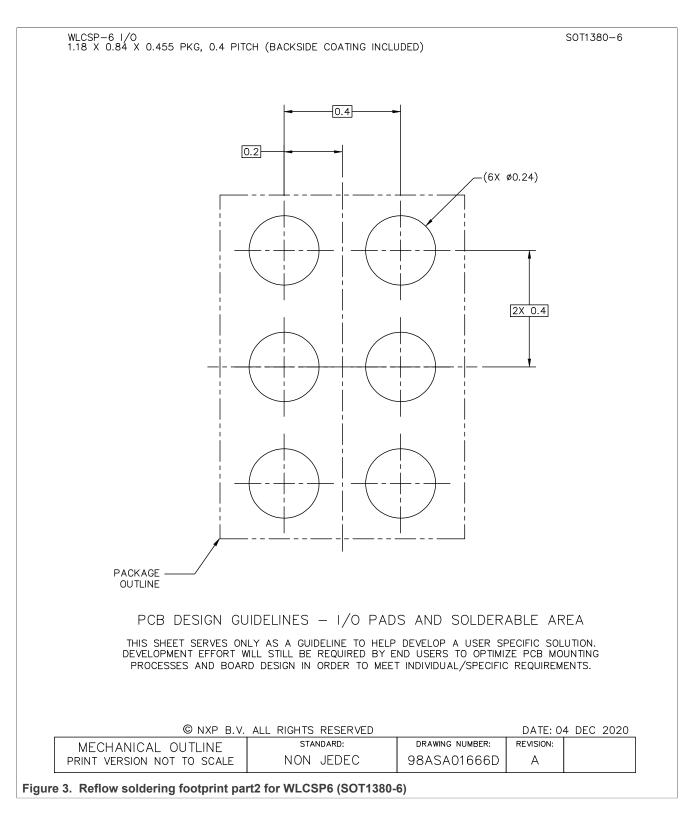
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3 Soldering

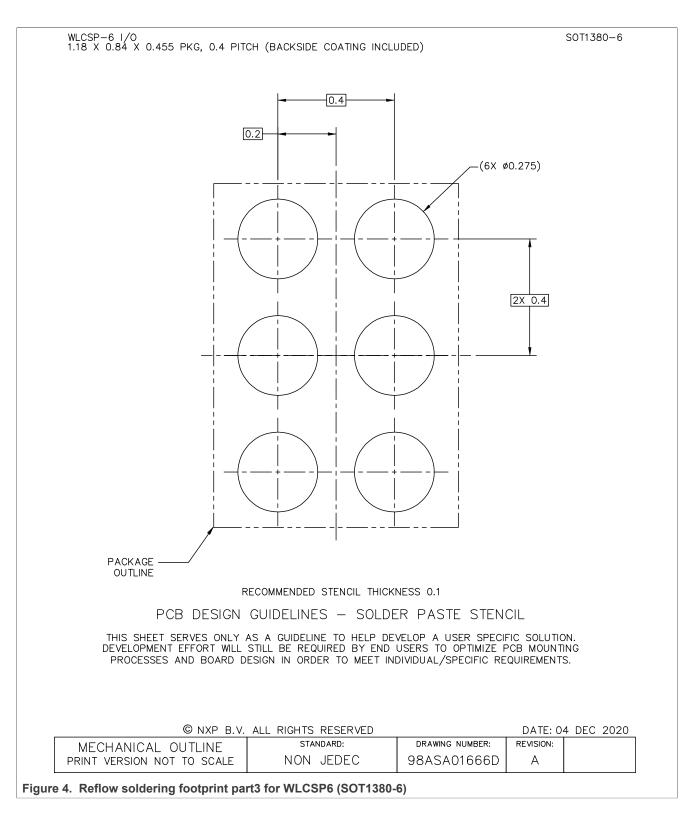


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WLCSP-6 I/O 1.18 X 0.84 X 0.455 PKG, 0.4 PITCH	(BACKSIDE COATING INC	LUDED)	SOT1380-	·6
NOTES:				
1. ALL DIMENSIONS IN MILLIMETER	25			
2. DIMENSIONING AND TOLERANCI		94		
$\sqrt{3}$ PIN A1 FEATURE SHAPE, SIZE				
A. MAXIMUM SOLDER BALL DIAME				
5 DATUM C, THE SEATING PLANE)F THE SOLDER BALL	S
6. THIS PACKAGE HAS A BACK S				0.
		01 010201		
© NXP B.V A	LL RIGHTS RESERVED		DATE: 04 DEC 20	20
MECHANICAL OUTLINE	STANDARD:	DRAWING NUMBER:	REVISION:	
PRINT VERSION NOT TO SCALE	NON JEDEC	98ASA01666D	A	
5. Package outline note WLCSP6 (SOT1380_6)			

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4 Legal information

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Contents

1	Package summary1
2	Package outline
3	Soldering3
4	Legal information7

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