SOT1364-1



plastic, low profile fine-pitch ball grid array package, 97 terminals, 0.65 mm pitch, 8 mm x 8 mm x 1.3 mm body

une 2020 Package information

1 Package summary

Terminal position code B (bottom)

Package type descriptive code LFBGA97

Package style descriptive code LFBGA (low profile fine-pitch ball grid array)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date23-12-2013Manufacturer package codeSOT1364-1

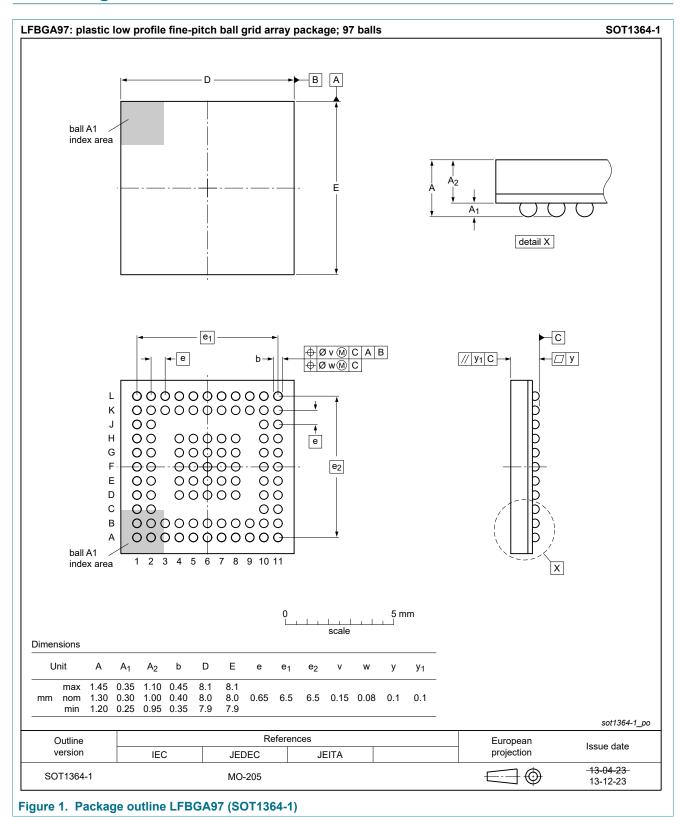
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	7.9	8	8.1	mm
package width	7.9	8	8.1	mm
seated height	1.2	1.3	1.45	mm
nominal pitch	-	0.65	-	mm
actual quantity of termination	-	97	-	



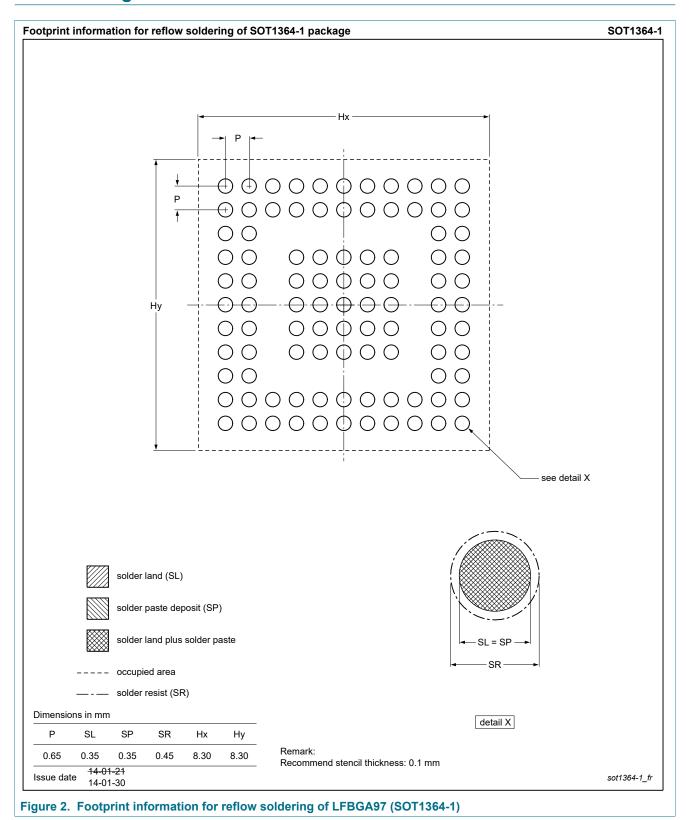
plastic, low profile fine-pitch ball grid array package, 97 terminals, 0.65 mm pitch, 8 mm x 8 mm x 1.3 mm body

2 Package outline



plastic, low profile fine-pitch ball grid array package, 97 terminals, 0.65 mm pitch, 8 mm x 8 mm x 1.3 mm body

3 Soldering



plastic, low profile fine-pitch ball grid array package, 97 terminals, 0.65 mm pitch, 8 mm x 8 mm x 1.3 mm body

4 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

plastic, low profile fine-pitch ball grid array package, 97 terminals, 0.65 mm pitch, 8 mm x 8 mm x 1.3 mm body

Contents

1	Package summary	1
2	Package outline	
3	Soldering	3
4	Legal information	