wafer level chip-scale package; 42 bumps; 3.56 x 3.41 x 0.57 mm (backside coating included)

8 February 2016

Package information

# 1. Package summary

Terminal position code B (bottom)

Package type descriptive code WLCSP

Package type industry code WLCSP42

Package style descriptive codeUC (uncased chip)Package style suffix codeNA (not applicable)

Package body material type P (plastic)

Mounting method type S (surface mount)

**Issue date** 19-1-2015

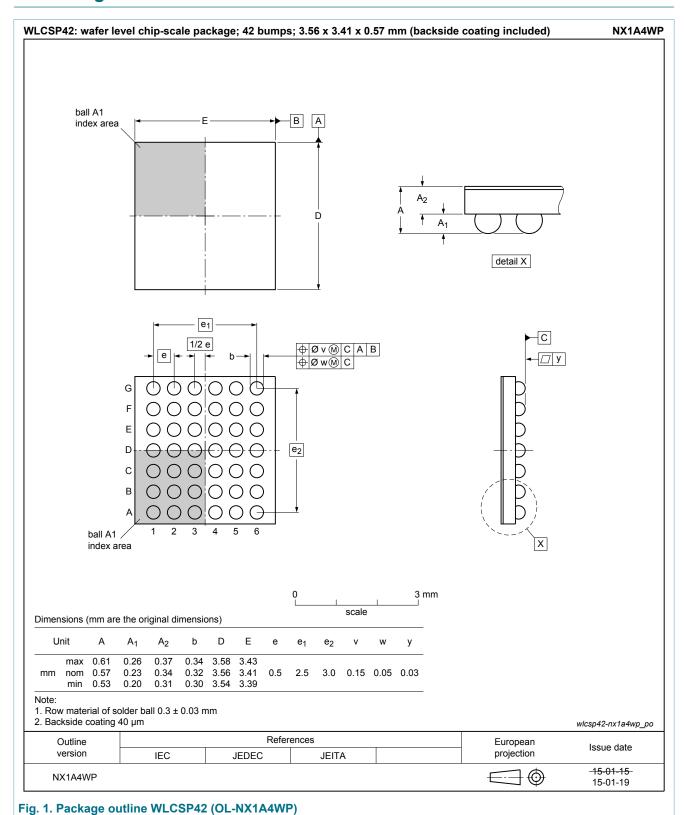
#### **Table 1. Package summary**

Symbol	Parameter	Min	Тур	Nom	Max	Unit
D	package length	3.54	-	3.56	3.58	mm
E	package width	3.39	-	3.41	3.43	mm
Α	seated height	0.53	-	0.57	0.61	mm
е	nominal pitch	-	-	0.5	-	mm
n <sub>2</sub>	actual quantity of termination	-	-	42	-	



wafer level chip-scale package; 42 bumps; 3.56 x 3.41 x 0.57 mm (backside coating included)

## 2. Package outline



NXP Semiconductors OL-NX1A4WP

wafer level chip-scale package; 42 bumps; 3.56 x 3.41 x 0.57 mm (backside coating included)

### 3. Legal information

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wafer level chip-scale package; 42 bumps; 3.56 x 3.41 x 0.57 mm (backside coating included)

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Date	e of release: 8 February 2016	