SOT1676-1



VQFN16, plastic, very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 1 mm body
18 August 2018 Package in

Package information

Package summary 1

Terminal position code Q (quad) VQFN16 Package type descriptive code

Package style descriptive code VQFN (very thin quad flatpack; no leads)

Package body material type P (plastic)

Mounting method type S (surface mount)

Issue date 05-01-2016

98ASA00063D Manufacturer package code

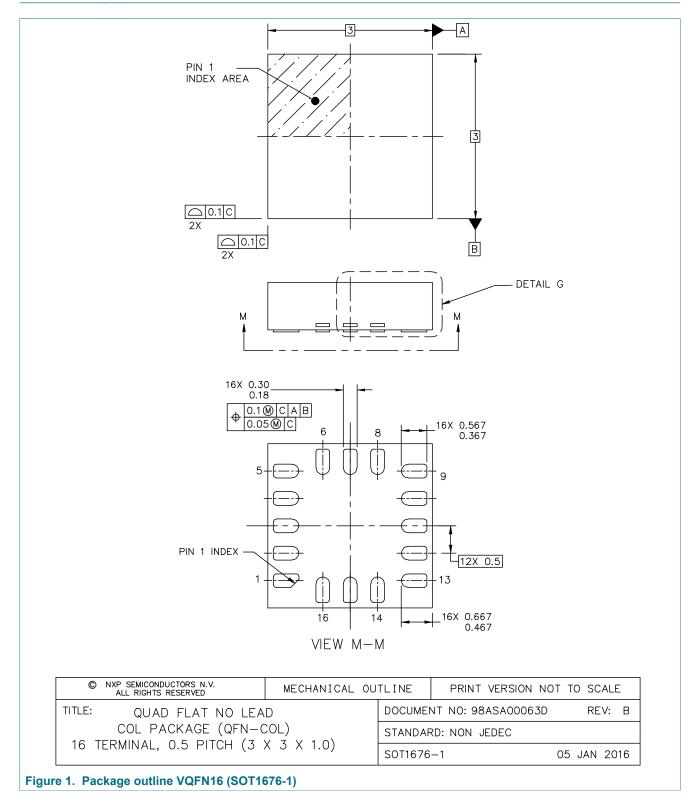
Table 1. Package summary

Parameter	Min	Nom	Max	Unit
package length	-	3	-	mm
package width	-	3	-	mm
package height	-	1	-	mm
nominal pitch	-	0.5	-	mm
actual quantity of termination	-	16	-	



VQFN16, plastic, very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 1 mm body

2 Package outline



VQFN16, plastic, very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 1 mm body

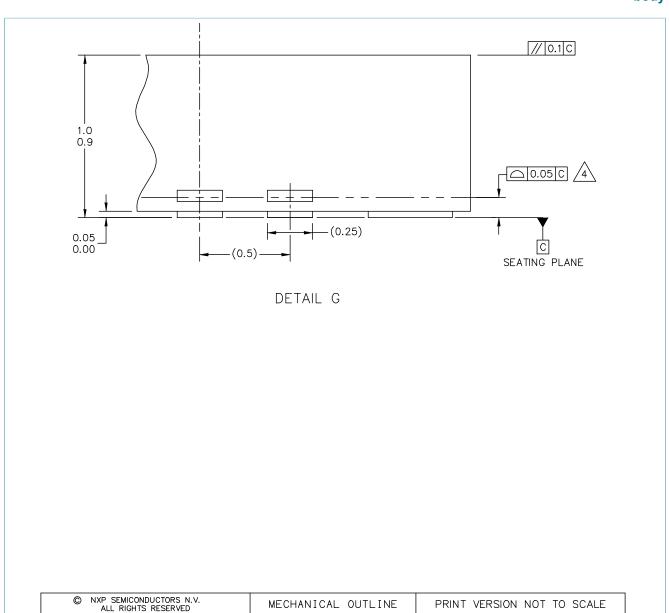


Figure 2. Package outline detail of VQFN16 (SOT1676-1)

QUAD FLAT NO LEAD COL PACKAGE (QFN-COL)

16 TERMINAL, 0.5 PITCH (3 X 3 X 1.0)

DOCUMENT NO: 98ASA00063D

STANDARD: NON JEDEC

SOT1676-1

REV: B

05 JAN 2016

TITLE:

VQFN16, plastic, very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 1 mm

NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-1994.
- 3. THIS IS NON JEDEC REGISTERED PACKAGE.



COPLANARITY APPLIES TO ALL LEADS.

5. MIN. METAL GAP SHOULD BE 0.2MM.

NXP SEMICONDUCTORS N.V. ALL RIGHTS RESERVED	MECHANICAL OU	TLINE	PRINT VERSION NOT	TO SCALE
TITLE: QUAD FLAT NO LEAD COL PACKAGE (QFN-COL) 16 TERMINAL, 0.5 PITCH (3 X 3 X 1.0)		DOCUME	NT NO: 98ASA00063D	REV: B
		STANDARD: NON JEDEC		
16 TERMINAL, U.S PITCH (3)	X 3 X 1.0)	SOT1676	-1 0	5 JAN 2016

Figure 3. Package outline note VQFN16 (SOT1676-1)

VQFN16, plastic, very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 1 mm

3 Legal information

Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including -without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

VQFN16, plastic, very thin quad flatpack; no leads; 16 terminals; 0.5 mm pitch; 3 mm x 3 mm x 1 mm body

Contents

1	Package summary1
2	Package outline2
3	Legal information5