ロジック IC

データスイッチ/アナログスイッチ
コンパレータ
レベルシフタ
汎用ロジック

Index

Configurable Logic（機能設定ロジック）

- PCBのレイアウトを変更することで、1デバイスで9ファンクションを設定可能
- 74LVC(AUP)1G(T)99, 74LVC(AUP)1G(T)98, 74LVC(AUP)1G(T)97, 74LVC(AUP)1G(T)58, 74LVC(AUP)1G(T)57

コンパレータ

- NCX22xx（オープンドレイン、プッシュプル出力）
- Rail to rail
- 低消費電流 ：<5uA
- Vcc= 1.3 ー 5.5V

レベルシフタ

- 1-32bit 単方向 / 双方向 レベルシフタ
- 自動方向制御 双方向レベルシフタNTB010x / NTS010x
- 1, 2, 4, 8 bit

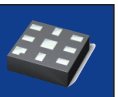
Bus Switches

- CBT, CBTLV ファミリ
- コンピュータ、テレコム、（活線挿抜アプリケーション）
- 高速スイッチ<1nS; R_{ON} < 5 Ω

General Purpose Sw

- HC, LVC, AHC, HEF ファミリ
- マルチプレクサ（mux/demux）、スイッチ
- *Product highlight: HEF4051 1-of-8 mux switch*

Audio Switches

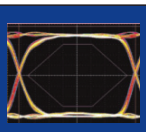


- R_{ON} < 1Ω; 低電流損失
- 7.5 kV ESD 耐性.
- *Product highlight: NX3L1T3157 Analog SPDT*

Video Switches

- 低挿入損失: 0.6dB
- R_{ON} <4Ω; Typical C_{ON} = 10pF
- 高周波数域 = 600MHz

Data Switches



- 信号絶縁：-90 dB
- 低 R_{ON} / C_{ON}
- 高周波数域 = 1.0GHz
- *Product highlight: NX3DV221 USB*

Load Switches

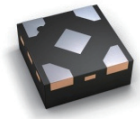
- 3.6V (max) , 500mA
- 漏れ電流：0.2uA（遮断時）
- WLCSP パッケージ
- *Product highlight: NX3P190/191*

「世界最小」と「0.5mmピッチ」を同時に実現した「ダイヤモンド」パッケージ

モバイル機器設計の重要課題を解消する新たな「ダイヤモンド」パッケージ

サイズを小型化する一方で、SOT1226でははんだ付けを容易にするために、これまでの製品に比べて50%広い0.5mmピッチが採用されており、設計に要する時間やコストが削減できます。デバイスの小型化を可能にする独自のダイヤモンドパッケージは、プリント基板（PCB）の面積が極めて限られたスマートフォンなどの最先端の携帯端末に最適であり、パッドピッチが小さな場合に必要な追加工程が不要なため、コストのかからない省スペースのロジック・ソリューションが実現します。

主な特徴

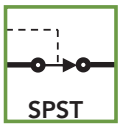


- 5 リードパッケージ
- サイズ0.8 x 0.8 x 0.35 mm
- ピッチ0.5 mm
- 低電圧CMOS（LVC）とCMOS Advanced Ultra-low Power（AUP）CMOSの汎用ロジックファミリのすべての機能を世界最小サイズのパッケージで提供

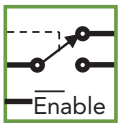
低抵抗アナログスイッチ／データスイッチ

スイッチ構成

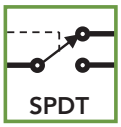
NX3L1G**66**
NX3L1T**66**



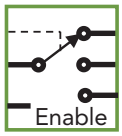
NX3L1G**384**
NX3L1T**384**



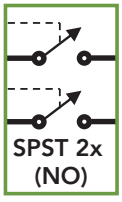
NX3L1G**53**
NX3L1T**53**



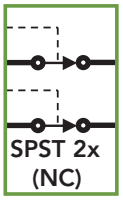
NX3L1G**3157**
NX3L1T**3157**



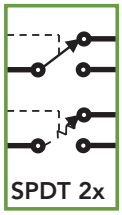
NX3L**4357**
NX3T**4357**



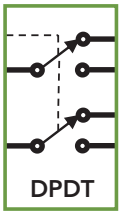
NX3L2G**66**
NX3L2T**66**



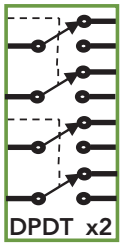
NX3L2G**384**
NX3L2T**384**



NX3L**4684**
NX3L**2267**



NX3DV**42**
NX3DV**221**
NX3DV**3899**



NX3L**2467**

NX3 ファミリ － 低 R_{ON} アナログスイッチ

特長

- 信号の減衰を最小にする低抵抗 (R_{ON} < 1.0 Ω)
- -90dBの高絶縁&高クロストーク
- 低電力消費
- 低 R_{ON} / C_{ON} 組み合わせ:
  - ポータブル・アプリケーションでデジタル・データ・スイッチングに最適
  - オーディオアップリケーションでアナログ・信号スイッチングに最適
  - アナログ信号、デジタル信号のマルチプレクサ・インターフェイス
- 7.5 kV ESD (HBM)
- “トランスレータ ／ レベルシフタ” 機能集積 (“T” モデルのみ)
  - 低電圧ASICとのインターフェイスが容易
  - 周辺部品とコストの削減
  - 例 通常品(CMOS スイッチングレベル): NX3L1G66
    トランスレータ(TTL スイッチレベル): NX3L1T66
- 小型パッケージ; TSSOPはもちろん、0.35mmピッチ・パッケージPicoGate 、MicroPakもリリース
- 汎用アナログスイッチ、マルチプレクサも多数取り扱いしております。

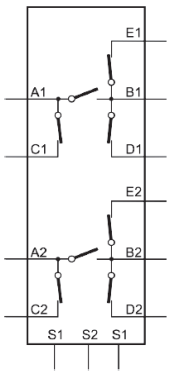
ファミリ	Ron (Ω)	Vcc (V)
NX3V	0.25	4.3
NX3L	0.5	4.3
NX3DV	4	5.5
LVC	6	5.5
AHC	14	5.5
HC	25	10
HEF	80	15

データスイッチ：NX 3(5)DV

データ・スイッチ特性表

スイッチ構成	製品名	RON (Ω)	f -3dB (MHz)	Switch 端子 トレラント機能	X-talk (dB)
1x DPDT	NX3DV42	4	950	yes	-30
	NX3DV221	4	1000	yes	-40
2x DPDT or 4PDT	NX3DV2567	6	330	-	-60
	NX3DV3899	4.5	200	-	-90
		9	500	-	-40
3x DPDT	NX3DV642	7.5	950	-	-55
4x SPDT	NX5DV330	5	300	yes	-63
2x bus exchange (5 ポート/sw)	NX3DV1066	4	720	yes	-30
2x DPDT + 1x 3PDT 2電源 1-of-2 VGA switch	NX5DV4885E	Analog switch : 4 Data switch : 9	850	yes	-50
	NX5DV713	Analog switch : 4 Data switch : 9	600	yes	-50
	NX5DV713E	Analog switch : 4 Data switch : 9	600	yes	-50
	NX5DV715	Analog switch : 4 Data switch : 9	600	yes	-50
DPDT + SPDT + power MHL / USB switch	NX3DV3200	USB : 3.9	720	yes	-40
		MHL : 5	1300	yes	-40

NX3DV1066 スイッチ構成



NX3DV1066 スイッチポジション

S3	S2	S1	チャネル
L	L	L	all channels off
L	L	H	Bn = En
L	H	L	Bn = Dn
L	H	H	An = Bn
H	L	L	An = Cn; Bn = En
H	L	H	all channels off
H	H	L	An = Cn; Bn = Dn
H	H	H	all channels off

H = HIGH voltage level; L = LOW voltage level.

低抵抗アナログスイッチ NX3L(V)

アナログスイッチ特性表

スイッチ構成	製品名	Ron (Ω)	f -3dB (MHz)	THD (%)	X-talk (dB)
1x SPST	NX3L1G/T66	0.75	60	0.024	-90
	NX3L1G/T384	0.75	60	0.024	-90
	NX3V1G/T66	0.45	25	0.01	-90
	NX3V1G/T384	0.45	25	0.01	-90
2x SPST	NX3L2G/T66/384	0.75	60	0.024	-90
	NX3V2G/T66/384	0.45	25	0.01	-90
1x SPDT	NX3L1G3157	0.75	60	0.024	-90
	NX3L1T3157	0.75	60	0.024	-90
	NX3L1G53	0.75	60	0.024	-90
	NX3L1T53	0.75	60	0.024	-90
2x SPDT	NX3L4684	0.8	20	0.01	-90
		0.5	15	0.01	-90
	NX3L2267 NX3L2267S *	0.75	60	0.024	-90
2x DPDT or 4PDT	NX3L2467	0.75	60	0.02	-90
1x SPTT	NX3L4357	0.75	30	0.02	-90
1x SP8T	NX3L4051	0.75	15	0.02	-90
3x SPDT	NX3L4053	0.8	60	0.02	-90

* シャント抵抗付き

ロードスイッチ NX3(5)P

ロードスイッチ特性表

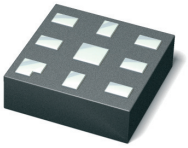
Part	入力電圧 (V)	Ron (mΩ)	放電回路	Io (Amax)
NX3P190	1.1 to 3.6	95	No	0.5
NX3P191	1.1 to 3.6	95	Yes	0.5
NX5P198	1.8 to 5.5	12	No	3.0
NX3P1108	0.9 to 3.6	32	Yes	1.2
NX5P2553	1.8 to 5.5	12	No	3.0

コンパレータ

コンパレータ特性表

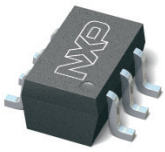
Number	Vcc min (V)	Vcc max (V)	IO typ (mA)	Icc typ (mA)	Prop. delay (us)	Vio max (mV)	Ta min (℃)	Ta max (℃)	Output	Package	
NCX2220 (2回路)	1.3	5.5	68	0.005	0.8	1	-40	125	push pull	SOT972(GU) 1.35 x 1.5 mm 0.4 p	SOT902(GM) 1.6 x 1.6 mm 0.5 p
NCX2222 (2回路)	1.3	5.5	68	0.005	0.8	1	-40	125	open drain		
NCX2200 (1回路)	1.3	5.5	68	0.006	0.8	6	-40	125	push pull	SOT886(GM) 1 x 1.45 mm 0.5 p	SOT353(GW) 2 x 2 mm 0.65 p
NCX2202 (1回路)	1.3	5.5	68	0.006	0.8	6	-40	125	open drain		

GM

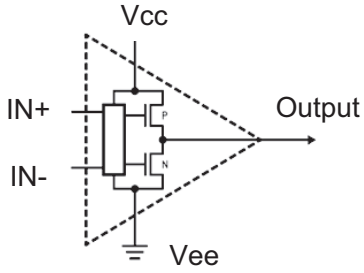


GT

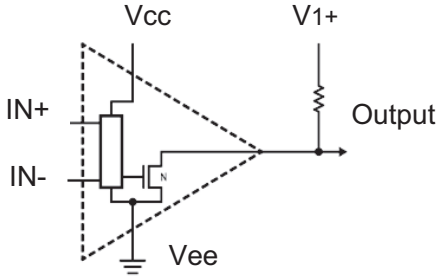
GW



コンパレータ構成図及び入出力関係



Push pull 構成

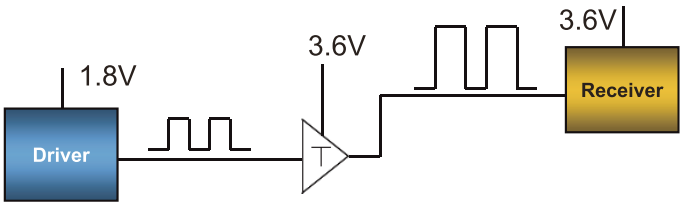


Open drain 構成

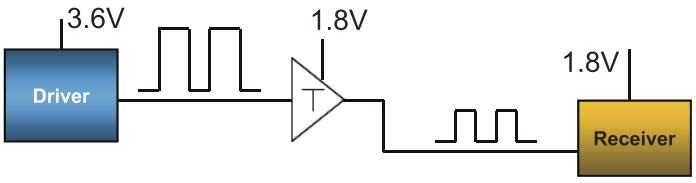
入力電圧	出力状態 (Push pull)	出力状態 (Open drain)
IN+ > IN-	OUT = Vcc	内部FET オープン
IN+ ≤ IN-	OUT = Vee	内部FET クローズ (OUT = Vee)

電圧レベルシフタ（トランスレータ）

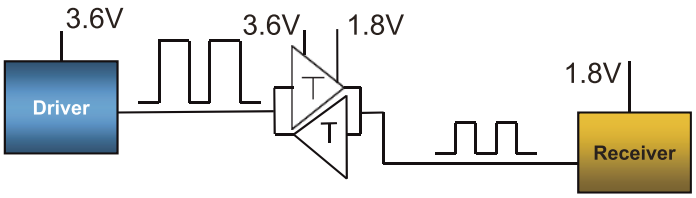
単方向（高⇒低電圧）、（低⇒高電圧）、双方向（高⇔低電圧）



低電圧→ 高電圧：単方向電圧レベルシフタ
TTL スイッチングレベル集積ファミリ“HCT, AHCT, VHCT, AUPT”
ex
74HCT2G / 3G34　：プッシュプル・バッファ
74AHCT1G07　：オープン・ドレイン・バッファ



高電圧→ 低電圧：単方向電圧レベルシフタ
入力トレラント機能集積ファミリ“AHC, LVC, ALVC, AVC, AUP”
ex
74LVC 1G / 2G / 3G34：プッシュプル・バッファ
74LVC 1G / 2G / 3G07：オープン・ドレイン・バッファ



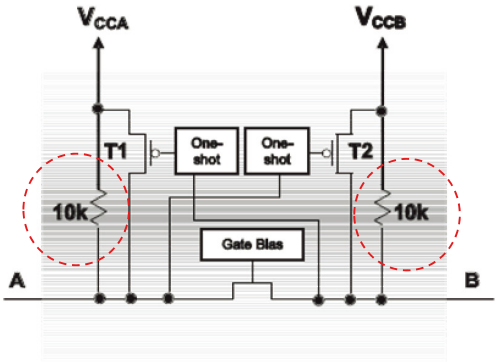
双方向トランスレータ
機能：“45”, “245”
Ex
74AUP16245　：変換電圧0.8V～3.6V
74AVC245　：変換電圧0.8V～3.6V
74LVC16245　：変換電圧1.6V～5.5V

NTB010X / NTS010X – 自動方向制御双方向レベルシフタ

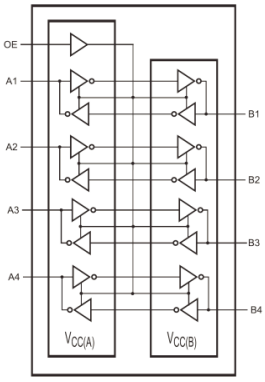
自動方向制御2電源双方向レベルシフタ。データ入出力ポート、enableピン、2電源で構成されています。1.2V, 1.5 V, 1.8 V, 2.5 V, 3.3 V, 5.0 V間の電圧のすべての電圧組み合わせで電圧変換に使用可能です。

デバイス・タイプ

NTB xxxx　：プッシュ・プル出力
NTS xxxx　：オープン・ドレイン出力（USB interface）、10kΩのプル・アップ抵抗集積



NTS（Open drain）I/O 構造
10kΩpull-pu抵抗集積



NTB0104（push pull）
symbol

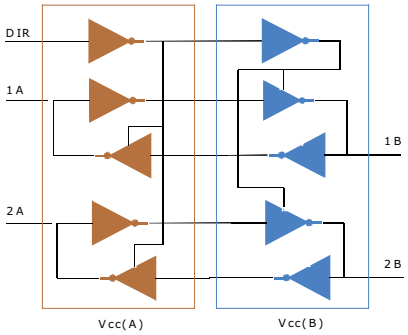
特長

- 電源電圧
  VccA = 1.2 - 3.6 V
  VccB = 1.65 - 5.5 V
- 入力電圧：最大5.5 V
- パーシャル・パワーダウン
  消費電流を低減
- Off状態で高インピーダンス（3state）
- 自動方向制御
- I/Oにパアワーダウン・プロテクション有り

2電源電圧双方向レベルシフタ

特長

- 豊富なポートフォリオ
- 広範囲な電源レンジ (Vcc=0.8V～5.5V)
- 伝送方向切り替え入力 (DIR ピンで制御)
- ハイスピード (500Mbps (max))
- 低消費 (low power / VCC= 1.8 V, C_{PD}= 2 $pF_{(Typical)}$)
- 活線挿抜 (Live insertion)
- 3ステート出力
- バス・ホールド機能



74LVC2T45 ブロックダイアグラム

2電源レベルシフタ　ポートフォリオ

Bit	製品名	VCC(A) (V)	VCC(B) (V)	tpd (ns)	Rate (Mbps)	方向制御
1	74AUP1T45	1.1 - 3.6	1.1 - 3.6	4.3	250	DIR ピン
	74AVC(H)1T45	0.8 - 3.6	0.8 - 3.6	2.1	500	DIR ピン
	74LVC(H)1T45	1.2 - 5.5	1.2 - 5.5	2.5	420	DIR ピン
	74AUP1T34	1.1 - 3.6	1.1 - 3.6	4.3	-	DIR ピン
	NTB0101	1.2 - 3.6	1.65 - 5.5	4.2	80	自動
	NTS0101	1.64 - 3.6	2.3 - 5.5	4.8	50	自動
2	74LVC(H)2T45	1.2 - 5.5	1.2 - 5.5	2.5	420	DIR ピン
	74AVC(H)2T45	0.8 - 3.6	0.8 - 3.6	2.1	500	DIR ピン
	NTB0102	1.2 - 3.6	1.65 - 5.5	4.2	80	自動
	NTS0102	1.64 - 3.6	2.3 - 5.5	4.8	50	自動
	NTSX2102	1.64 - 3.6	2.3 - 5.5	3.0	18	自動
3	NTS0103	1.2 - 3.6	1.65 - 5.5	4.8	50	自動
4	74AVC(H)4T45	0.8 - 3.6	0.8 - 3.6	2.1	500	DIR ピン
	NTB0104	1.2 - 3.6	1.65 - 5.5	4.2	80	自動
	NTBA0104	1.2 - 3.6	1.65 - 5.5	4.2	80	自動
	NTS0104	1.64 - 3.6	2.3 - 5.5	4.8	50	自動
8	74AVC(H)8T245	0.8 - 3.6	0.8 - 3.6	2.1	500	DIR ピン
	74LVC(H)8T245	1.2 - 5.5	1.2 - 5.5	2.5	420	DIR ピン
	74LVC4245	1.2 - 5.5	1.2 - 5.5	3.4	300	DIR ピン
16	74AVC(H)16T245	0.8 - 3.6	0.8 - 3.6	2.1	380	DIR ピン
	74ALVC164245	1.5 - 5.5	1.5 - 3.6	2.9	300	DIR ピン
20	74AVC(H)20T245	0.8 - 3.6	0.8 - 3.6	3.5	380	DIR ピン

5V, 低電圧ロジック・ファミリ・ 一般機能、特性表

Typical features and performance for 5-volt products (5Vロジック)						
Family	HEF4000	HC/T	AHC/T	FAST	ABT	CBT (CBTLV)
伝達遅延 tpd	60 ns	9 ns	5 ns	4 ns	3 ns	〈1-ns prop delay
ドライブ電	±3 mA	±8 mA	±8 mA	-15/+24 mA	-32/+64 mA	5Ω RON
スタンバイ電流	600 μA	80 μ	40 μ	90 mA	250 μ	3 μ
Vcc	5-15V	2-6V	2-6V	4.5-5.5V	4.5-5.5V	4.5-5.5V (1.8-3.6V)
Multisourced	Yes	Yes	Yes	Yes	Yes	Yes
PicoGateパッケージ		Yes	Yes			
特長	・Gate, MSI, バッファ	・Gate, MSI, バッファ ・アナログスイッチ	・Gate, バスインターフェイス ・VHC/T相当品 ・過電圧入力トレラント ・パッケージ PicoGate	・Gate, バスインターフェイス option ・終端抵抗	・Gate, バスインターフェイス ・活線挿抜 option ・バス・ホールド ・終端抵抗	・信号とスイッチングを絶縁 ・活線挿抜用プリチャージ回路 ・ショットキー/チャージポンプ・アンダーシュート・プロテクション ・レベルシフタ用ダイオード集積

Typical features and performance for low-voltage products (低電圧ロジック)							
Family	LV	LVC	ALVC	LVT	ALVT	AVC	AUP
伝達遅延 tpd	9 ns	4 ns	2 ns	2 ns	1.5 ns	1.3 ns	2.5 ns
ドライブ電流	±8 m	±24 m	±24 m	-32/+64 m	-32/+64 m	±8-m	2-mA
スタンバイ電流	20 μA	20 μA	40 μA	120-190 μA	90 μA	20 μA	0.9 μA
Vcc	1.0-3.6 V	1.2-3.6 V	1.2-3.6 V	2.7-3.6 V	2.3-3.6 V	1.2-3.3 V	0.8-3.6 V
Multisourced	Yes	Yes	Yes	Yes	Yes	Yes	Yes
I/O電圧トレラント		5	5	(inputs) 5	5	3.6	3.6
Bus hold		Optional	Optional	Built-in	Built-in	Optional	
終端抵抗 Termination resistor		Optional	Optional	Optional	Optional	Optional	
活線挿抜 Live insertion				Yes	Yes		
DQFN, BGA パッケージ		Yes	Yes	Yes			
特長	・Gate, MSI, バッファ	・Gate, MSI, 8/16/32-bit バスインターフェイス ・過電圧入力トレラント ・パッケージ PicoGate MicroPak ・LCX相当品	・Gate, MSI, 8/16/32-bit バスインターフェイス ・VCX相当品	・Gate, MSI, 8/16/32-bit バスインターフェイス ・パッケージ PicoGate	・バスインターフェイス	・2.5V電源 ・バス・インターフェイス	・1.8V電源 ・パッケージ PicoGate microPak

AHC/VHC ファミリ 特性比較表

パラメータ	NXP's VHC/AHC	Toshiba's VHC
V_{CC} Supply Voltage	2V to 5.5V	2V to 5.5V
I_{CC} (25℃)	2μA (Max)	2μA (Max)
V_{IH} (3V to 5.5V)	0.7 V_{CC}	0.7 V_{CC}
V_{IL} (3V to 5.5V)	0.3 V_{CC}	0.3 V_{CC}
V_{OH} (I_{OH} = -8mA and V_{CC} = 4.5V)	3.94V	3.94V
V_{OL} (I_{OH} = -8mA and V_{CC} = 4.5V)	0.36V	0.36V
I_{IN} (25℃)	0.1μA	0.1μA
C_{PD}	7pF (低消費)	15pF
T_{PD} (C_L = 50pF, 25℃, V_{CC} = 3.3V to 5V)	8ns to 12ns (Typ)	8ns to 12ns (Typ)
I_K Input Clamping Diode Current	-20mA	-20mA
Input Rise Time (V_{CC} = 3.3V)	0 - 100ns	0 - 100ns
Input Rise Time (V_{CC} = 5V)	0 - 20ns	0 - 20ns
ESD (HBM)	≥2000V (per JESD22-A114E 規格準拠)	Not Available
RoHS Compliant	Yes	Yes
Dark Green (RoHS + Halogen- & Antimony-Free)	Yes	Yes
Operating Temperature Range	-40℃ to +125℃ (広温度範囲)	-40℃ to + 85℃
IBIS Models Available	Yes	Yes

LMOS 互換表

Logic	Package	Pitch mm	Pins	SOT	NXP	Toshiba	ON Semi
CMOS	PicoGate	0.95	5	SOT753	XC7SHxxxGV	TC7SHxxxF	MC7VHC1GxxxDTT
CMOS	PicoGate	0.65	5	SOT353-1	XC7SHxxxGW	TC7SHxxxFU	MC7VHC1GxxxDFT
CMOS	TSSOP	0.65	8	SOT505-2	XC7WHxxxDP	TC7WHxxxFU	
TTL	PicoGate	0.95	5	SOT753	XC7SETxxxGV	TC7SETxxxF	MC7VHC1GTxxxDTT
TTL	PicoGate	0.65	5	SOT353-1	XC7SETxxxGW	TC7SETxxxFU	MC7VHC1GTxxxDFT

機能設定ロジック (Configurable Logic)

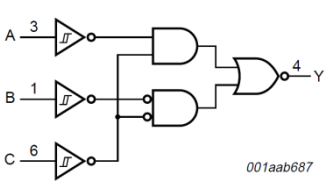
概要

1パッケージに最大6ロジック・ゲートを集積しているため、PCBの配線を替えることで希望のロジック・ファンクションを設定することができます。
1パーツあたり最大9個の機能 (ファンクション) 設定可能

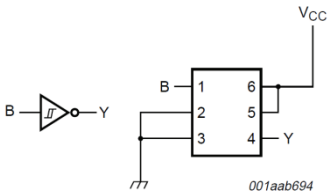
☆1 デバイス = 9 パーツ設定可能、 ☆小型パッケージ、 ☆コスト削減、
☆在庫管理の簡略化

パッケージ

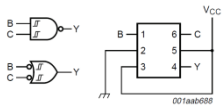
DP (SOT505), GW (SOT353/363), GM (SOT1049), GT (SOT833), GF (SOT891)



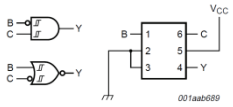
"58"Logic diagram



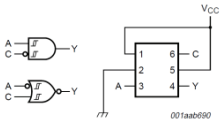
Inverter



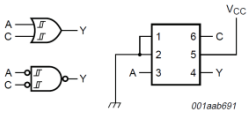
2-input NAND gate or 2-input OR with both inputs inverted



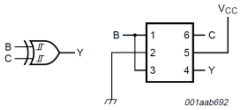
2-input AND gate with inverted B input or 2-input NOR gate with inverted C input



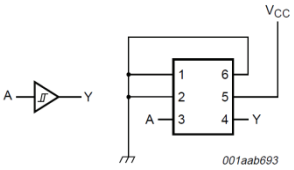
2-input AND gate with inverted C input or 2-input NOR gate with inverted A input



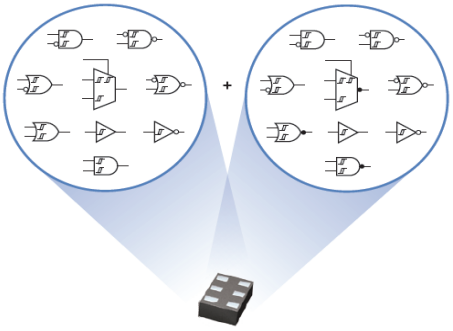
2-input OR gate or 2-input NAND gate with both inputs inverted



2-input XOR gate



Buffer



Device	AND 08	OR 32	NAND 03	NOR 02	XOR 4077	XNOR 7266	BUF. 34	INV. 04	MUX 157
74AUP1G57	○			○		○	○	○	
74LVC1G58		○	○		○		○	○	
74AUP1G97	○	○					○	○	○
74LVC1G98			○	○			○	○	○
74LVC1G99	○		○		○	○	○	○	○

NXP パッケージ

ピン・ピッチ	5 pins	6 pins	8 pins	10 pins	12 pins	16 pins	20 pins
0.95 mm pitch	 GV(SOT753) 2.8×2.9×1.0	 GV(SOT457) 2.8×2.9×1.0					
0.65 mm pitch	 GW(SOT353) 2.0×2.0×1.0	 GW(SOT363) 2.0×2.0×1.0	 DP (SOT505 / TSSOP) 3.0×4.0×1.1			 PW (SOT402) 4.4 × 5.0 × 1.1	 PW - SOT360 4.4 × 6.5 × 1.1
0.5 mm pitch	 GX - SOT1226 0.8 × 0.8 × 0.35	 GM - SOT886 1.0×1.5×0.5	 DC(SOT765 /VSSOP 2.0×3.0×1.0  GD(SOT996) 2.0×3.0×0.5  GM (SOT902) 1.6×1.6×0.5  GT(SOT833) 1.0×1.95×0.5	 TK(SOT650) 3.0×3.0×0.85  GM (SOT1049) 2.0×1.55×0.5		 HR (SOT1039) 3.0×3.0×0.5  BQ (SOT763) 2.5 × 3.5 × 0.85	 BX(SOT1045) 2.5×4.5×0.5  BQ (SOT764) 2.5 × 4.5 × 0.85
0.4 mm pitch			 GU8 (SOT1309) 1.2 × 1.4 × 0.5	 GU(SOT1160) 1.4×1.8×0.5	 GU(SOT1174) 1.7×2.0×0.5	 GU(SOT1161) 1.8×2.6×0.5	
0.35 mm pitch		 GF(SOT891) 1.0×1.0×0.5  GS(SOT1202) 1.0×1.0×0.35	 GF(SOT1089) 1.0×1.35×0.5  GS(SOT1203) 1.0×1.35×0.35	 GF(SOT1081) 1.7×1.0×0.4			
0.3 mm pitch		 GN(SOT1115) 1.0×0.9×0.35	 GN(SOT1116) 1.0×1.2×0.35				

縦×横×高さ[mm]

パッケージ・クロス・リファレンス

Package	Pins	NXP	Toshiba	TI	Fairchild	ON
SO	14, 16	D / T	FW FN	D / DW	M SC	D
SO	20, 24, 28	D	FW	DW	WM SC	DW
SSOP	14, 16, 20, 24	DB	FS	DB	MSA	SD
SSOP	48, 56	DL	-	DL	MEA	-
TSSOP	8	DP	FU	DCT	-	-
TSSOP	14, 16, 20, 24	PW	FT	PW	MTC	DT
TSSOP	48, 56	DGG	FT	DGG	MTD	DT
VFBGA	48, 56	EV	-	GQL	-	-
DQFN	14, 16, 20, 24	BQ	-	-	BQ	-
SOT353	5	GW	FU	DCK	P5	DFT
SOT753	5	GV	F	DBV	M5	DTT
SOT363	6	GW	FU	DCK	P6	DFT
SOT457	6	GV	F	DBV	-	DTT
SOT505-2	8	DP	FU	DCT	-	-
SOT765-1	8	DC	FK	DCv	K8	US
MicroPak	6, 8	GM	-	RSE RSE	L6 L8	-
DIP		P	BP	E	CN	CP

プロダクトポートフォリオ マトリクス

Families	Functions																Special features						Process		
	Buffers/line drivers	Flip-flops	Counters	Shift registers	Encoders/multiplexers	Decoders/demultiplexers	Comparators/parity generators	Arithmetic	Gates	Schmitt triggers	Analog switches	Transceivers	FIFOs	Level shifters/translation	Phase lock loops	Bus switches	Bus hold	Series damping resistors	Live insertion	Overvoltage-tolerant I/Os	Power-off output disable	Power-up reset	Bipolar	CMOS	BiCMOS
1.8-volt logic																									
AUP	•	•							•	•		•		•						•	•			•	
2.5-volt logic																									
AVC	•	•										•					•	•		• [1]	•			•	
3.3-volt logic																									
LV	•	•	•	•	•	•			•	•	•	•												•	
LVC	•	•	•		•	•			•	•	•	•		•			•	•		•	•			•	•
ALVC	•	•							•	•		•		•			•	•		• [2,3]				•	
LVT	•	•							•			•					•	•	•	• [1]	•	•			•
ALVT	•	•										•					•	•	•	• [3]	•	•			•
5-volt logic																									
HEF4000	•	•	•	•	•	•	•	•	•	•	•	•		•	•									•	
HC/HCT	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•									•	
AHC/AHCT	•	•		•	•	•			•	•	•	•								• [3]				•	
FAST	•	•	•	•	•	•	•	•	•	•	•	•						•							
ABT	•	•					•		•			•					•	•	•		•	•			•
MULTIBYTE	•	•										•							•		•	•			•
CBT					•	•								•		•				• [4]	•			•	•

Notes: 1 - To 3.6 V only 2 - Inputs only 3 - To 4.6 V max 4 - Control inputs only

機能の説明

▶ Bus hold；フローティング入力時に、入力レベルを確定し出力の不定ステータスを防止します。

▶ Series damping resistor；ダンピング抵抗を内蔵した製品。オーバー・アンダーシュートを低減します。

▶ Live insertion；特別な手順を踏むことなく通電中のシステムに挿抜を可能にする機能です。

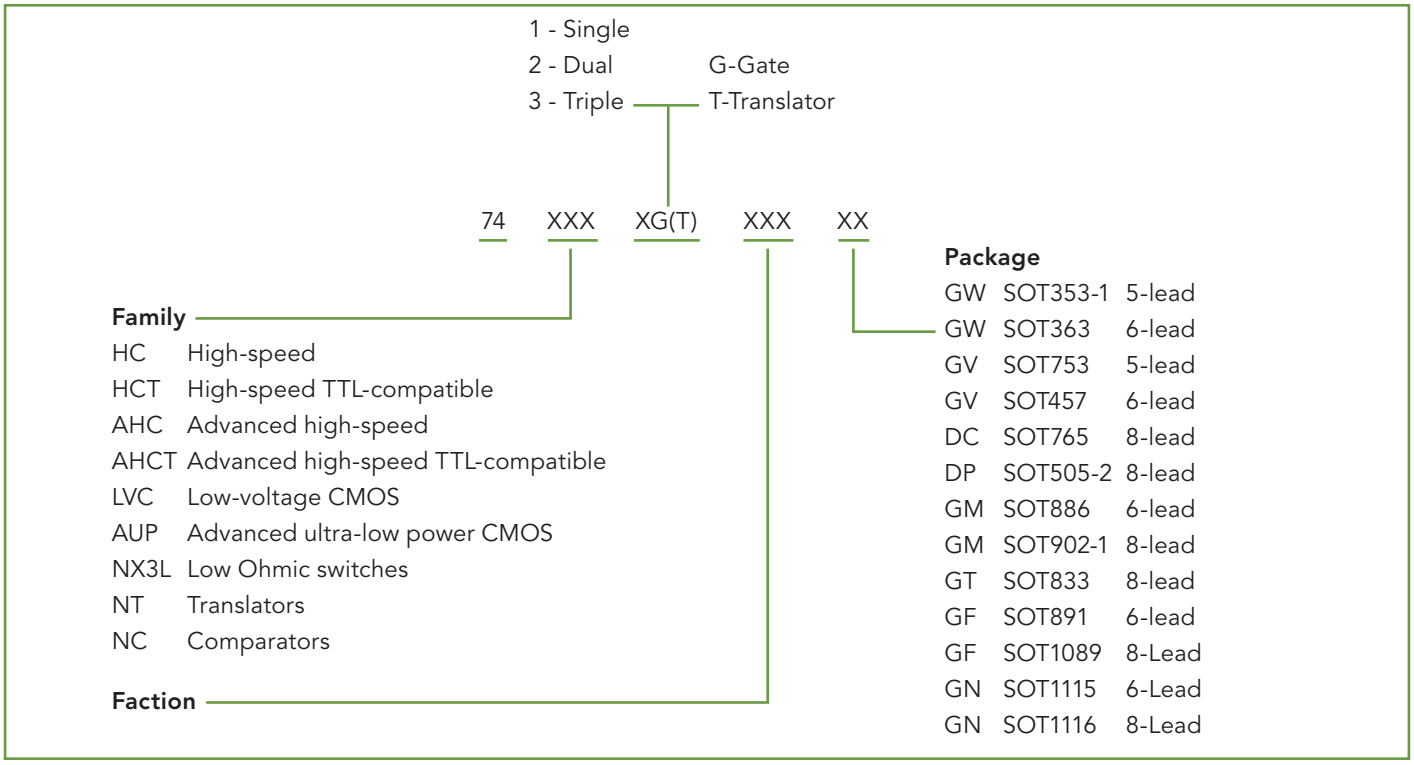
▶ Overvoltage-tolerant；入力端子または出力端子に電源電圧以上の電圧の印加を可能にします。

▶ Power-off disable；電源電圧が0Vのときに入力端子および出力端子のHi-zを保証します。

▶ Power-up reset；電源投入時にリセットを掛ける機能です。

汎用ロジック

汎用ロジックの型番は下記のようにファミリ (family) 、ゲート数 (pigo gateのみ) 、機能 (Function) 、パッケージから製品の型番が構成されています。



動作温度範囲

N	コマーシャル温度範囲 (FAST製品に適用)
I	インダストリ温度範囲 (FAST製品に適用)
P	旧Philips製品 (HC/HCT製品に適用)
74	コマーシャル・インダストリ温度範囲

Note; HEF4000ファミリは除く

TTLレベル入力

T	TTL互換入力製品 (HCT、AHCT製品に適用)

バスホールド

H	バスホールド機能付き製品。

バスビット幅

1G	1回路入り製品
2G	2回路入り製品
3G	3回路入り製品

製品名称のルール

N	74	LVC	T	H	32	2	244	A	-1	D

N	動作温度範囲
74	動作温度範囲
LVC	ファミリ
T	TTL互換入力
H	バスホールド
32	バスビット幅
2	ダンピング抵抗
244	機能
A	バージョン
D	パッケージ

表面実装パッケージ

	Width (mil)	Pitch mm (mil)	JEDEC code	EIAJ type	5	6	8	14	16	20	24	28	48	52	56	96/114	Suffix	Low-voltage products	5.0-V products
SOIC	150 300	1.27 (50)	ms-012 ms-013	I III													D	LV LVT (A)LVC	F, AHC/T ABT HEF, HC/T
SSOP	212	0.65 (25)	mo-150	II													DB	LV, LVC LVT	F ABT
	299	0.635 (25)	mo-118	III													DL	(A)LVC16 (A)LVT16	HC/T
TSSOP	177	0.65 (25.6)	mo-153	I													PW	LV, LVC LVT, (A)LVC	HC, AHC ABT
	244	0.5 (19.7)	mo-153	II													DGG	(A)LVC16 (A)LVT16	ABT
DQFN	2.5 3.5	0.5 (19.7)	mo-241														BQ	LVC LVT	HC/T AHC/T
PQFP	394 x 394	0.65 (25.6)	mo-108														BB		MB
TVSOP	173	0.40 (15.7)	mo-194														DG-V	ALVC	CBT
LFBGA	217	0.8 (31.5)	mo-205														EC	(A)LVC32 (A)LVT32	
VFBGA	177	0.65 (25.6)	mo-225														EV	(A)LVC16 (A)LVT16	
PicoGate	2.0 2.8 3.1 4.0	0.65 0.95 0.5 0.65	mo-203 mo-187														GW GV DC DP	 LVC AUP	HC/T AHC/T
MicroPak	1.0 1.6 1.0 1.0	0.5 0.5 0.5 0.35	mo-252														GM GM GT GF	LVC, AUP LVC, AUP LVC, AUP LVC, AUP	HC/T AHC/T

Package suffix	GW	GW	DC	GV	GV	DP	GM	GT	GM	GF
	5-pin	6-pin	8-pin	5-pin	6-pin	8-pin	6-pin	8-pin	8-pin	6-pin
Single-gate	1G SOT353	1G SOT363	1G SOT765	1G SOT753	1G SOT457	1G SOT505-2	SOT886	SOT883	SOT902	SOT891
Dual-gate		2G SOT363	2G SOT765		2G SOT457	2G SOT505-2	SOT886	SOT883	SOT902	SOT891
Triple-gate	3 mm		3G SOT765			3G SOT505-2		SOT883	SOT902	
	5-pin	6-pin	8-pin	5-pin	6-pin	8-pin	6-pin	8-pin	8-pin	6-pin
Width (mm)	2.1	2.1	3.1	2.8	2.8	4	1.00	1.00	1.60	1.00
Length (mm)	2.0	2.0	2.0	2.9	2.9	3.0	1.45	1.95	1.60	1.00
Pitch (mm)	0.65	0.65	0.5	0.95	0.95	0.65	0.50	0.50	0.50	0.35

クロスリファレンス；標準ロジック製品

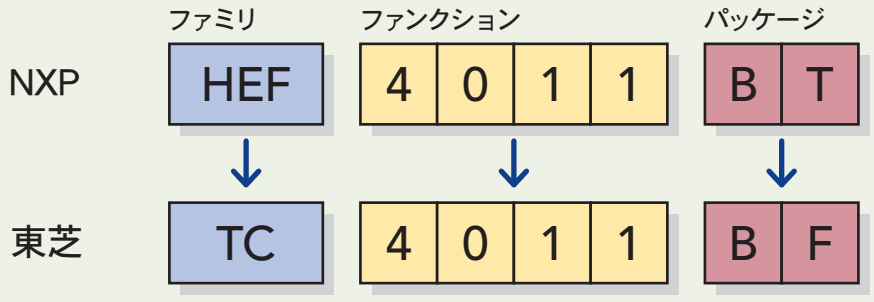
	Package	NXP	Texas Instruments	Fairchild Semiconductor	ON Semiconductor	Toshiba	STMicroelectronics
CMOS							
HEF4000B	DIP	HEF4xxxP	CD4xxxE(E4)	CD4xxxCN	MC14xxxCP	TC4xxxBP	HCF4xxxEY
	SOIC	HEF4xxxT	CD4xxxM(96/96E4/E4)	CD4xxxC(W)M(X)	MC14xxxD	TC4xxxBFN	HCF4xxxM1/M013TR
	SSOP I						
	SSOP II	HEF4xxxTS			MC14xxxCP		
	TSSOP	HEF4xxxTT	CD4xxxPW(E4/R/RE4)	CD4xxxCMTC(X)	MC14xxxDT	TC4xxxBFT	
HC(T) T = TTL	DIP	74HC(T)xxxN	SN74HC(T)xxxN CD74HC(T)xxxE(E4)	MM74HC(T)xxxN	MC74HC(T)xxxN	TC74HC(T)xxxAP	M74HCxxxB1R
	SOIC	74HC(T)xxxD	SN74HC(T)xxxD(W) (E4/G4/R/RE4/RG4/T/TE4) CD/74HC(T)xxxM(96/96E4/E4/T/TE)	MM74HC(T)xxx(W)M(X)	MC74HC(T)xxxxD(W)	TC74HC(T)xxxAFN/W	M74HCxxxM1R/ RM13TR
	SSOP II	74HC(T)xxxDB					
	TSSOP	74HC(T)xxxPW	CD/SN74HC(T)xxxPW(E4/R/RE4/T/TE4)	MM74HC(T)xxxMTC(X)	MC74HC(T)xxxDT	TC74HCxxxAFT	M74HCxxxTTR
	DQFN	74HC(T)xxxBQ		MM74HC(T)xxxBQ			
AHC(T) T = TTL	SOIC	74AHC(T)xxxD	SN74AHC(T)xxxD(W)(E4/R/RE4)	74VHCxxx(W)M(X)	MC74VHC(T)xxxD(W)	TC74VHC(T)xxx(A)FN/W	74VHC(T)xxxMTR
	TSSOP	74AHC(T)xxxPW	SN74AHC(T)xxxPW(E4/LE/R/RE4)	74VHCxxxMTC(X)	MC74VHC(T)xxxDT	TC74VHC(T)xxx(A)FT	74VHC(T)xxxTTR
	DQFN	74AHC(T)xxxBQ		74VHCxxxBQ(X)			
Low-voltage CMOS							
LVC(H) H = bus hold feature	SOIC	74LVC(H)xxxD(W)	SN74LVC(H)xxxD(W)(E4/R/RE4/G4/T/TE4)	74LCXxxx(W)M(X)	MC74LCXxxxD(W)	TC74LCXxxxFN/W	74LVCxxxMTR 74LCXxxxMTR
	SSOP II	74LVC(H)xxxDB	SN74LVC(H)xxxDB(R/RE4)	74LCXxxxMSA(X)			
	TSSOP I	74LVC(H)xxxPW	SN74LVC(H)xxxPW(E4/R/RE4/T/TE4)	74LCXxxxMTC(X)	MC74LCXxxxDT	TC74LCXxxxFT	74LVCxxxTTR 74LCXxxxTTR
	DQFN	74LVC(H)xxxBQ		74LCXxxxBQ			
	SSOP III	74LVC(H)16xxxDL	SN74LVC(H)xxxDL(G4/R/RG4)	74LCX16xxxMEA(X)			
	TSSOP II	74LVC(H)16xxxDGG	SN74LVC(H)xxxDGGR(E4)	74LCX16xxxMTD(X)	MC74LCX16xxxDT	TC74LCX16xxxFT	74LVC(Z)16xxxTTR/ 74LCX(H)16xxxTTR
	LFBGA	74LVC(H)32xxxEC	SN74LVC(H)32xxxGKER				
	VFBGA	74LVC(H)32xxxEV	SN74LVC(H)32xxxGQL				
ALVC(H) H = bus hold feature	SOIC	74ALVCxxxD	SN74ALVCxxxD(W)(E4/R/RE4/W/WR/WRE4)	74VCXxxxM(X)			
	TSSOP	74ALVCxxxPW	SN74ALVCxxxPW(E4/G4/LE/R/RE4/GG4)	74VCXxxxMTC(X)		TC74VCXxxxFT	
	DQFN	74ALVCxxxBQ		74VCXxxxBQX			
	TSSOP II	74ALVC(H)16xxxDGG	SN74ALVC(H)16xxxDGGR(G4)	74VCX16xxxMTD(X)		TC74VCX(H)(R)16xxxFT	74ALVCH16xxxTTR 74VCX(H)16xxxTTR
	LFBGA	74ALVC(H)32xxxEC	SN74ALVC(H)32xxxKR SN74ALVC(H)32xxxZKER				
LV series	SOIC	74LVxxxD	SN74LVxxxD(W)(E4/G4/R/RE4)	74LVXxxxM/WM	MC74LVXxxxD	TC74LVCxxxFN/W	
	SSOP II	74LVxxxDB	SN74LVxxxDBR(E4)	74LVXxxxMSA(X)		TC74LVCxxxFS	
	TSSOP I	74LVxxxPW	SN74LVxxxPW(E4/G4/LE/R/RE4/RG4/T/TE4)	74LVXxxxMTC(X)	MC74LVXxxxDT	TC74LVCxxxFT	
5-volt BiCMOS							
ABT(H) H = bus hold feature	DIP	74ABTxxxN	SN74ABTxxxN				
	SOIC	74ABTxxxD	SN74ABTxxxD(W)(E4/R/RE4)	74ABTxxxSC(X)			
	SSOP II	74ABTxxxDB	SN74ABTxxxDBR(G4)	74ABTxxxMSA(X)			
	TSSOP	74ABTxxxPW	SN74ABTxxxPW(E4/R)	74ABTxxxMTC(X)			
	SSOP III	74ABT(H)16xxxDL	SN74ABT(H)16xxxDL(G4/R)	74ABT16xxxSSC(X)			
	TSSOP II	74ABT(H)16xxxDGG	SN74ABT(H)16xxxDGGR(E4)	74ABT16xxxMTD(X)			
Low-voltage BiCMOS							
LVT series Built-in bus hold feature	SOIC	74LVTxxxD	SN74LVTHxxxD(W)(E4/R/RE4)	74LVTHxxxM/WM			
	SSOP II	74LVTxxxDB	SN74LVTHxxxDBR(E4/G4)	74LCTHxxxMSA(X)			
	TSSOP	74LVTxxxPW	SN74LVTHxxxPW(E4/R/RE4)	74LVTHxxxMTC(X)			
	DQFN	74LVTxxxBQ					
	SSOP III	74LVT16xxxDL	SN74LVTH16xxxDL(G4/R/RG4)	74LVTH16xxxMEA(X)			
	TSSOP II	74LVT16xxxDGG	SN74LVTH16xxxDGGR(E4)	74LVTH16xxxMTD(X)			
	VFBGA	74LVT16xxxEV	SN74LVTH16xxxGQLR				
	LFBGA	74LVT16xxxEC	SN74LVTH16xxxGKER SN74LVTH16xxxZKER				
ALVT series Built-in bus hold feature	SSOP II	74ALVT16xxxDL	SN74ALV16xxxDL(G4/R/RG4)				
	TSSOP II	74ALVT16xxxDGG	SN74ALV16xxxGR(E4)				
Bipolar							
FAST	DIP	N74FxxxN	SN74FxxxN	74Fxxx(S)PC			
	SOIC	N74FxxxD	SN74FxxxD(W)(E4/G4/R/RE4/RG4)	74FxxxSC			
	SSOP II	N74FxxxDB	SN74FxxxDBR(E4)	74FxxxMSA(X)			

NOTE: Texas Instruments changes package suffixes from D to DW when part is 20-28 pins

クロスリファレンス；PicoGate、MicroPak製品

Package	NXP	Texas Instruments	Fairchild Semiconductor	ON Semiconductor	Toshiba	STMicroelectronics
HC series						
SOT353	74HC1GxxxGW		NC7SxxxP5X	MC74HC1GxxxDFT	TC7SxxxFU	74H1GxxxCTR
SOT753	74HC1GxxxGV		NC7SxxxM5X	MC74HC1GxxxDTT	TC7SxxxF	74H1GxxxSTR
SOT363	74HC2GxxxGW					
SOT457	74HC2GxxxGV					
SOT505-2	74HC2GxxxDP				TC7WxxxFU	
SOT765	74HC2GxxxDC					
SOT505-2	74HC3GxxxDP				TC7WxxxFU	
SOT765	74HC3GxxxDC					
HCT series						
SOT353	74HCT1GxxxGW		NC7STxxxP5X			
SOT753	74HCT1GxxxGV		NC7STxxxM5X			
SOT363	74HCT2GxxxGW					
SOT457	74HCT2GxxxGV					
SOT505-2	74HCT2GxxxDP				TC7WTxxxFU	
SOT765	74HCT2GxxxDC					
SOT505-2	74HCT3GxxxDP					
SOT765	74HCT3GxxxDC					
AHC series						
SOT353	74AHC1GxxxGW	SN74AHC1GxxxDCK		MC74VHC1GxxxDFT	TC7SHxxxFU	74V1GxxxCTR
SOT753	74AHC1GxxxGV	SN74AHC1GxxxDBV		MC74VHC1GxxxDTT	TC7SHxxxF	74V1GxxxSTR
SOT505-2	74AHC2GxxxDP				TC7WHxxxFU	74V2GxxxSTR
SOT765	74AHC2GxxxDC					
SOT505-2	74AHC3GxxxDP				TC7WHxxxFU	
SOT765	74AHC3GxxxDC					
AHCT series						
SOT353	74AHCT1GxxxGW	SN74AHCT1GxxxDCK		MC74VHC1GTxxxDFT	TC7SETxxxFU	74V1GTxxxCTR
SOT753	74AHCT1GxxxGV	SN74AHCT1GxxxDBV		MC74VHC1GTxxxDTT	TC7SETxxxF	74V1GTxxxSTR
SOT505-2	74AHCT2GxxxDP					
SOT765	74AHCT2GxxxDC					
SOT505-2	74AHCT3GxxxDP					
SOT765	74AHCT3GxxxDC					
LVC series						
SOT353	74LVC1GxxxGW	SN74LVC1GxxxDCK	NC7SZxxxP5X	NL17SZxxxDFT2(G)	TC7SZxxxFU	74LX1GxxxCTR
SOT753	74LVC1GxxxGV	SN74LVC1GxxxDBV	NC7SZxxxM5X		TC7SZxxxF	74LX1GxxxSTR
SOT886	74LVC1GxxxGM	SN74LVC1GxxxYZP	NC7SZxxxL6X			
SOT363	74LVC1GxxxGW	SN74LVC1GxxxDCK	NC7SZxxxP6X	NL17SZxxxDFT2(G)		
SOT457	74LVC1GxxxGV	SN74LVC1GxxxDBV				
SOT891	74LVC1GxxxGF					
SOT505-2	74LVC1GxxxDP	SN74LVC1GxxxDCT SN74LVC2GxxxDCT				
SOT765	74LVC1GxxxDC	SN74LVC1GxxxDCU SN74LVC2GxxxDCU	NC7SZxxxK8	NL17SZxxxUS		
SOT883	74LVC1GxxxGT	SN74LVC1GxxxYZP SN74LVC2GxxxYZP				
SOT902	74LVC1GxxxGM		NC7SZxxxL8			
SOT886	74LVC2GxxxGM	SN74LVC2GxxxYZP	NC7WZxxxL6			
SOT363	74LVC2GxxxGW	SN74LVC2GxxxDBV/DCK	NC7WZxxxP6	NL27SZxxxDFT		
SOT457	74LVC2GxxxGV	SN74LVC1GxxxDCT SN74LVC2GxxxDCT		NL27SZxxxDTT		
SOT891	74LVC2GxxxGF					
SOT505-2	74LVC2GxxxDP	SN74LVC2GxxxDCT			TC7WZxxxFU	
SOT765	74LVC2GxxxDC	SN74LVC2GxxxDCU	NC7SZxxxK8	NL27SZxxxUS		
SOT883	74LVC2GxxxGT	SN74LVC2GxxxYZP				
SOT902	74LVC2GxxxGM		NC7SZxxxL8			
SOT505-2	74LVC3GxxxDP	SN74LVC3GxxxDCT				
SOT765	74LVC3GxxxDC	SN74LVC3GxxxDCU	NC7NZxxxK8	NL37SZxxxUS		
SOT883	74LVC3GxxxGT	SN74LVC3GxxxYZP				
SOT902	74LVC3GxxxGM		NC7NZxxxL8			
AUP series						
SOT353	74AUP1GxxxGW	SN74AUP1GxxxDCK	NC7SPxxxP5		TC7SGxxxFU	
SOT886	74AUP1GxxxGM	SN74AUP1GxxxYZP	NC7SPxxxL6			
SOT363	74AUP1GxxxGW	SN74AUP1GxxxDCK	NC7SPxxxP6			
SOT891	74AUP1GxxxGF					
SOT765	74AUP1GxxxDC		NC7SPxxxK8			
SOT833	74AUP1GxxxGT					
SOT902	74AUP1GxxxGM		NC7WPxxxL8			
SOT886	74AUP2GxxxGM		NC7SPxxxL6			
SOT363	74AUP2GxxxGW		NC7WPxxxP6			
SOT891	74AUP2GxxxGF					
SOT765	74AUP2GxxxDC		NC7WPxxxK8		TC7WGxxxFK	
SOT833	74AUP2GxxxGT					
SOT902	74AUP2GxxxGM		NC7WPxxxL8			

4000 ロジック品番 NXP vs. 東芝



ファミリ

NXP	HEF
東芝	TC

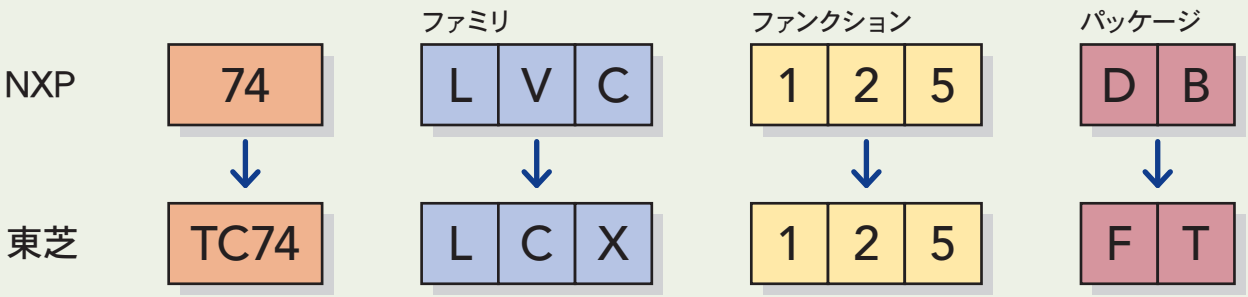
ファンクション

パッケージ

NXP	東芝
BT	BF
BP	BP

NXPでは4000のゲートロジックの取り扱いはありません

汎用ロジック品番 NXP vs. 東芝



NXP	74
東芝	TC74

ファミリ

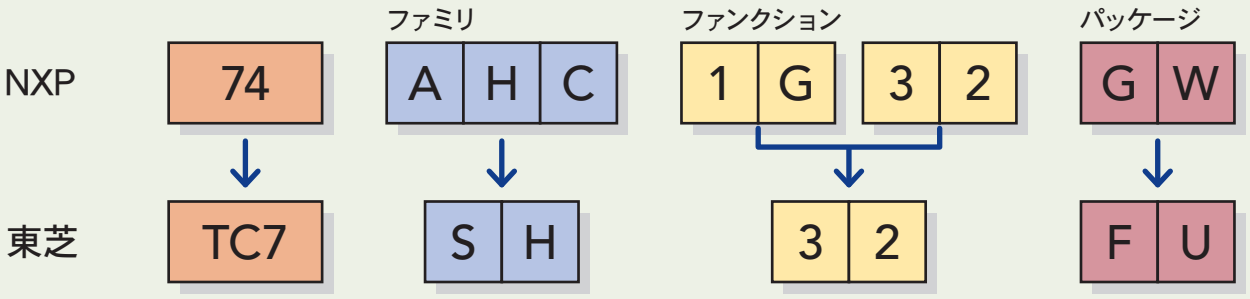
NXP	東芝
HC HCT	HC HCT
LVC	LCX
LV	LVX
ALVCH	VCX
AHC AHCT	VHC VHCT

ファンクション

パッケージ

NXP	東芝
N	P
D	FW
D	FN
DB	FT
DB	FS
DP	FU
GV	F
DC	FK
HR	FTG

ゲートロジック品番 NXP vs. 東芝



NXP	東芝
74	TC7

ファミリ

NXP	東芝
LVC ALVCH	PA
AHC	PH
AHC	S
LVC ALVCH	SA
AHCT	SET
AHC	SH
LVC	SZ
HCT	W
AHC	WH
HCT	WT

ファンクション

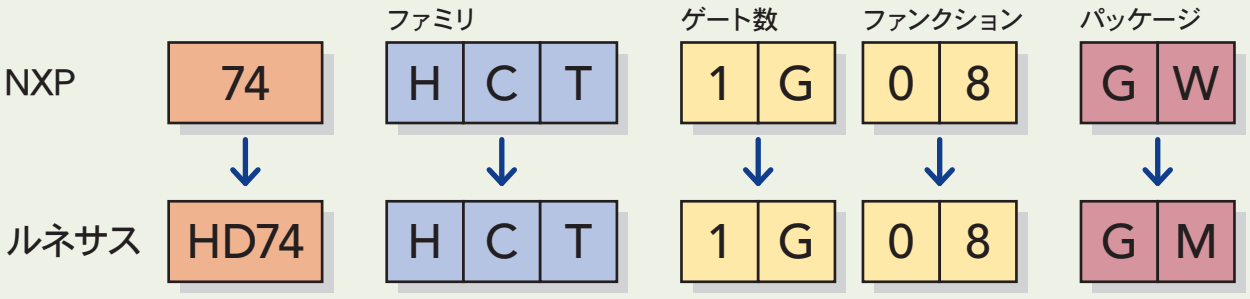
ゲート数（東芝 vs.NXP）
- S ≒ 1G
- W ≒ 1,2,3G

NXP スイッチングレベル
- CMOS レベル：“G”
- TTLレベル ：“T”

パッケージ

NXP	東芝
GW DP	FU
DC	FK

ゲートロジック品番 NXP vs. ルネサス



NXP	ルネサス
74	HD74

ファミリ

NXP	ルネサス
HC	HC
HCT	HCT
LV	LV
LVT	LVT
ALVC	ALVC
ABT	BC
CBT	CBT
ABT	LS
LVC	LVC

ファンクション

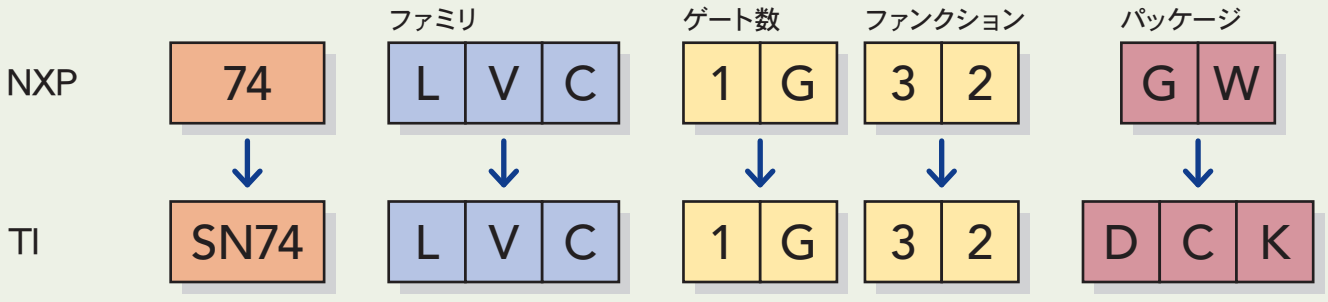
ゲート数
- xG（x= ゲート数）

スイッチングレベル
- CMOS レベル：“G”
- TTLレベル：“T”

パッケージ

NXP	ルネサス
GW	CM
DC	US
N	P
PW	T
DC	FP

汎用ロジック品番 NXP vs. TI



NXP	TI
74	SN74

ファミリ

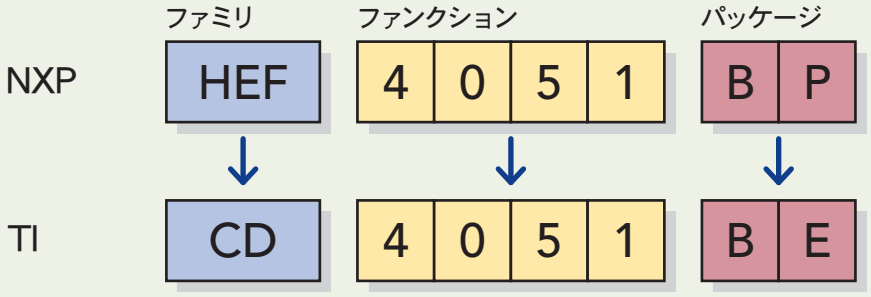
NXP	TI
HC HCT	HC HCT
AHC AHCT	AHC AHCT
F	F
ABT	ABT
CBT CBTLV	CBT CBTLV
LV	LV
LVC	LVC
ALVC	ALVC
AUP*	AUP or AUC
LVT ALVT	LVT ALVT
AVC	AVC

ファンクション
ゲート数
xG (x= ゲート数)
スイッチングレベル
CMOS レベル：“G”
TTLレベル：“T”

パッケージ

NXP	TI
N or BP	N or E*
D	D or M*
PW	PW
GW	DCK
DC	DCU
GM	YZP or DRY*
DGG	DGG or G*
DB	DB
GV	DDC or DBV*
GF	DQE

4000 ロジック品番 NXP vs. TI



ファミリ

NXP	TI
HEF	CD

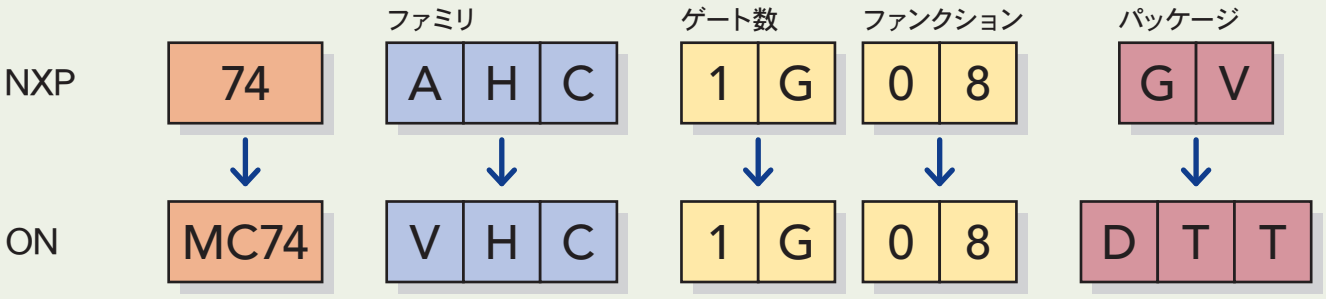
ファンクション

パッケージ

NXP	TI
BP	BE
BT	BM
BTT	–
BTS	–

NXPでは4000のゲートロジックの取り扱いはありません

汎用ロジック品番 NXP vs. ON



NXP	ON
74	MC74

Sometimes MC is omitted

ファミリ

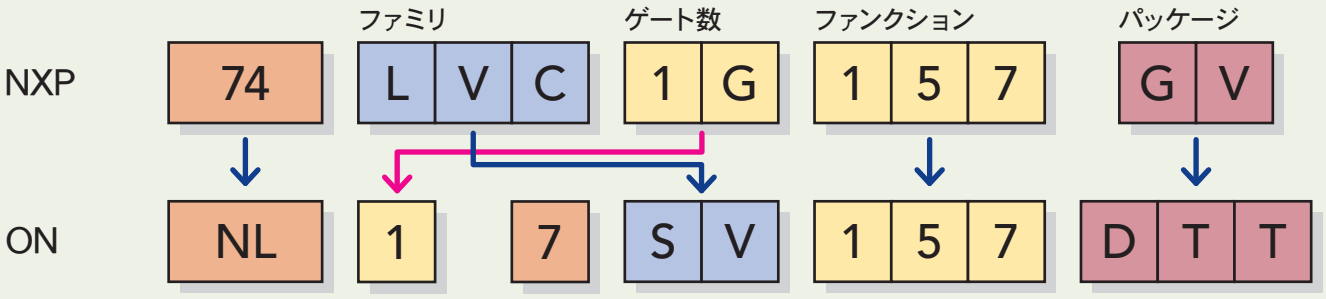
NXP	ON
HC HCT	HC HCT
LVC	LCX
LV	LVX
ALVCH	VCX
AHC AHCT	VHC VHCT

ファンクション
ゲート数
xG (x= ゲート数)
スイッチングレベル
CMOS レベル：“G”
TTLレベル：“T”

パッケージ

NXP	ON
N or BP	N
D	D
PW	DT
GW	DFT
GV	DTT
DC	US
DS	QZ
BQ	MN
GM	AMX
GS	CMX

ゲートロジック品番 NXP vs. ON (MiniGates)



NXP	ON
74	NL - 74

ファミリ

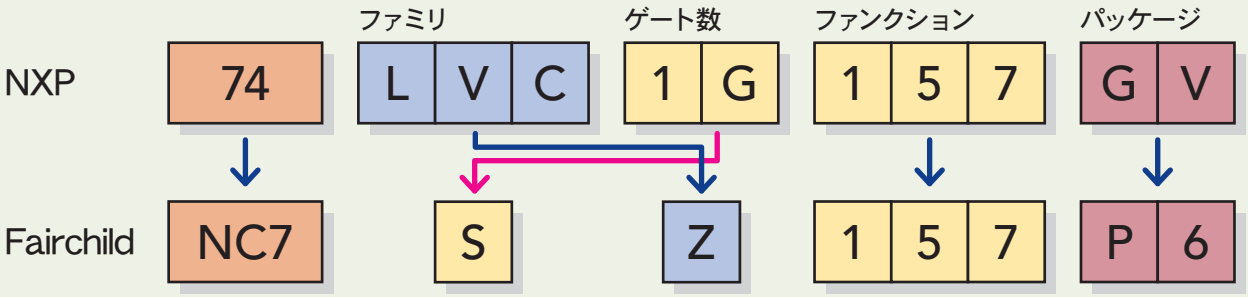
NXP	ON
AUP	SV
LVC	SZ
LVC	WZ
LVC	WB

ファンクション
ゲート数
= 1G = 1 Gate
2 = 2G = 2 Gates
3 = 3G = 3 Gates
“1” がなく
“NL7” の製品もあります

パッケージ

NXP	ON
N or BP	N
D	D
PW	DT
GW	DFT
GV	DTT
DC	US
DS	QZ
BQ	MN
GM	AMX
GS	CMX

ゲートロジック品番 NXP vs. Fairchild (MiniGates)



NXP	Fairchild
74	NC7

ファミリ

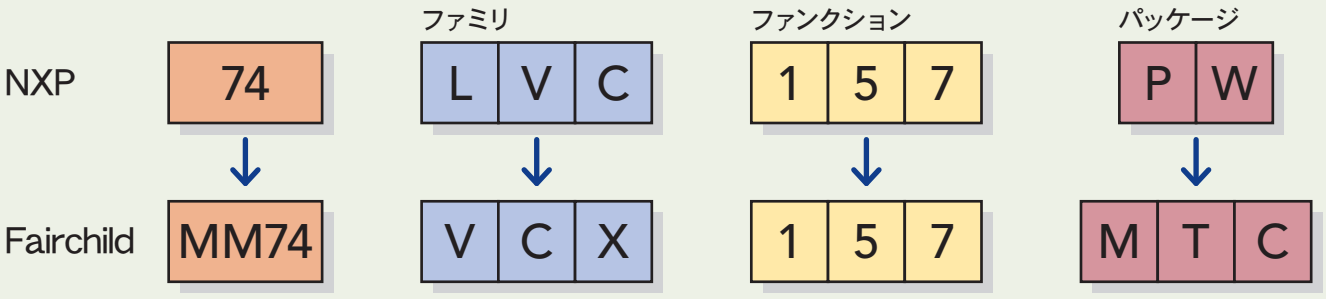
NXP	Fairchild
LVC	B
AUP	P
HC HCT	T
AUP	V
LVC	Z

ファンクション
ゲート数
S = 1G = 1 Gate
W = 2G = 2 Gates
N = 3G = 3 Gates

パッケージ

NXP	FSC
DB	MSA
DL	MEA
PW	MTC
DGG	MTD
BQ	BQ
GW	P5
GV	M5
GW	P6
DC	K8
GM	L6 L8

汎用ロジック品番 NXP vs. Fairchild



NXP	Fairchild
74	MM74

ファミリ

NXP	Fairchild
ABT	ABT
F	F
HC HCT	HC HCT
LVC	LCX
LV	LVX
ALVCH	VCX
AHC AHCT	VHC VHCT
ABT	ABT

ファンクション

パッケージ

NXP	FSC
N	N PC SPC
D	M SC
DB	MSA
DL	MEA
PW	MTC
DGG	MTD
BQ	BQ
GW	P5
GV	M5
GW	P6
DC	K8
GM	L6 L8

NXPセミコンダクター ロジック製品　ステータスガイド

http://ics.nxp.com/

低電圧CMOSロジック

AVC ファミリ　Advanced Very-low-voltage CMOS AVC(M)

http://www.nxp.com/products/logic/family/AVC_M/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74AVC1T45	Dual supply translating transceiver; 3-state				GW			GM						
74AVCH1T45	Dual supply translating transceiver; 3-state				GW			GM						
74AVC2T45	Dual-bit, dual-supply voltage level translator/transceiver; 3-state				DP				DC	GD	GF	GN	GS	GT
74AVCH2T45	Dual-bit, dual-supply voltage level translator/transceiver; 3-state								DC	GD	GF	GN	GS	GT
74AVC4T245	4-bit dual supply translating transceiver with configurable voltage translation; 3-state			D	PW	BQ								
74AVCH4T245	4-bit dual supply translating transceiver with configurable voltage translation; 3-state			D	PW	BQ								
74AVC4TD245	2.5V 4-bit dual-supply voltage-level translating transceiver with configurable voltage translation (3-state)				PW									
74AVC8T245	8-bit dual supply translating transceiver with configurable voltage translation; 3-state				PW	BQ								
74AVCH8T245	8-bit dual supply translating transceiver with configurable voltage translation; 3-state				PW	BQ								
74AVC16T245	16-bit dual supply translating transceiver with configurable voltage translation; 3-state				DGG DGV									
74AVCH16T245	16-bit dual supply translating transceiver with configurable voltage translation; 3-state				DGG									
74AVC16244	16-bit buffer/line driver; 3-state (3.6 V tolerant)				DGG									
74AVC16373	16-bit D-type transparent latch; 3.6 V tolerant; 3-state				DGG									
74AVC16374	16-bit edge triggered D-type flip-flop; 3.6 V tolerant; 3-state				DGG									
74AVCH16244	16-bit buffer/line driver; 3.6 V tolerant; 3-state				DGG									
74AVC16245	2.5V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)				DGG									
74AVC16334A	2.5V 16-Bit Registered Driver with Inverted Register Enable (3-State)				DGG									
74AVC16834A	2.5V 18-Bit Registered Driver with Inverted Register Enable (3-State)				DGG DGV									
74AVC16835A	2.5V 18-Bit Registered Driver (3-State)				DGG DGV									
74AVC16836A	2.5V 20-Bit Registered Driver with Inverted Register Enable (3-State)				DGG DGV									
74AVCH16245	2.5V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)				DGG									
74AVCM162834	2.5V 18-Bit Registered Driver with Inverted Register Enable, 15 Ohm Termination Resistors, and 3.6V Tolerant I/Os (3-State)				DGG									
74AVCM162835	2.5V 18-Bit Registered Driver with 15 Ohm Termination Resistors and 3.6V Tolerant I/Os (3-State)				DGG									
74AVCM162836	2.5V 20-Bit Registered Driver with Inverted Register Enable, 15 Ohm Termination Resistors, and 3.6V Tolerant I/Os (3-State)				DGG									
74AVC20T245	2.5V 20-bit dual-supply voltage-level translating transceiver with configurable voltage translation (3-state)				DGG DGV	BX								
74AVCH20T245	2.5V 20-bit dual-supply voltage-level translating transceiver with configurable voltage translation and bus hold (3-state)				DGG DGV									

＊リストにないパッケージはお問い合わせください。

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74ALVC00	Quad 2-input NAND gate			D	PW	BQ								
74ALVC02	Quad 2-input NOR gate			D	PW	BQ								
74ALVC04	Hex inverter			D	PW	BQ								
74ALVC08	Quad 2-input AND gate			D	PW	BQ								
74ALVC14	3.3V Hex Inverting Schmitt Trigger			D	PW	BQ								
74ALVC125	Quad buffer/line driver; 3-state			D	PW	BQ								
74ALVC244	Octal buffer/line driver; 3-state			D	PW	BQ								
74ALVC245	3.3V Octal Transceiver with Direction Pin, Non-Inverting (3-State)			D	PW	BQ								
74ALVC32	Quad 2-input OR gate			D	PW	BQ								
74ALVC373	Octal D-type transparent latch; 3-state			D	PW	BQ								
74ALVC374	Octal D-type flip-flop; positive-edge trigger; 3-state			D	PW	BQ								
74ALVC541	Octal buffer/line driver; 3-state			D	PW	BQ								
74ALVC573	Octal D-type transparent latch; 3-state			D	PW	BQ								
74ALVC574	Octal D-type flip-flop; positive edge trigger; 3-state			D	PW	BQ								
74ALVC74	Dual D-type flip-flop with set and reset; positive-edge trigger			D	PW	BQ								
74ALVC_ ALVCH16244	3.3V 16-Bit Buffer/Line Driver; Non-Inverting with Bus Hold (3-State)		DL		DGG									
74ALVC_ ALVCH16244	3.3V 16-Bit Buffer/Line Driver; Non-Inverting (3-State)		DL		DGG									
74ALVCH162244	2.5 V / 3.3 V 16-bit buffer/line driver (3-state)		DL		DGG									
74ALVC_ ALVCH16244	2.5 V / 3.3 V 16-bit buffer/line driver (3-State)		DL		DGG									
74ALVC_ ALVCH16245	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)		DL		DGG									
74ALVC_ ALVCH16245	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)		DL		DGG									
74ALVC164245	16-bit dual supply translating transceiver; 3-state		DL		DGG	BQ								
74ALVCH162245	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ohm Termination Resistors (3-State)		DL		DGG									
74ALVC162334A	3.3V 16-Bit Registered Driver with Inverted Register Enable with 30 Ohm Termination Resistors (3-State)				DGG									
74ALVCH16373	2.5 V / 3.3 V 16-bit D-type transparent latch (3-State)		DL		DGG									
74ALVCH16374	2.5 V/3.3 V 16-bit edge-triggered D-type flip-flop; 3-state		DL		DGG									
74ALVCH16500	3.3V 18-Bit Universal Bus Transceiver; Negative Edge Trigger Clock with Bus Hold (3-State)				DGG									
74ALVCH16501	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold (3-State)		DL		DGG									
74ALVCH16543	3.3V 16-Bit Latched Transceiver; Non-Inverting with Bus Hold (3-State)				DGG									
74ALVCH16600	3.3V 18-Bit Universal Bus Transceiver; Negative Edge Trigger Clock with Bus Hold (3-State)				DGG									
74ALVCH162601	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold and 30 Ohm Termination Resistors; (3-State)				DGG									
74ALVCH16601	3.3V 18-Bit Universal Bus Transceiver; Positive Edge Trigger Clock with Bus Hold (3-State)				DGG									
74ALVCH16646	3.3V 16-Bit Transceiver/Register with Bus Hold (3-State)				DGG									
74ALVCH16652	3.3V 16-Bit Transceiver/Register; Non-Inverting with Bus Hold (3-State)				DGG									
74ALVCH16821	20-bit bus-interface D-type flip-flop' positive-edge trigger (3-State)		DL		DGG									
74ALVCH16823	18-bit bus-interface D-type flip-flop with reset and enable (3-State)		DL		DGG									
74ALVCH16825	18-bit buffer/driver (3-State)				DGG									
74ALVCH16827	20-bit buffer/line driver, non-inverting (3-State)				DGG									
74ALVCH162827	20-bit buffer/line driver, non-inverting, with 30 Ohm termination resistors (3-State)				DGG									
74ALVCHS16830	3.3V 18-Bit to 36-Bit Address Driver with Bus Hold (3-State)				GB									
74ALVCH16832	3.3V 7-Bit to 28-Bit Address Register/Driver (3-State)				DGG									
74ALVCHS162830	3.3V 18-Bit to 36-Bit Address Driver with Bus Hold (3-State)				GB									
74ALVC162834A	3.3V 18-Bit Registered Driver with Inverted Register Enable with 30 Ohm Termination Resistors (3-State)				DGG									
74ALVC162835A	3.3V 18-Bit Registered Driver with 30 Ohm Termination Resistors (3-State)				DGG									
74ALVC162836A	3.3V 20-Bit Registered Driver with Inverted Register Enable with 30 Ohm Termination Resistors (3-State)				DGG									
74ALVC16834A	3.3V 18-Bit Registered Driver with Inverted Register Enable (3-State)				DGG									
74ALVC16835A	3.3V 18-Bit Registered Driver (3-State)				DGG									
74ALVC16836A	3.3V 20-Bit Registered Driver with Inverted Register Enable (3-State)				DGG									
74ALVCH16841	20-bit bus interface D-type latch (3-State)				DGG									
74ALVCH16843	18-bit bus-interface D-type latch (3-State)				DGG									
74ALVCH16952	3.3V 16-Bit Registered Transceiver with Bus Hold (3-State)				DGG									

＊リストにないパッケージはお問い合わせください。

低電圧CMOSロジック

LVCファミリ：（Low-Voltage CMOS）-1

http://www.nxp.com/products/logic/family/LVC/#products

NXP 品名	機能名	パッケージ												
		SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)	BGA (EC/EV/UK)
74LVC1G00	Presettable synchronous 4-bit up/down binary counter	DB	D	PW	BQ									
74LVC1G02	Single 2-input NAND-gate			GW		GV	GM			GF	GN	GS		
74LVC1G04	Single 2-input NOR gate			GW		GV	GM			GF	GN	GS		
74LVC1G06	Single inverter			GW		GV	GM			GF	GN	GS		
74LVC1G07	inverter with open-drain output			GW		GV	GM			GF	GN	GS		
74LVC1G08	Buffer with open-drain output			GW		GV	GM			GF	GN	GS		
74LVC1G10	Single 2-input AND gate			GW		GV	GM			GF	GN	GS		
74LVC1G11	Single 3-input NAND gate			GW		GV	GM			GF	GN	GS		
74LVC1G125	Single 3-input AND gate			GW		GV	GM			GF				
74LVC1G126	Bus buffer/line driver; 3-state			GW		GV	GM			GF	GN	GS		
74LVC1G14	Single Schmitt-trigger inverter			GW		GV	GM			GF	GN	GS		
74LVC1G157	Single 2-input multiplexer			GW		GV	GM			GF	GN	GS		
74LVC1G175	Bus buffer/line driver; 3-state			GW		GV	GM			GF	GN	GS		
74LVC1G18	Single D-type flip-flop with reset; positive edge trigger			GW		GV	GM			GF	GN	GS		
74LVC1G19	1-of-2 non-inverting demultiplexer with 3-state deselected output			GW		GV								
74LVC1G3157	2-channel analog multiplexer/demultiplexer			GW		GV	GM			GF	GN	GS		
74LVC1G32	1-of-2 decoder/demultiplexer			GW		GV	GM			GF	GN	GS		
74LVC1G332	Single 2-input OR gate			GW		GV	GM			GF	GN	GS		
74LVC1G34	Single 3-input OR gate			GW		GV	GM			GF	GN	GS		
74LVC1G38	Single buffer			GW		GV	GM			GF	GN	GS		
74LVC1G384	2-inout NAND gate; open drain			GW		GV								
74LVC1G386	Bilateral switch			GW		GV	GM			GF	GN	GS		
74LVC1G53	2-channel analog multiplexer/demultiplexer			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC1G57	3-input EXCLUSIVE-OR gate			GW		GV								
74LVC1G58	Low-power configurable multiple function gate			GW		GV	GM			GF	GN	GS		
74LVC1G66	Low-power configurable multiple function gate			GW		GV	GM			GF	GN	GS		
74LVC1G74	Bilateral switch			GW		GV	GM			GF				
74LVC1G79	Single D-type flip-flop with set and reset; positive edge trigger			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC1G80	Single D-type flip-flop; positive- edge trigger			GW		GV	GM			GF	GN	GS		
74LVC1G86	Single D-type flip-flop; positive-edge trigger			GW		GV	GM			GF	GN	GS		
74LVC1G97	2-input EXCLUSIVE-OR gate			GW		GV	GM			GF	GN	GS		
74LVC1G98	Low-power configurable multiple function gate			GW		GV	GM			GF	GN	GS		
74LVC1G99	Low-power configurable multiple function gate			GW		GV	GM			GF	GN	GS		
74LVC1GU04	Ultra-configurable multiple function gate; 3-state			DP			GM		GD	GF	GN	GS	GT	
74LVC1GX04	Inverter			GW		GV	GM			GF	GN	GS		
74LVC2G00	Octal D-type flip-flop with reset; positive-edge trigger	DB	D	PW	BQ									
74LVC2G02	Dual 2-input NAND gate			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC2G04	Dual 2-input NOR gate			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC2G06	Dual inverter			GW		GV	GM			GF	GN	GS		
74LVC2G07	Inverters with open-drain outputs			GW		GV	GM			GF	GN	GS		
74LVC2G08	Buffers with open-drain outputs			GW		GV	GM			GF	GN	GS		
74LVC2G125	Dual 2-input AND gate			DP			GM	DC	GD				GT	
74LVC2G126	Dual bus buffer/line driver; 3-state			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC2G14	Dual inverting Schmitt trigger with 5 V tolerant input			GW		GV	GM			GF	GN	GS		
74LVC2G17	Dual non-inverting Schmitt trigger with 5 V tolerant input			GW		GV	GM			GF	GN	GS		
74LVC2G240	Dual bus buffer/line driver; 3-state			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC2G241	Dual inverting buffer/line driver; 3-state			DP			GM	DC		GF			GT	
74LVC2G32	Dual buffer/line driver; 3-state			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC2G34	Dual 2-input OR gate			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC2G38	Dual buffer gate			GW		GV	GM			GF				
74LVC_ LVCH2T45	Octal buffer/line driver; 3-state	DB	D	PW	BQ/BX									
74LVC2G53	Dual 2-input NAND gate; open drain			DP			GM	DC	GD	GF	GN	GS		
74LVC2G66	2-channel analog multiplexer/demultiplexer						GM							
74LVCV2G66	Overvoltage tolerant bilateral switch			DP				DC	GD					
74LVC2G74	Bilateral switch			DP			GM	DC	GD				GT	
74LVC2G86	Single D-type flip-flop with set and reset; positive edge trigger			DP			GM	DC	GD	GF			GT	
74LVC2GU04	Dual 2-input EXCLUSIVE-OR gate			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC3G04	Quad 2-input NAND gate (open drain)	DB	D	PW	BQ									
74LVC3G06	Triple inverter			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC3G07	Triple inverter with open-drain output			DP			GM	DC	GD				GT	
74LVC3G14	Triple inverting Schmitt trigger with 5 V tolerant input			DP			GM	DC	GD	GF		GS	GT	
74LVC3G17	Triple non-inverting Schmitt trigger with 5 V tolerant input			DP			GM	DC	GD	GF	GN		GT	
74LVC3G34	Triple buffer with open-drain output			DP			GM	DC	GD		GN	GS	GT	
74LVC3GU04	Triple buffer			DP			GM	DC	GD	GF	GN	GS	GT	
74LVC_ LVCH8T245	8-bit dual supply translating transceiver; 3-state			PW	BQ									
74LVC_ LVCH8T245	8-bit dual supply translating transceiver; 3-state			PW	BQ									
74LVC00A	Dual supply translating transceiver; 3-state						GM	DC	GD	GF		GS	GT	
74LVC02A	Quad 2-input NAND gate	DB	D	PW	BQ									
74LVC04A	Quad 2-input NOR gate	DB	D	PW	BQ									
74LVCU04A	Hex inverter	DB	D	PW	BQ									

＊リストにないパッケージはお問い合わせください。

低電圧CMOSロジック

LVCファミリ：（Low-Voltage CMOS）-2

http://www.nxp.com/products/logic/family/LVC/#products

NXP 品名	機能名	パッケージ												
		SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)	BGA (EC/EV/UK)
74LVC06A	Hex inverter	DB	D	PW	BQ									
74LVC07A	Hex inverter with open-drain outputs		D	PW	BQ									
74LVC08A	Hex buffer with open-drain outputs		D	PW	BQ									
74LVC109	Quad 2-input AND gate	DB	D	PW	BQ									
74LVC10A	Dual JK(not) flip-flop with set and reset; positive-edge trigger	DB	D	PW										
74LVC11	Triple 3-input NAND gate	DB	D	PW	BQ									
74LVC27	Octal buffer/line driver with 5 V tolerant inputs/outputs (3-State)	DB	D	PW										
74LVC32A	Dual inverter			GW		GV	GM			GF	GN	GS		
74LVC38A	Octal D-type flip-flop with data enable; positive-edge trigger	DB	D	PW										
74LVC74A	Octal D-type flip-flop with 5 V tolerant inputs/outputs; positive edge-trigger (3-state)	DB	D	PW	BQ									
74LVC86A	10-bit transparent latch with 5 V tolerant inputs/outputs; 3-state	DB	D	PW	BQ									
74LVC125A	Triple 3-input AND gate	DB	D	PW	BQ									
74LVC126A	Quad buffer/line driver with 5 V tolerant input/outputs; 3-state	DB	D	PW	BQ									
74LVC132A	Quad 2-input NAND Schmitt trigger		D	PW	BQ									
74LVC138A	Quad buffer/line driver with 5 Volt tolerant input/outputs; 3-state	DB	D	PW	BQ									
74LVC139	3-to-8 line decoder/demultiplexer; inverting	DB	D	PW	BQ									
74LVC14A	Hex inverting Schmitt trigger with 5 V tolerant input	DB	D	PW										
74LVC157A	Quad 2-input multiplexer	DB	D	PW	BQ									
74LVC161	Dual 2-to-4 line decoder/demultiplexer	DB	D	PW	BQ									
74LVC163	16-bit buffer/line driver with 5 V tolerant inputs/outputs; 3-state		DL	DGG										
74LVC169	Presettable synchronous 4-bit binary counter; synchronous reset	DB	D	PW	BQ									
74LVC240A	Octal buffer/line driver with 30 Ohm series termination resistors; 5 V tolerant input/output; 3-state	DB	D	PW	BQ									
74LVC241A	Octal buffer/line driver with 5 V tolerant inputs/outputs; inverting; 3-state	DB	D	PW	BQ									
74LVC2244A	X-tal driver			GW		GV								
74LVC2245A	Octal transceiver with direction pin, 30 W series termination resistors; 5 V tolerant input/output; 3-state	DB	D	PW	BQ									
74LVC257A	Quad 2-input multiplexer with 5 V tolerant inputs/outputs; 3-state	DB	D	PW	BQ									
74LVC273	Triple 3-input NOR gate	DB	D	PW	BQ									
74LVC373A	Quad 2-input OR gate	DB	D	PW	BQ									
74LVC374A	Octal D-type transparent latch with 5 V tolerant inputs/outputs; (3-State)	DB	D	PW	BQ									
74LVC377	Octal D-type flip-flop with 5 V tolerant inputs/outputs; positive edge-trigger; 3-state	DB	D	PW	BQ									
74LVC4066	Triple inverter			DP			GM	DC	GD	GF		GS	GT	
74LVC4245A	Quad bilateral switch		D	PW	BQ									
74LVC541A	Octal dual supply translating transceiver; 3-state		D	PW	BQ									
74LVC543A	Octal D-type registered transceiver; 3-state	DB	D	PW	BQ									
74LVC544A	Octal D-type registered transceiver; inverting; 3-state	DB	D	PW										
74LVC573A	Octal buffer/line driver with 5 V tolerant inputs/outputs (3-state)	DB	D	PW	BQ									
74LVC574A	Octal D-type transparent latch with 5 V tolerant inputs/outputs; 3-state	DB	D	PW	BQ									
74LVC594A	8-bit shift register with output register		D	PW	BQ									
74LVC595A	8-bit serial-in/serial-out or parallel-out shift register; 3-state		D	PW	BQ									
74LVC623A	Octal transceiver with dual enable; 3-state	DB	D	PW										
74LVC646A	Octal bus transceiver/register; 3-state	DB	D	PW										
74LVC821A	Dual D-type flip-flop with set and reset; positive-edge trigger	DB	D	PW	BQ									
74LVC823A	10-bit D-type flip-flop with 5 V tolerant inputs/outputs; positive-edge trigger; 3-state	DB	D	PW	BQ									
74LVC827A	9-bit D-type flip-flop with 5 V tolerant inputs/outputs; positive edge-trigger; 3-state	DB	D	PW	BQ									
74LVC841A	10-bit buffer/line driver with 5 V tolerant inputs/outputs; 3-state	DB	D	PW										
74LVC2952A	Octal registered transceiver with 5 V tolerant inputs/outputs; 3-state	DB	D	PW										
74LVC16240A	Presettable synchronous 4-bit binary counter; asynchronous reset	DB	D	PW	BQ									
74LVC16241A	16-bit buffer/line driver with 5 V tolerant inputs/outputs; inverting; 3-state		DL	DGG										
74LVCH162374A	Quad 2-input EXCLUSIVE-OR gate	DB	D	PW	BQ									
74LVCH16541A	16-bit edge-triggered D-type flip-flop with 30 Ohm series termination resistors; 5 V input/output tolerant; 3-state		DL	DGG										
74LVCH322244A	16-bit buffer/line driver; 3-state		DL	DGG										
74LVCH322245A	32-bit bus transceiver with direction pin; 30 Ohm series termination resistors; 5 V tolerant; 3-state													EC
74LVCH32244A	32-bit buffer/line driver; with 30 Ohm series termination resistors; 5 V input/output tolerant; 3-state													EC
74LVCH32245A	32-bit bus transceiver with direction pin; 5 V tolerant; 3-state													EC
74LVCH32373A	32-bit buffer/line driver; 5 V input/output tolerant; 3-state													EC
74LVCH32374A	32-bit transparent D-type latch with 5 V tolerant inputs/outputs; 3-state													EC
74LVCH32374A	32-bit edge-triggered D-type flip-flop with 5 V tolerant inputs/outputs; 3-state													EC
74LVC_ LVCH162244A	3.3V single dual-supply translating transceiver (3-state)			GW			GM			GF	GN	GS		
74LVC_ LVCH162373A	16-bit buffer/line driver; 30 Ohm series termination resistors; 5 V tolerant input/output; 3-state	DL		DGG										
74LVC_ LVCH16244A	16-bit D-type transparent latch; 30 Ohm series termination resistors; 5 V tolerant inputs/outputs; 3-state	DL		DGG										
74LVC_ LVCH16245A	16-bit buffer/line driver; 5 V input/output tolerant; 3-state	DL		DGG	BQ									EV
74LVC_ LVCH16373A	16-bit bus transceiver with direction pin; 5 V tolerant; 3-state				BQ									
74LVC_ LVCH16374A	16-bit D-type transparent latch with 5 V tolerant inputs/outputs; 3-state	DL		DGG										
74LVC_ LVCH1T45	16-bit edge-triggered D-type flip-flop with 5 V tolerant inputs/outputs; 3-state	DL		DGG	BQ									
74LVC_ LVCH244A	Dual supply translating transceiver; 3-state			GW			GM			GF	GN	GS		
74LVC_ LVCH244A	Octal buffer/line driver; 3-state	DB	D	PW	BQ BX									
74LVC_ LVCH245A	Octal bus transceiver; 3-state	DB	D	PW	BQ BX									
74LVC_ LVCH245A	Octal bus transceiver; 3-state	DB	D	PW	BQ BX									

＊リストにないパッケージはお問い合わせください。

LV ファミリ：Low-Voltage

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74LV27	Triple 3-input NOR gate			D										
74LV32	Quad 2-input OR gate	N	DB	D	PW	BQ								
74LV86	Quad 2-input exclusive-OR gate	N	DB	D	PW	BQ								
74LV74	Dual D-type flip-flop with set and reset; positive-edge trigger	N	DB	D	PW									
74LV273	Octal D-type flip-flop with reset; positive-edge trigger	N	DB	D	PW									
74LV365	Hex buffer/line driver (3-State)	N	DB	D	PW									
74LV367	Hex buffer/line driver (3-State)	N	DB	D	PW									
74LV373	Octal D-type transparent latch (3-State)	N	DB	D	PW									
74LV374	Octal D-type flip-flop; positive edge-trigger; 3-state	N	DB	D	PW									
74LV377	Octal D-type flip-flop with data enable; positive edge-trigger	N	DB	D	PW									
74LV393	Dual 4-bit binary ripple counter	N	DB	D	PW									
74LV541	Octal buffer/line driver; 3-state	N	DB	D	PW									
74LV573	Octal D-type transparent latch; 3-state	N	DB	D	PW									
74LV574	Octal D-type flip-flop; positive edge-trigger; 3-state	N	DB	D	PW									
74LV4020	14-stage binary ripple counter	N	DB	D	PW									
74LV4051	8-channel analog multiplexer/demultiplexer	N	DB	D	PW	BQ								
74LV4052	Dual 4-channel analog multiplexer/demultiplexer	N	DB	D	PW									
74LV4053	Triple 2-channel analog multiplexer/demultiplexer	N	DB	D	PW	BQ								
74LV4060	14-stage binary ripple counter with oscillator	N	DB	D	PW									
74LV4066	Quad bilateral switches	N	DB	D	PW									

＊リストにないパッケージはお問い合わせください。

低電圧CMOSロジック

AUPファミリ：（Advanced Ultra-low Power）

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74AUP1G00	Low-power 2-input NAND gate				GW			GM			GF	GN	GS	
74AUP1G02	Low-power 2-input NOR-gate				GW			GM			GF	GN	GS	
74AUP1G04	Low-power inverter				GW		GV	GM			GF	GN	GS	
74AUP1G06	Low-power inverter with open-drain output				GW			GM			GF	GN	GS	
74AUP1G07	Low-power buffer with open-drain output				GW			GM			GF	GN	GS	
74AUP1G08	Low-power 2-input AND gate				GW			GM			GF	GN	GS	
74AUP1G0832	Low-power 3-input AND-OR gate				GW			GM			GF	GN	GS	
74AUP1G09	Low-power 2-input AND gate with open-drain				GW			GM			GF	GN	GS	
74AUP1G11	Low-power 3-input AND gate				GW			GM			GF	GN	GS	
74AUP1G125	Low-power buffer/line driver; 3-state				GW			GM			GF	GN	GS	
74AUP1G126	Low-power buffer/line-driver; 3-state				GW			GM			GF	GN	GS	
74AUP1G132	Low-power 2-input NAND Schmitt trigger				GW			GM			GF	GN	GS	
74AUP1G14	Low-power Schmitt trigger inverter				GW			GM			GF	GN	GS	
74AUP1G17	Low-power Schmitt trigger				GW			GM			GF	GN	GS	
74AUP1G175	Low-power D-type flip-flop with reset; positive-edge trigger				GW			GM			GF	GN	GS	
74AUP1G18	Low-power 1-of-2 demultiplexer with 3-state deselected output				GW			GM			GF	GN	GS	
74AUP1G19	Low-power 1-of-2 decoder/demultiplexer				GW			GM			GF	GN	GS	
74AUP1G240	Low-power inverting buffer/line driver; 3-state				GW			GM			GF	GN	GS	
74AUP1G32	Low-power 2-input OR-gate				GW			GM			GF	GN	GS	
74AUP1G3208	Low-power 3-input OR-AND gate				GW			GM			GF			
74AUP1G332	Low-power 3-input OR-gate				GW			GM			GF	GN	GS	
74AUP1G34	Low-power buffer				GW			GM			GF	GN	GS	
74AUP1G373	Low-power D-type transparent latch; 3-state				GW			GM			GF	GN	GS	
74AUP1G374	Low-power D-type flip-flop; positive-edge trigger; 3-state				GW			GM			GF			
74AUP1G38	Low-power 2-input NAND-gate (open drain)				GW			GM			GF	GN	GS	
74AUP1G386	Low-power 3-input EXCLUSIVE-OR gate				GW			GM			GF	GN	GS	
74AUP1G57	Low-power configurable multiple function gate				GW			GM			GF	GN	GS	
74AUP1G58	Low-power configurable multiple function gate				GW			GM			GF			
74AUP1G74	Low-power D-type flip-flop with set and reset; positive-edge trigger							GM	DC	GD				GT
74AUP1G79	Low-power D-type flip-flop; positive-edge trigger				GW		GV	GM			GF	GN	GS	
74AUP1G80	Low-power D-type flip-flop; positive-edge trigger				GW			GM			GF			
74AUP1G86	Low-power 2-input EXCLUSIVE-OR gate				GW			GM			GF	GN	GS	
74AUP1G885	Low-power dual function gate							GM	DC	GD	GF	GN	GS	GT
74AUP1G97	Low-power configurable multiple function gate				GW			GM			GF	GN	GS	
74AUP1G98	Low-power configurable multiple function gate							GM			GF	GN	GS	
74AUP1GU04	Low-power unbuffered inverter				GW			GM			GF	GN	GS	
74AUP1T34	Low-power dual supply translating buffer				GW			GM			GF	GN	GS	
74AUP1T45	Low-power dual supply translating transceiver; 3-state				GW			GM			GF	GN	GS	
74AUP1T57	Low-power configurable gate with voltage-level translator				GW			GM			GF	GN	GS	
74AUP1T58	Low-power configurable gate with voltage-level translator				GW			GM			GF	GN	GS	
74AUP1T97	Low-power configurable gate with voltage-level translator				GW			GM			GF	GN	GS	
74AUP1T98	Low-power configurable gate with voltage-level translator				GW			GM			GF	GN	GS	
74AUP1Z04	Low-power X-tal driver with enable and internal transistor				GW			GM			GF	GN	GS	
74AUP1Z125	Low-power X-tal driver with enable and internal resistor							GM	DC	GD	GF	GN	GS	GT
74AUP2G00	low-power dual 2-input NAND gate							GM	DC	GD	GF	GN	GS	GT
74AUP2G02	Low-power dual 2-input NOR gate							GM	DC	GD	GF	GN	GS	GT
74AUP2G04	Low-power dual inverter				GW			GM			GF	GN	GS	
74AUP2G06	Low-power dual inverter with open-drain output				GW			GM			GF	GN	GS	
74AUP2G07	Low-power dual buffer with open-drain output				GW			GM			GF	GN	GS	
74AUP2G08	Low-power dual 2-input AND gate							GM	DC	GD				GT
74AUP2G125	Low-power dual buffer/line driver; 3-state							GM	DC	GD	GF	GN	GS	GT
74AUP2G126	Low-power dual buffer/line driver; 3-state							GM	DC	GD	GF	GN	GS	GT
74AUP2G14	Low-power dual Schmitt trigger inverter				GW			GM			GF	GN	GS	
74AUP2G17	Low-power dual Schmitt trigger				GW			GM			GF	GN	GS	
74AUP2G240	Low-power dual inverting buffer/line driver; 3-state							GM	DC	GD	GF	GN	GS	GT
74AUP2G241	Low-power dual buffer/line driver; 3-state							GM	DC	GD	GF	GN	GS	GT
74AUP2G32	Low-power dual 2-input OR gate							GM	DC	GD	GF	GN	GS	GT
74AUP2G34	Low-power dual buffer				GW			GM			GF	GN	GS	
74AUP2G38	low-power dual 2-input NAND gate; open drain							GM	DC	GD	GF	GN	GS	GT
74AUP2G79	Low-power dual D-type flip-flop; positive-edge trigger							GM	DC	GD	GF	GN	GS	GT
74AUP2G80	Low-power dual D-type flip-flop; positive-edge trigger							GM	DC	GD	GF	GN	GS	GT
74AUP2G86	Low-power dual 2-input EXCLUSIVE-OR gate							GM	DC	GD				GT
74AUP2GU04	Low-power dual unbuffered inverter				GW			GM			GF	GN	GS	
74AUP2T1326	Low-power dual supply buffer/line driver; 3-state										GF			
74AUP3G04	Low-power triple inverter							GM	DC	GD				GT

＊リストにないパッケージはお問い合わせください。

低電圧CMOSロジック

ALVT ファミリ　Advanced Low-Voltage BiCMOS Technology (ALVT)

http://www.nxp.com/products/logic/family/ALVT/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74ALVT162240	16-bit inverting buffer/driver with 30 Ohm termination resistors (3-State)		DL		DGG									
74ALVT162241	2.5V/3.3V 16-bit buffer/driver with 30 Ohm termination resistors (3-State)		DL		DGG									
74ALVT162244	2.5 V / 3.3 V 16-bit buffer/driver with 30 Ohm termination resistors (3-State)		DL		DGG									
74ALVT16240	16-bit inverting buffer/driver; 3-state		DL		DGG									
74ALVT16241	16-bit buffer/driver; 3-state		DL		DGG									
74ALVT16244	2.5 V / 3.3 V 16-bit buffer/driver (3-State)		DL		DGG									
74ALVT16260	12-bit to 24-bit multiplexed D-type latches; 3-state		DL		DGG									
74ALVT162821	2.5 V / 3.3 V 20-bit bus-interface D-type flip-flop, positive-edge trigger with 30 Ohm termination resistors (3-State)		DL		DGG									
74ALVT162823	18-bit bus-interface D-type flip-flop with reset and enable with 30 Ohm termination resistors; 3-state		DL		DGG									
74ALVT162827	20-bit buffer/line driver, non-inverting,with 30Ohm termination resistors (3-State)		DL		DGG									
74ALVT16373	2.5 V / 3.3 V 16-bit transparent D-type latch (3-State)		DL		DGG									
74ALVT16374	16-bit edge-triggered D-type flip-flop; 3-state		DL		DGG									
74ALVT16821	20-bit bus interface D-type flip-flop; positive-edge trigger; 3-state		DL		DGG									
74ALVT16823	18-bit bus-interface D-type flip-flop with reset and enable; 3-state		DL		DGG									
74ALVT16827	20-bit buffer/line driver; non-inverting; 3-state		DL		DGG									
74ALVT162245	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ohm Termination Resistors (3-State)		DL		DGG									
74ALVT16245	3.3V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold (3-State)		DL		DGG									
74ALVT16501	3.3V 18-Bit Universal Bus Transceiver; Positive-Edge Trigger Clock with Bus Hold (3-State)		DL		DGG									
74ALVT16543	3.3V 16-Bit Latched Transceiver; Non-Inverting with Bus Hold (3-State)		DL		DGG									
74ALVT16601	3.3V 18-Bit Universal Bus Transceiver; Positive Edge-Trigger Clock with Bus Hold (3-State)		DL		DGG									
74ALVT16652	3.3V 16-Bit Transceiver/Register; Non-Inverting with Bus Hold (3-State)		DL		DGG									

＊リストにないパッケージはお問い合わせください。

NXP 品名	機能名	パッケージ												
		SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)	BGA (EC/EV/UK)
74LVT00	3.3 V Quad 2-input NAND gate	DB	D	PW										
74LVT02	3.3 V Quad 2-input NOR gate	DB	D	PW										
74LVT04	3.3 V Hex inverter	DB	D	PW										
74LVT08	3.3 V Quad 2-input AND gate	DB	D	PW										
74LVT10	3.3 V Triple 3-input NAND gate	DB	D	PW										
74LVT_LVTH125	3.3 V quad buffer; 3-state	DB	D	PW	BQ									
74LVT126	3.3 V quad buffer; 3-state	DB	D	PW	BQ									
74LVT1403	3.3 V combined 8-bit bus receiver and 4-bit bus driver			DR										
74LVT2241	3.3V Octal buffer/line driver with 30 Ohm series termination resistors; 3-State	DB	D	PW										
74LVT2244	3.3V Octal buffer/line driver with 30Ohm series termination resistors (3-State)	DB	D	PW										
74LVT240	ABT octal inverting buffer (3-State)	DB	D	PW										
74LVT241	3.3 V octal buffer/line driver; 3-state	DB	D	PW	BQ									
74LVT273	3.3 V octal D-type flip-flop	DB	D	PW	BQ									
74LVT32	3.3 V Quad 2-input OR gate	DB	D	PW										
74LVT374	3.3V Octal D-type flip-flop; positive-edge trigger (3-State)	DB	D	PW										
74LVT534	3.3 V Octal D-type flip-flop; inverting (3-State)	DB	D	PW										
74LVT573	3.3 V octal D-type transparent latch; 3-state	DB	D	PW	BQ									
74LVT74	3.3V Dual D-type flip-flop		D	PW	BQ									
74LVT_ LVTH244A	3.3 V octal buffer/line driver; 3-state	DB	D	PW	BQ									
74LVT_ LVTH244B	3.3 V octal buffer/line driver; 3-state	DB	D	PW	BQ									
74LVT245	3.3V transceiver with direction pin; non-inverting with bus hold (3-state)	DB	D	PW	BQ									
74LVT245B	3.3V transceiver with direction pin; non-inverting with bus hold (3-state)	DB	D	PW	BQ									
74LVT2952	3.3V registered transceiver with bus hold (3-state)	DB	D	PW										
74LVT373	3.3V octal D-type transparent latch (3-state)													
74LVT_LVTH574	3.3 V octal D-type flip-flop; 3-state	DB	D	PW	BQ									
74LVT543	3.3V latched transceiver; non-inverting with bus hold (3-state)	DB	D	PW										
74LVT640	3.3V transceiver with direction pin; inverting with bus hold (3-state)	DB	D	PW										
74LVT646	3.3V transceiver/register with bus hold (3-state)	DB	D	PW										
74LVT652	3.3V transceiver/register; non-inverting with bus hold (3-state)	DB	D	PW										
74LVT1403 Series	3.3V hex inverter schmitt-trigger			R										
74LVTN16245B	3.3V 16-bit transceiver with direction pin; non-inverting without bus hold (3-state)			DGG	BX									
74LVT_ LVTH16245B	3.3V 16-bit transceiver with direction pin; non-inverting with bus hold (3-state)	DL		DGG	BX									EV
74LVT_ LVTH2245	3.3V transceiver with direction pin; non-inverting with bus hold and 30Ω termination resistors (3-state)	DB	D	PW										
74LVT162373	3.3 V LVT 16-bit transparent D-type latch with 30 Ohm termination resistors (3-State)	DL		DGG										
74LVT16373A	3.3V LVT 16-bit transparent D-type latch (3-State)	DL		DGG										
74LVT162374	3.3 V 16-bit edge-triggered D-type flip-flop with 30 Ohm termination resistors; 3-state	DL		DGG										
74LVT_ LVTH16374A	3.3 V 16-bit edge-triggered D-type flip-flop; 3-state	DL		DGG	BQ									EV
74LVT162240A	3.3 V LVT 16-bit inverting buffer/driver with 30 Ohm termination resistors (3-State)	DL		DGG										
74LVT16240A	3.3 V LVT 16-bit inverting buffer/driver (3-State)	DL		DGG										
74LVT_ LVTH16244B	3.3 V 16-bit buffer/driver; 3-state	DL		DGG	BQ									EV
74LVT162244B	3.3 V 16-bit buffer/driver with 30 Ohm termination resistors	DL		DGG										
74LVTN16244B	3.3 V 16-bit buffer/driver; 3-state			DGG	BQ									
74LVT16543A	3.3V 16-bit latched transceiver; non-inverting with bus hold (3-state)	DL		DGG										
74LVT16646A	3.3V 16-bit transceiver/register with bus hold (3-state)	DL		DGG										
74LVT16652A	3.3V 16-bit transceiver/register; non-inverting with bus hold (3-state)	DL		DGG										
74LVT16500A	3.3V 18-Bit universal bus transceiver; negative edge trigger clock with bus hold (3-state)	DL		DGG										
74LVT16501A	3.3V 18-Bit universal bus transceiver; positive edge trigger clock with bus hold (3-state)	DL		DGG										
74LVTH322245	3.3V 32-bit bus transceiver with 30Ω termination resistors (3-state)													EC
74LVT162245B	3.3V 16-bit transceiver with direction pin; non-inverting with bus hold and 30Ω termination resistors (3-state)	DL		DGG										
74LVTH32245	3.3V 32-bit bus transceiver (3-state)													EC
74LVT32374	3.3 V 32-bit edge-triggered D-type flip-flop; 3-state													EC

＊リストにないパッケージはお問い合わせください。

5V COMOS

AHC ファミリ ：Advanced High-speed CMOS

http://www.nxp.com/products/logic/family/AHC_T/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74AHC_AHCT1G00	2-input NAND gate				GW		GV							
74AHC_AHCT1G02	2-input NOR gate				GW		GV							
74AHC_AHCT1G04	Inverter				GW		GV							
74AHC1GU04	Inverter				GW		GV							
74AHC_AHCT1G06	Inverter with open drain output				GW		GV							
74AHC_AHCT1G07	Buffer with open-drain output				GW		GV							
74AHC_AHCT1G08	2-input AND gate				GW		GV							
74AHC1G09	2-input AND gate with open-drain output				GW		GV							
74AHC_AHCT1G14	5V Single Inverting Schmitt-Trigger; TTL Enabled				GW		GV							
74AHC_AHCT1G125	Bus buffer/line driver; 3-state				GW		GV	GM			GF			
74AHC_AHCT1G126	Bus buffer/line driver; 3-state				GW		GV	GM			GF			
74AHC_AHCT1G32	2-input OR gate				GW		GV							
74AHC_AHCT1G66	Single-pole single-throw analog switch				GW		GV							
74AHC_AHCT1G79	Single D-type flip-flop; positive-edge trigger				GW		GV							
74AHC_AHCT1G86	2-input EXCLUSIVE-OR gate				GW		GV							
74AHC_AHCT2G00	Dual 2-input NAND gate				DP				DC	GD				
74AHC_AHCT2G08	Dual 2-input AND gate				DP				DC	GD				
74AHC_AHCT2G125	Dual buffer/line driver; 3-state				DP				DC	GD				
74AHC_AHCT2G126	Dual buffer/line driver; 3-state				DP				DC	GD				
74AHC_AHCT2G241	Dual buffer/line driver; 3-state				DP				DC	GD				
74AHC_AHCT2G32	Dual 2-input OR gate				DP				DC	GD				
74AHC3GU04	Inverter				DP		GV	GM	DC					
74AHC_AHCT3G04	Inverter				DP			GM	DC	GD				
74AHC_AHCT3G14	5V Triple Inverting Schmitt-Trigger; TTL Enabled				DP				DC	GD				GT
74AHC_AHCT00	Quad 2-input NAND gate			D	PW	BQ								
74AHC_AHCT02	Quad 2-input NOR gate			D	PW	BQ								
74AHC_AHCT04	Hex inverter			D	PW	BQ								
74AHCU04	Hex inverter			D	PW	BQ								
74AHC_AHCT08	Quad 2-input AND gate			D	PW	BQ								
74AHC_AHCT14	5V Hex Inverter Schmitt-Trigger; TTL Enabled			D	PW	BQ								
74AHC_AHCT30	8-input NAND gate			D	PW	BQ								
74AHC_AHCT32	Quad 2-input OR gate			D	PW	BQ								
74AHC_AHCT74	Dual D-type flip-flop with set and reset; positive-edge trigger			D	PW	BQ								
74AHC_AHCT86	Quad 2-input EXCLUSIVE-OR gate			D	PW	BQ								
74AHC_AHCT123A	5V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input; TTL Enabled			D	PW	BQ								
74AHC_AHCT125	Quad buffer/line driver; 3-state			D	PW	BQ								
74AHC_AHCT126	Quad buffer/line driver; 3-state			D	PW	BQ								
74AHC_AHCT132	5V Quad 2-Input NAND Schmitt-Trigger; TTL Enabled			D	PW	BQ								
74AHC_AHCT138	3-to-8 line decoder/demultiplexer; inverting			D	PW	BQ								
74AHC_AHCT139	Dual 2-to-4 line decoder/demultiplexer			D	PW									
74AHC_AHCT157	5V Quad 2-Input Multiplexer; TTL Enabled			D	PW	BQ								
74AHC_AHCT164	5V 8-Bit Serial-In/Parallel-Out Shift Register; TTL Enabled			D	PW	BQ								
74AHC_AHCT240	Octal buffer/line driver; inverting; 3-state			D	PW	BQ								
74AHC_AHCT241	Octal buffer/line driver; 3-state			D	PW	BQ								
74AHC_AHCT244	Octal buffer/line driver; 3-state			D	PW	BQ								
74AHC_AHCT244	5V Buffer/Line Driver; Non-Inverting (3-State)			D	PW	BQ								
74AHC_AHCT245	5V Transceiver with Direction Pin; Non-Inverting; TTL Enabled (3-State)			D	PW	BQ								
74AHC_AHCT257	5V Quad 2-Input Multiplexer; TTL Enabled (3-State)			D	PW	BQ								
74AHC_AHCT259	8-bit addressable latch			D	PW									
74AHC_AHCT273	Octal D-type flip-flop with reset; positive-edge trigger			D	PW	BQ								
74AHC_AHCT373	Octal D-type transparent latch; 3-state			D	PW									
74AHC_AHCT374	Octal D-type flip-flop; positive edge-trigger; 3-state			D	PW									
74AHC_AHCT377	Octal D-type flip-flop with data enable; positive-edge trigger			D	PW									
74AHC_AHCT541	Octal buffer/line driver; 3-state			D	PW	BQ								
74AHC_AHCT573	Octal D-type transparant latch; 3-state			D	PW	BQ								
74AHC_AHCT574	Octal D-type flip-flop; positive edge-trigger; 3-state			D	PW	BQ								
74AHC_AHCT594	5V 8-bit shift register with output register; TTL enabled		DB	D	PW	BQ								
74AHC_AHCT595	5V 8-Bit Shift Register with Output Latches; TTL Enabled (3-State)			D	PW	BQ								

＊リストにないパッケージはお問い合わせください。

5V COMOS

VHCファミリ：Very High-speed CMOS

http://www.nxp.com/products/logic/family/VHC_T/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74VHC_VHCT02	5V quad 2-input NOR gate			D	PW	BQ								
74VHC_VHCT08	Quad 2-input AND gate			D	PW	BQ								
74VHC_VHCT14	5V hex inverting Schmitt trigger			D	PW	BQ								
74VHC_VHCT32	Quad 2-input OR gate			D	PW	BQ								
74VHC_ VHCT125	Quad buffer/line driver; 3-state			D	PW	BQ								
74VHC_ VHCT126	Quad buffer/line driver; 3-state			D	PW	BQ								
74VHC_ VHCT244	Octal buffer/line driver; 3-state			D	PW	BQ								
74VHC_ VHCT245	5V octal bus transceiver with direction pin; non-inverting (3-state)			D	PW	BQ								
74VHC_ VHCT541	Octal buffer/line driver; 3-state			D	PW	BQ								
74VHC_ VHCT595	5V 8-bit serial-in/serial-out or parallel-out shift register with output latches (3-state)			D	PW	BQ								

＊リストにないパッケージはお問い合わせください。

XC7 シリーズ(7SH, 7WH, and 7SET)：LMOS

http://www.nxp.com/products/logic/family/XC7/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
XC7SH02	2-input NOR gate				GW		GV							
XC7SET02	5V single 2-input NOR gate; TTL enabled				GW		GV							
XC7SH04	Inverter				GW		GV							
XC7SET04	Inverter				GW		GV							
XC7SHU04	Inverter				GW		GV							
XC7SH08	2-input AND gate				GW		GV							
XC7SET08	2-input AND gate				GW		GV							
XC7SH14	5V single inverting Schmitt trigger				GW		GV							
XC7SET14	5V single inverting Schmitt trigger; TTL enabled				GW		GV							
XC7WH14	5V triple inverting Schmitt trigger				DP				DC	GD				GT
XC7WT14	5V triple inverting Schmitt trigger; TTL enabled				DP				DC	GD				GT
XC7SH32	2-input OR gate				GW		GV							
XC7SET32	2-input OR gate				GW		GV							
XC7SH86	2-input EXCLUSIVE-OR gate				GW		GV							
XC7SET86	2-input EXCLUSIVE-OR gate				GW		GV							
XC7SH125	Bus buffer/line driver; 3-state				GW		GV	GM			GF			
XC7SET125	Bus buffer/line driver; 3-state				GW		GV	GM			GF			
XC7WH126	Dual buffer_line driver; 3-state				DP				DC	GD				

＊リストにないパッケージはお問い合わせください。

5V COMOS

HCファミリ：High-speed CMOS - 1

http://www.nxp.com/products/logic/family/HC_T/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74HC_HCT1G00	2-input NAND gate				GW		GV							
74HC_HCT1G02	2-input NOR gate				GW		GV							
74HC_HCT1G04	Inverter				GW		GV							
74HC1GU04	Inverter				GW		GV							
74HC_HCT1G08	2-input AND gate				GW		GV							
74HC_ HCT1G125	Bus buffer/line driver; 3-state				GW		GV							
74HC_ HCT1G126	Bus buffer/line driver; 3-state				GW		GV							
74HC_HCT1G32	2-input OR gate				GW		GV							
74HC_HCT1G66	Single-pole single-throw analog switch				GW		GV							
74HC_HCT1G86	2-input EXCLUSIVE-OR gate				GW		GV							
74HC_HCT2G00	Dual 2-input NAND gate				DP			GM	DC	GD				
74HC_HCT2G02	Dual 2-input NOR gate				DP				DC	GD				
74HC_HCT2G04	Dual inverter				GW		GV							
74HC2GU04	Dual unbuffered inverter				GW		GV							
74HC_HCT2G08	Dual 2-input AND gate				DP				DC	GD				
74HC_ HCT2G125	Dual buffer/line driver; 3-state				DP				DC	GD				
74HC_ HCT2G126	Dual buffer/line driver; 3-state				DP				DC	GD				
74HC_HCT2G32	Dual 2-input OR gate				DP				DC	GD				
74HC_HCT2G34	Dual buffer gate				GW		GV							
74HC_HCT2G66	Dual single-pole single-throw analog switch				DP				DC	GD				
74HC_HCT2G86	Dual 2-input exclusive-OR gate				DP				DC	GD				
74HC_HCT3G04	Inverter				DP				DC	GD				
74HC3GU04	Inverter				DP				DC	GD				
74HC_HCT3G06	Triple inverter with open-drain outputs				DP				DC	GD				
74HC_HCT3G07	Triple buffer with open-drain outputs				DP				DC	GD				
74HC_HCT3G34	Triple buffer gate				DP				DC	GD				
74HC_HCT00	Quad 2-input NAND gate	N	DB	D	PW	BQ								
74HC_HCT02	Quad 2-input NOR gate	N	DB	D	PW	BQ								
74HC_HCT03	Quad 2-input NAND gate	N	DB	D	PW									
74HC_HCT04	Hex inverter	N	DB	D	PW	BQ								
74HCU04	Hex inverter	N	DB	D	PW	BQ								
74HC05	Hex inverter with open-drain outputs			D	PW	BQ								
74HC_HCT08	Quad 2-input AND gate	N	DB	D	PW	BQ								
74HC_HCT10	Triple 3-input NAND gate	N	DB	D	PW									
74HC_HCT107	Dual JK flip-flop with reset; negative-edge trigger	N	DB	D	PW									
74HC_HCT109	Dual JK flip-flop with set and reset; positive-edge trigger	N	DB	D	PW									
74HC_HCT11	Triple 3-input AND gate	N	DB	D	PW									
74HC_HCT14	5V Hex Inverter Schmitt-Trigger; TTL Enabled	N	DB	D	PW	BQ								
74HC_HCT112	dual JK flip-flop with set and reset; negative-edge trigger	N	DB	D	PW									
74HC_HCT123	5V Dual Retriggerable Monostable Multivibrator with Reset; Triggerable via Reset Input; TTL Enabled	N	DB	D	PW	BQ								
74HC_HCT125	Quad buffer/line driver; 3-state	N	DB	D	PW									
74HC_HCT126	Quad buffer/line driver; 3-state	N	DB	D	PW									
74HC_HCT138	3-to-8 line decoder, demultiplexer; inverting	N	DB	D	PW	BQ								
74HC_HCT139	Dual 2-to-4 line decoder/demultiplexer	N	DB	D	PW									
74HC_HCT132	5V Quad 2-Input NAND Schmitt-Trigger; TTL Enabled	N	DB	D	PW									
74HC137	3-to-8 line decoder, demultiplexer with address latches; inverting	N	DB	D										
74HC_HCT147	10-to-4 line priority encoder	N	DB	D										
74HC_HCT151	5V 8-input multiplexer; TTL enabled	N	DB	D	PW									
74HC_HCT153	5V Dual 4-Input Multiplexer; TTL Enabled	N	DB	D	PW									
74HC_HCT154	4-to-16 line decoder/demultiplexer	N	DB	D	PW	BQ								
74HC_HCT157	5V Quad 2-Input Multiplexer; TTL Enabled	N	DB	D	PW	BQ								
74HC158	5V Quad 2-Input Multiplexer; Inverting	N		D										
74HC_HCT160	Presettable synchronous BCD decade counter; asynchronous reset	N	DB	D	PW									
74HC_HCT161	Presettable synchronous 4-bit binary counter; asynchronous reset	N	DB	D	PW									
74HC_HCT163	Presettable synchronous 4-bit binary counter; synchronous reset	N	DB	D	PW									
74HC_HCT173	Quad D-type flip-flop; positive-edge trigger; 3-state	N	DB	D	PW									
74HC_HCT174	Hex D-type flip-flop with reset; positive-edge trigger	N	DB	D	PW									
74HC_HCT175	quad D-type flip-flop with reset; positive-edge trigger	N	DB	D	PW									
74HC_HCT191	Presettable synchronous 4-bit binary up/down counter	N	DB	D	PW									
74HC_HCT193	Presettable synchronous 4-bit binary up, down counter	N	DB	D	PW									
74HC_HCT20	Dual 4-input NAND gate	N	DB	D	PW									
74HC21	Dual 4-input AND gate	N	DB	D	PW									
74HC237	3-to-8 line decoder, demultiplexer with address latches	N	DB	D										
74HC_HCT238	3-to-8 line decoder/demultiplexer	N	DB	D	PW	BQ								
74HC_HCT240	Octal buffer/line driver; 3-state; inverting	N	DB	D	PW	BQ								
74HC_HCT241	Octal buffer/line driver; 3-state	N	DB	D	PW									
74HC_HCT244	Octal buffer, line driver; 3-state	N	DB	D	PW	BQ								
74HC_HCT259	8-bit addressable latch	N	DB	D	PW	BQ								
74HC_HCT27	Triple 3-input NOR gate	N	DB	D	PW	BQ								
74HC_HCT273	Octal D-type flip-flop with reset; positive-edge trigger	N	DB	D	PW	BQ								
74HC_HCT30	8-input NAND gate	N	DB	D	PW									
74HC_HCT32	Quad 2-input OR gate	N	DB	D	PW	BQ								

＊リストにないパッケージはお問い合わせください。

5V COMOS

HCファミリ：High-speed CMOS-2

http://www.nxp.com/products/logic/family/HC_T/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74HC_HCT365	Hex buffer/line driver; 3-state	N	DB	D	PW									
74HC_HCT366	Hex buffer/line driver; 3-state; inverting	N	DB	D	PW									
74HC_HCT367	Hex buffer/line driver; 3-state	N	DB	D	PW									
74HC_HCT368	Hex buffer/line driver; 3-state; inverting	N	DB	D	PW									
74HC_HCT373	Octal D-type transparent latch; 3-state	N	DB	D	PW	BQ								
74HC_HCT374	Octal D-type flip-flop; positive edge-trigger; 3-state	N	DB	D	PW									
74HC_HCT377	Octal D-type flip-flop with data enable; positive-edge trigger	N	DB	D	PW									
74HC283	4-bit binary full adder with fast carry	N	DB	D	PW									
74HC_HCT390	Dual decade ripple counter	N	DB	D	PW									
74HC_HCT393	Dual 4-bit binary ripple counter	N	DB	D	PW	BQ								
74HC_HCT4002	Dual 4-input NOR gate	N	DB	D	PW	BQ								
74HC_HCT40105	4-bit x 16-word FIFO register	N	DB	D	PW									
74HC_HCT4016	Quad bilateral switches	N		D	PW									
74HC_HCT4017	Johnson decade counter with 10 decoded outputs	N	DB	D	PW	BQ								
74HC_HCT4020	14-stage binary ripple counter	N	DB	D	PW	BQ								
74HC_HCT4040	12-stage binary ripple counter	N	DB	D	PW	BQ								
74HC_HCT4051	8-channel analog multiplexer/demultiplexer	N	DB	D	PW	BQ								
74HC_HCT4052	Dual 4-channel analog multiplexer/demultiplexer	N	DB	D	PW									
74HC_HCT4053	Triple 2-channel analog multiplexer/demultiplexer	N	DB	D	PW	BQ								
74HC_HCT4059	programmable divide-by-n counter	N	DB	D										
74HC_HCT4060	14-stage binary ripple counter with oscillator	N	DB	D	PW	BQ								
74HC_HCT4066	Quad bilateral switches	N	DB	D	PW	BQ								
74HC_HCT4067	16-channel analog multiplexer/demultiplexer	N	DB	D	PW	BQ								
74HC_HCT4075	Triple 3-input OR gate	N	DB	D	PW									
74HC_HCT42	BCD to decimal decoder (1-of-10)	N		D										
74HC_HCT4316	Quad bilateral switches	N	DB	D	PW	BQ								
74HC_HCT4351	8-channel analog multiplexer/demultiplexer with latch	N	DB	D										
74HC_HCT4353	Triple 2-channel analog multiplexer/demultiplexer with latch	N		D										
74HC_HCT4511	BCD to 7-segment latch/decoder/driver	N		D										
74HC_HCT4514	4-to-16 line decoder/demultiplexer with input latches	N	DB	D	PW	BQ								
74HC_HCT4515	4-to-16 line decoder/demultiplexer with input latches; inverting	N		D										
74HC_HCT4520	Dual 4-bit synchronous binary counter	N	DB	D	PW									
74HC_HCT4851	8-channel analog multiplexer/demultiplexer with injection-current effect control			D	PW	BQ								
74HC_HCT4852	Dual 4-channel analog multiplexer/demultiplexer with injection-current effect control			D	PW	BQ								
74HC_HCT540	Octal buffer/line driver; 3-state; inverting	N	DB	D										
74HC_HCT541	Octal buffer/line driver; 3-state	N	DB	D	PW									
74HC_HCT563	Octal D-type transparent latch; 3-state; inverting	N	DB	D										
74HC_HCT573	Octal D-type transparent latch; 3-state	N	DB	D	PW	BQ								
74HC_HCT574	Octal D-type flip-flop; positive edge-trigger; 3-state	N	DB	D	PW									
74HC_HCT688	8-bit magnitude comparator	N	DB	D	PW									
74HC_HCT7030	9-bit x 64-word FIFO register; 3-state	N		D										
74HC_HCT74	Dual D-type flip-flop with set and reset; positive-edge trigger	N	DB	D	PW	BQ								
74HC_HCT7403	4-Bit x 64-word FIFO register; 3-state	N		D										
74HC_HCT85	4-bit magnitude comparator	N	DB	D	PW									
74HC_HCT86	Quad 2-input EXCLUSIVE-OR gate	N	DB	D	PW									
74HC_HCT93	4-bit binary ripple counter	N	DB	D	PW									
74HC_HCT93	4-bit binary ripple counter	N	DB	D										
74HC40103	8-bit synchronous binary down counter	N	DB	D	PW									
74HC4024	7-stage binary ripple counter	N	DB	D	PW									
74HC4049	Hex inverting HIGH-to-LOW level shifter	N	DB	D	PW									
74HC4050	Hex high-to-low level shifter	N	DB	D	PW									
74HC564	Octal D-type flip-flop; positive-edge trigger; 3-state; inverting	N		D										
74HC58	Dual AND-OR gate	N	DB	D										
74HC590	8-bit binary counter with output register; 3-state	N		D	PW	BQ								
74HC7266	Quad 2-input EXCLUSIVE-NOR gate	N	DB	D										
74HC73	Dual JK flip-flop with reset; negative-edge trigger	N	DB	D	PW									
74HC75	Quad bistable transparent latch	N	DB	D	PW									
74HCT534	5 V octal D-type flip-flop; positive-edge trigger; 3-state; inverting	N		D										
74HCT7273	Octal D-type flip-flop with reset; positive edge-trigger; open drain outputs	N		D										
74HC_HCT6323A	Programmable ripple counter with oscillator; 3-state			D										

＊リストにないパッケージはお問い合わせください。

5V COMOS

HEF4000 シリーズ

http://www.nxp.com/products/logic/family/HEF4000/#products

NXP 品名	機能名	パッケージ												
		DIP (P)	SSOP (TS)	SOP (T)	TSSOP (TT)	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
HEF4000B	Dual 3-input NOR gate and inverter	P		T										
HEF4001B	Quad 2-input NOR gate	P		T										
HEF4002B	Dual 4-input NOR gate	P		T										
HEF4007UB	Dual complementary pair and inverter	P		T										
HEF40098B	Hex inverting buffer; 3-state	P		T										
HEF4011B	Quadruple 2-input NAND gate	P		T										
HEF4011UB	Quadruple 2-input NAND gate	P		T										
HEF4013B	Dual D-type flip-flop	P		T	TT									
HEF4014B	8-Bit Static Shift Register with Synchronous Parallel Enable Input	P		T										
HEF4015B	Dual 4-Bit Serial-In/Parallel-Out Shift Register	P		T										
HEF4016B	Quadruple bilateral switches	P		T										
HEF40174B	Hex D-type flip-flop	P		T										
HEF40175B	Quad D-type flip-flop	P		T	TT									
HEF4017B	5-stage Johnson decade counter	P		T										
HEF40193B	4-bit up/down binary counter	P		T										
HEF4020B	14-stage binary counter	P		T										
HEF4021B	8-Bit Static Shift Register with Asynchronous Parallel Load Input	P		T										
HEF4023B	Triple 3-input NAND gate	P		T										
HEF4024B	7-stage binary counter	P		T										
HEF4025B	Triple 3-input NOR gate	P		T										
HEF4027B	Dual JK flip-flop	P		T										
HEF4028B	BCD to decimal decoder	P		T										
HEF4030B	Quadruple exclusive-OR gate	P		T										
HEF4040B	12-stage binary ripple counter	P		T										
HEF4043B	Quad R/S latch with 3-state outputs	P		T										
HEF4044B	Quad R/S latch with 3-state outputs	P		T										
HEF4046B	PLL with VCO	P		T										
HEF4047B	Monostable/Astable Multivibrator	P		T										
HEF4049B	Hex inverting buffers	P		T										
HEF4050B	Hex non-inverting buffers	P		T										
HEF4051B	8-channel analog multiplexer/demultiplexer	P	TS	T	TT									
HEF4052B	Dual 4-channel analog multiplexer/demultiplexer	P		T	TT									
HEF4053B	Triple single-pole double-throw analog switch	P		T										
HEF4059B	Programmable divide-by-n counter	P		T										
HEF4060B	14-stage ripple-carry binary counter/divider and oscillator	P		T										
HEF4060B	14-stage ripple-carry binary counter/divider and oscillator			T										
HEF4066B	Quad single-pole single-throw analog switch	P		T										
HEF4067B	16-channel analog multiplexer/demultiplexer	P		T										
HEF4068B	8-input NAND gate	P		T										
HEF4069UB	Hex inverter	P		T	TT									
HEF4070B	Quadruple exclusive-OR gate	P		T										
HEF4071B	Quad 2-input OR gate	P		T										
HEF4072B	Dual 4-input OR gate	P		T										
HEF4073B	Triple 3-input AND gate	P		T										
HEF4075B	Triple 3-input OR gate	P		T										
HEF4077B	Quadruple EXCLUSIVE-NOR gate	P		T										
HEF4081B	Quad 2-input AND gate	P		T										
HEF4082B	Dual 4-input AND gate	P		T										
HEF4093B	Quad 2-Input NAND Schmitt-Trigger	P		T										
HEF4094B	8-Stage Shift-and-Store Bus Register	P	TS	T										
HEF4104B	Quad low-to-high voltage translator with 3-state outputs	P		T										
HEF40240B	HEF40240B_4	P		T										
HEF40244B	Octal buffers with 3-state outputs	P		T										
HEF40373B	Octal transparent latch with 3-state outputs	P		T										
HEF40374B	Octal D-type flip-flop with 3-state outputs	P		T										
HEF4511B	BCD to 7-segment latch/decoder/driver	P		T										
HEF4514B	1-of-16 decoder/demultiplexer with input latches	P		T										
HEF4515B	1-of-16 decoder/demultiplexer with input latches	P		T										
HEF4516B	Binary up/down counter	P		T										
HEF4517B	Dual 64-Bit Static Shift Register	P		T										
HEF4518B	Dual BCD counter	P		T										
HEF4520B	Dual binary counter	P		T										
HEF4521B	24-stage frequency divider and oscillator	P		T										
HEF4526B	Programmable 4-bit binary down counter	P		T										
HEF4538B	Dual Retriggerable Precision Monostable Multivibrator	P		T										
HEF4541B	Programmable Timer	P		T										
HEF4543B	BCD to 7-segment latch/decoder/driver	P		T										
HEF4555B	1-of-4 decoder/demultiplexer	P		T										
HEF4557B	1-to-64 Bit Variable Length Shift Register	P		T										
HEF4585B	4-bit magnitude comparator	P		T										
HEF4794B	8-Stage Shift-and-Store Register LED Driver	P		T										
HEF4894B	12-Stage Shift-and-Store Register LED Driver	P		T	TT									
HEF4938B	Dual Retriggerable Precision Monostable Multivibrator with Reset			T										
HEF4952B	Dual 3-channel analog multiplexer/demultiplexer with supplementary switches			T										
HEF40106B	Hex Inverting Schmitt-Trigger	P		T	TT									

＊リストにないパッケージはお問い合わせください。

5V Bi CMOSロジック

ABTファミリ：（Advanced BiCMOS Technology）

http://www.nxp.com/products/logic/family/ABT/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DL/DB)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74ABT00	Quad 2-input NAND gate	N	DB	D	PW									
74ABT02	Quad 2-input NOR gate	N	DB	D	PW									
74ABT04	Hex inverter	N	DB	D	PW									
74ABT08	Quad 2-input AND gate	N	DB	D	PW									
74ABT20	Dual 4-input NAND gate	N	DB	D	PW									
74ABT32	Quad 2-input OR gate	N	DB	D	PW									
74ABT74	Dual D-type flip-flop	N	DB	D	PW									
74ABT125	Quad buffer; 3-state	N	DB	D	PW	BQ								
74ABT126	Quad buffer; 3-state		DB	D	PW									
74ABT2240	Octal inverting buffer with 30 Ohm series termination resistors; 3-state	N		D	PW									
74ABT2244	Octal buffer/line driver with 30 Ohm series termination resistors (3-State)	N	DB	D	PW									
74ABT240	Octal inverting buffer (3-State)	N	DB	D	PW									
74ABT241	Octal buffer/line driver (3-State)	N	DB	D	PW									
74ABT244	Octal buffer/line driver (3-State)	N	DB	D	PW									
74ABT245	5V Transceiver with Direction Pin; Non-Inverting (3-State)		DB	D	PW									
74ABT273A	Octal D-type flip-flop	N	DB	D	PW									
74ABT373A	Octal transparent latch (3-State)	N	DB	D	PW									
74ABT374A	Octal D-type flip-flop; positive-edge trigger (3-State)	N	DB	D	PW									
74ABT377A	Octal D-type flip-flop with enable	N	DB	D	PW									
74ABT540	Octal buffer, inverting (3-State)	N	DB	D	PW									
74ABT541	Octal buffer/line driver (3-State)	N	DB	D	PW									
74ABT543A	5V Latched Transceiver; Non-Inverting (3-State)		DB	D	PW									
74ABT544	5V Latched Transceiver; Inverting (3-State)		DB	D	PW									
74ABT573A	Octal D-type transparent latch (3-State)	N	DB	D	PW									
74ABT574A	Octal D-type flip-flop (3-State)	N	DB	D	PW									
74ABT620	5V Transceiver with Dual Enable; Inverting (3-State)	N	DB	D	PW									
74ABT623	5V Transceiver with Dual Enable; Non-Inverting (3-State)		DB	D	PW									
74ABT640	5V Transceiver with Direction Pin; Inverting (3-State)	N	DB	D	PW									
74ABT646A	5V Transceiver/Register (3-State)		DB	D	PW									
74ABT648	5V Octal Transceiver/Register; Inverting (3-State)			D	PW									
74ABT651	5V Octal Transceiver/Register; Inverting (3-State)	N		D	PW									
74ABT652A	5V Transceiver/Register; Non-Inverting (3-State)		DB	D	PW									
74ABT657	5V Octal Transceiver with Parity Generator/Checker; Non-Inverting (3-State)		DB	D	PW									
74ABT821	10-bit D-type flip-flop; positive-edge trigger; 3-state		DB	D	PW									
74ABT823	9-bit D-type flip-flop with reset and enable; 3-state		DB	D	PW									
74ABT827	10-bit buffer/line driver; non-inverting; 3-state		DB	D	PW									
74ABT841	10-bit bus interface latch; 3-state		DB	D	PW									
74ABT899	5V Latched Transceiver with Parity Generator/Checker (3-State)		DB	D										
74ABT2241	5V Octal Buffer/Line Driver; Non-Inverting with 30 Ohm Termination Resistors (3-State)	N	DB	D	PW									
74ABT2245	5V Transceiver with Direction Pin; Non-Inverting with 30 ohm Termination Resistors (3-State)		DB	D	PW									
74ABT16240A	16-bit inverting buffer/line driver; 3-state		DL		DGG									
74ABT162244	16-bit buffer/line driver with 30 Ohm series termination resistors; 3-state		DL		DGG									
74ABT16244A	16-bit buffer/line driver; 3-state		DL		DGG									
74ABT_ H162245A	5V 16-Bit Transceiver with Direction Pin; Non-Inverting with Bus Hold and 30 Ohm Termination Resistors (3-State)		DL		DGG									
74ABT_ H162245A	5V 16-Bit Transceiver with Direction Pin; Non-Inverting with 30 Ohm Termination Resistors (3-State)		DL		DGG									
74ABT_ H16245B	5V 16-Bit Transceiver with Direction Pin; Non-Inverting (3-State)		DL		DGG									
74ABT16273	16-bit D-type flip-flop		DL		DGG									
74ABT16373B	16-bit transparent latch (3-State)		DL		DGG									
74ABT16374B	16-bit D-type flip-flop, positive-edge trigger (3-State)		DL		DGG									
74ABT16821A	20-bit bus-interface D-type flip-flop; positive-edge trigger; 3-state		DL		DGG									
74ABT16823A	18-bit bus interface D-type flip-flop with reset and enable (3-State)		DL		DGG									
74ABT16841A	20-bit bus interface latch (3-State)		DL		DGG									

＊リストにないパッケージはお問い合わせください。

Bipolar ロジック

FASTファミリ：FAST Advanced Schottky TTL

http://www.nxp.com/products/logic/family/FAST/#products

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DB)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74F00 Series	Quad 2-input NAND gate	N		D										
74F02 Series	Quad 2-input NOR gate	N		D										
74F04 Series	Hex inverter	N		D										
74F06 Series	Inverter/buffer drivers	N		D										
74F07 Series	Inverter/buffer drivers	N		D										
74F08 Series	Quad two-input AND gate	N		D										
74F10_11 Series	Triple 3-input NAND gate; Triple 3-input AND gate	N		D										
74F109 Series	Positive J-Knot positive edge-triggered flip-flops	N		D										
74F112 Series	Dual J-K negative edge-triggered flip-flop	N		D										
74F125_126 Series	Quad buffers (3-State)	N		D										
74F132	5V Quad 2-Input NAND Schmitt-Trigger	N		D										
74F133 Series	13-input NAND gate	N		D										
74F138 Series	1-of-8 decoder/demultiplexer	N		D										
74F139 Series	Dual 1-of-4 decoder/demultiplexer	N		D										
74F14	5V Hex Inverter Schmitt-Trigger	N		D										
74F153	5V Dual 4-Input Multiplexer	N		D										
74F157A_158A	5V Quad 2-Input Multiplexer; Inverting	N		D										
74F161A_163A Series	4-bit binary counter	N		D										
74F164	5V 8-Bit Serial-In/Parallel-Out Shift Register	N		D										
74F166	5V 8-Bit Bidirectional Universal Shift Register	N		D										
74F174	5V hex D-type flip-flop with reset; positive-edge trigger	N		D										
74F194	5V 4-Bit Bidirectional Universal Shift Register	N		D										
74F20 Series	Dual 4-input NAND gate	N		D										
74F240 Series	Octal inverting buffer (3-state)	N	DB	D										
74F241 Series	Octal buffer (3-state)	N		D										
74F242_243	5V Quad Transceiver; Inverting (3-State)	N		D										
74F244_244B Series	Octal buffers (3-State)	N	DB	D										
74F245	5V Transceiver with Direction Pin; Non-Inverting (3-State)	N	DB	D										
74F253	5V Dual 4-Input Multiplexer (3-State)	N		D										
74F257A	5V Quad 2-Input Multiplexer (3-State)	N		D										
74F258A	5V Quad 2-Input Multiplexer; Inverting (3-State)	N		D										
74F259 Series	Latch	N		D										
74F260 Series	Dual 5-input NOR gate	N		D										
74F269 Series	8-bit bidirectional binary counter		DB	D										
74F27 Series	Triple 3-input NOR gate	N		D										
74F273A Series	Octal D flip-flop	N		D										
74F280B	5V 9-Bit Odd/Even Parity Generator/Checker	N		D										
74F283 Series	4-bit binary full adder with fast carry	N		D										
74F298	5V Quad 2-Input Multiplexer with Storage	N		D										
74F299	5V 8-Bit Universal Shift/Store Register (3-State)	N		D										
74F30 Series	8-input NAND gate	N		D										
74F3037 Series	Quad 2-input NAND 30Ohm driver	N		D										
74F3038 Series	Quad 2-input NAND 30Ohm driver (open collector)	N		D										
74F32 Series	Quad 2-input OR gate	N		D										
74F367 Series	Hex buffer/driver	N		D										
74F373_374 Series	Octal transparent latch (3-State); Octal D flip-flop (3-State)		DB											
74F38 Series	Quad 2-input NAND buffer (open collector)	N		D										
74F399	5V Quad 2-Port Register	N		D										
74F5074 Series	Synchronizing dual D-type flip-flop/clock driver	N		D										
74F51 Series	Dual 2-wide 2-input, 2-wise 3-input AND-OR-invert gate	N		D										
74F521	5V 8-Bit Identity Comparator	N		D										
74F534 Series	Octal D flip-flop, inverting (3-State)	N		D										
74F540_541 Series	Octal inverter buffer (3- State); Octal buffer (3- State)	N		D										
74F543	5V Latched Transceiver; Non-Inverting (3-State)		DB	D										
74F573_574 Series	Octal transparent latch (3-State); Octal transparent latch (3-State)	N	DB	D										
74F579	5V 8-Bit Bidirectional Binary Counter; 20-Pin (3-State)	N		D										
74F656A Series	Octal buffer/driver with parity; non-inverting; 3-state			D										
74F74 Series	Dual D-type flip-flop	N		D										
74F786 Series	4-bit asynchronous bus arbiter	N		D										
74F827 Series	10-bit buffer/line driver; non-inverting; 3-state		DB	D										
74F86 Series	Quad 2-input exclusive-OR gate	N		D										
74F1244	5V Buffer/Light Load F244 (3-State)	N		D										

＊リストにないパッケージはお問い合わせください。

NXP 品名	機能名	パッケージ												
		DIP (N)	SSOP (DS/DB/DK/ DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74CBTLV1G125	Single bus switch				GW		GV	GM			GF	GN	GS	
74CBTLV3125	4-bit bus switch		DS		PW	BQ								
74CBTLV3126	4-bit bus switch		DS		PW	BQ								
74CBTLV3244	8-bit bus switch with 4-bit output enables		DS		PW	BQ								
74CBTLV3245	8-bit bus switch with output enable		DS		PW	BQ								
74CBTLV3384	10-bit bus switch with 5-bit output enables		DK		PW	BQ								
74CBTLV3861	10-bit bus switch with output enable		DK		PW	BQ								
74CBTLVD3861	10-bit level-shifting bus switch with output enable		DK		PW	BQ								
74CBTLV3253	Low-Volt Dual 1-of-4 5Ω Multiplexer/Demultiplexer with Output Enables		DS	D	PW	BQ								
74CBTLV3257	Low-Volt Quad 1-of-2 5Ω Multiplexer/Demultiplexer with Single Output Enable		DS	D	PW	BQ								
74CBTLVD3244	8-bit level-shifting bus switch with 4-bit output enables		DS		PW	BQ								
74CBTLVD3245	8-bit level-shifting bus switch with output enable		DS		PW	BQ								
74CBTLVD3384	10-bit level-shifting bus switch with 5-bit output enables				PW	BQ BX								
CBT3125	Quadruple FET bus switch		DS (SSOP16) DB (SSOP14)	D	PW									
CBT3126	Quad FET bus switch		DS (SSOP16) DB (SSOP14)	D	PW									
CBT3244A	Octal bus switch with quad output enables		DS (SSOP16) DB (SSOP14)	D	PW	BQ								
CBT3245A	Octal bus switch		DS (SSOP16) DB (SSOP14)	D	PW	BQ								
CBT3253A	Dual 1-of-4 FET multiplexer/demultiplexer		DS (SSOP16) DB (SSOP14)	D	PW									
CBT3257A	Quad 1-of-2 multiplexer/demultiplexer		DS (SSOP16) DB (SSOP14)	D	PW	BQ								
CBT3306	Dual bus switch			D	PW			GM						GT
CBTD3306	Dual bus switch with level shifting			D	PW			GM						GT
CBT3251	1-of-8 FET multiplexer/demultiplexer		DS (SSOP16) DB (SSOP16)	D	PW									
CBT3384	10-bit bus switch with 5-bit output enables		DK (SSOP24) DB (SSOP24)	D	PW									
CBT3861	10-bit bus switch with output enable		DK (SSOP24)		PW	BQ								
CBT6810	10-bit bus switch with precharged outputs and Schottky undershoot protection for live insertion		DK (SSOP24)		PW									
CBTD3384	10-bit level shifting bus switch with 5-bit output enables		DK DB	D	PW									
CBTD3861	10-bit level shifting bus switch with output enable		DK		PW	BQ								
CBT16292	12-bit 1-of-2 multiplexer/demultiplexer				DGG									
CBT6832E	16-bit controlled enable rate 1-of-2 multiplexer/demultiplexer with precharged outputs and Schottky undershoot protection for live insertion				DGG									
CBT6820	20-bit bus switch with precharged outputs and Schottky undershoot protection for live insertion				DGG									
CBT16210	20-bit bus switch with 10-bit output enables		DL		DGG									
CBTD16210	20-bit level shifting bus switch with 10-bit output enables		DL		DGG									
74CBTLV16211	24-bit bus switch				DGG DGV									
CBTD16211	24-bit level shifting bus exchange switch with 12-bit output enables		DL		DGG									
CBT16211	24-bit bus switch with 12-bit output enables		DL		DGG									
CBT16212	24-bit bus exchange switch with 12-bit output enables		DL		DGG									

＊リストにないパッケージはお問い合わせください。

アナログスイッチ

NX3L（V） アナログスイッチ：NXP switch

NXP 品名	機能名	パッケージ												
		SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GT)	BGA (EC/EV/UK)	HXSON (GU)	HVSON (TK)
NX3L1G384	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX3L1G53	Low-ohmic single-pole double-throw analog switch					GM			GD					
NX3L1G66	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX3L1T3157	Low-ohmic single-pole double-throw analog switch			GW		GM								
NX3L1T384	low-ohmic single-pole single-throw analog switch			GW		GM								
NX3L1T5157	Low-ohmic single-pole double-throw analog switch					GM								
NX3L1T53	Low-ohmic single-pole double-throw analog switch					GM			GD		GT			
NX3L1T66	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX3L2267	Low-ohmic dual single-pole double-throw analog switch					GM							GU	
NX3L2467	Dual low-ohmic double-pole double-throw analog switch			PW	HR								GU	
NX3L2G384	Dual low-ohmic single-pole single-throw analog switch					GM			GD		GT			
NX3L2G66	Dual low-ohmic single-pole single-throw analog switch					GM			GD		GT			
NX3L2T384	Dual low-ohmic single-pole double-throw analog switch					GM			GD		GT			
NX3L2T66	Dual low-ohmic single-pole single-throw analog switch					GM			GD		GT			
NX3L4051	Single low-ohmic 8-channel analog switch			PW	HR									
NX3L4053	Triple low-ohmic single-pole double-throw analog switch			PW	HR									
NX3L4357	Low-ohmic single-pole triple-throw analog switch with enable input					GM								
NX3L4684	Dual low-ohmic single-pole double-throw analog switch					GM								TK
NX3V1G384	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX3V1G66	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX3V1T384	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX3V1T66	Low-ohmic single-pole single-throw analog switch			GW		GM								
NX5DV330	Quad 1-of-2 video multiplexer/demultiplexer	DS	D	PW										
NX5DV715	Dual supply 1-of-2 VGA switch				HF									
NXB0102	Dual supply translating transceiver; auto direction sensing; 3-state			DP					GD	GF	GT			
NXS0102	Dual supply translating transceiver; open drain; auto direction sensing			DP					GD	GF	GT			
NX3DV221	High-speed USB 2.0 switch with enable (DPDT-CO)						GM							
NX3DV2567	Single low-voltage, 0.5Ω low-ohmic, four-pole, double-throw analog switch (4PDT)												GU	HR
NX3DV3899	Dual low-voltage double-pole, double-throw analog switch (2 X DPDT)												GU	HR
NX3DV42	Dual high-speed USB 2.0 double-pole double-throw analog switch						GM						GU	
NX3L1G3157	Single low-voltage 0.5Ω low-ohmic, single-pole, double-throw analog switch (SPDT)			GW			GM							
NX3L2267S	Dual low-voltage 0.5Ω low-ohmic, single-pole, double-throw analog switch (2 X SPDT) with termination resistors												GU	
NX3P190	Single 95mΩ single-pole, single-throw high-side power switch (SPST-NO) with internal P-channel MOSFET											UK		
NX3P191	Single 95mΩ single-pole, single-throw high-side power switch (SPST-NO) with internal P-channel MOSFET											UK		
NX5DV4885E	Dual supply 1-of-2 VGA switch													HF
NX5DV713	Dual supply 1-of-2 VGA switch													HF
NX5DV713E	Dual supply 1-of-2 VGA switch													HF

＊リストにないパッケージはお問い合わせください。

汎用アナログスイッチ

NXP 品名	機能名	スイッチ構成	パッケージ											
			SSOP (DB/TS)	SOP (D/T)	TSSOP (PW/ TI) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)
74LVC1G53	2-channel analog multiplexer/demultiplexer	1x SPDT			DP			GM	DC	GD	GF	GN	GS	GT
74LVC2G53	2-channel analog multiplexer/demultiplexer	1x SPDT			DP			GM	DC	GD	GF	GN	GS	GT
74AHC_ AHCT1G66	Single-pole single-throw analog switch	1x SPST-NO			GW		GV							
74HC_HCT1G66	Single-pole single-throw analog switch	1x SPST-NO			GW		GV							
74LVC1G66	Single 3.3V single-pole, single-throw bilateral analog switch (SPST-NO)	1xSPST-NO			GW		GV	GM			GF	GN		
74LVC2G66	2-channel analog multiplexer/demultiplexer	2x SPST-NO			DP			GM	DC	GD				GT
74HC_HCT2G66	Dual single-pole single-throw analog switch	2x SPST-NO			DP				DC	GD				
74LVCV2G66	Overvoltage tolerant bilateral switch	2x SPST-NO			DP				DC	GD				
74LVC1G384	Single 3.3V single-pole, single-throw analog switch (SPST-NC)	1 x SPST-NC						GM			GF	GN	GS	
74LVC1G3157	2-channel analog multiplexer/demultiplexer	1 x SPDT			GW		GV	GM			GF	GN	GS	
74HC_HCT4016	Quad 5V single-pole, single-throw bilateral analog switch	4 X SPST-NO		D	PW									
HEF4016B	Quadruple bilateral switches	4 X SPST-NO		T										
74HC_HCT4051	8-channel analog multiplexer/demultiplexer	SP8T	DB	D	PW	BQ								
HEF4051B	8-channel analog multiplexer/demultiplexer	SP8T	TS	T	TT									
74LV4051	8-channel analog multiplexer/demultiplexer	SP8T	DB	D	PW	BQ								
74HC_HCT4052	Dual 4-channel analog multiplexer/demultiplexer	DP4T	DB	D	PW									
HEF4052B	Dual 4-channel analog multiplexer/demultiplexer	DP4T		T	TT									
74LV4052	Dual 4-channel analog multiplexer/demultiplexer	DP4T	DB	D	PW									
74HC_HCT4053	Triple 2-channel analog multiplexer/demultiplexer	3 X SPDT	DB	D	PW	BQ								
HEF4053B	Triple single-pole double-throw analog switch	3 X SPDT		T										
74LV4053	Triple 2-channel analog multiplexer/demultiplexer	3 X SPDT	DB	D	PW	BQ								
74LVC4066	Quad 3.3V single-pole, single-throw bilateral analog switch (4 X SPST-NO)	4 X SPST-NO			DP		GM	DC	GD	GF		GS		GT
HEF4066B	Quad single-pole single-throw analog switch	4 X SPST-NO		T										
74LV4066	Quad 3.3V low-ohmic, single-pole, single-throw bilateral analog switch (4 X SPST-NO)	4 X SPST-NO	DB	D	PW									
74HC_HCT4066	Quad 5V low-ohmic, single-pole, single-throw bilateral analog switch (4 X SPST-NO); TTL enabled	4 X SPST-NO	DB	D	PW	BQ								
74HC_HCT4067	16-channel analog multiplexer/demultiplexer	SP16T)	DB	D	PW	BQ								
HEF4067B	16-channel analog multiplexer/demultiplexer	SP16T)		T										
74HC_HCT4316	Quad 5V single-throw, single-throw analog switch (4 X SPST-NO) with logic level translation	4 X SPST-NO	DB	D	PW	BQ								
74HC_HCT4351	8-channel analog multiplexer/demultiplexer with latch	SP8T	DB	D										
74HC_HCT4353	Triple 2-channel analog multiplexer/demultiplexer with latch	3 X SPDT-CO		D										
74HC_HCT4851	8-channel analog multiplexer/demultiplexer with injection-current effect control	SP8T		D	PW	BQ								
74HC_HCT4852	Dual 4-channel analog multiplexer/demultiplexer with injection-current effect control	DP4T		D	PW	BQ								
HEF4952B	Dual 3-channel analog multiplexer/demultiplexer with supplementary switches	SP8T		T										

＊リストにないパッケージはお問い合わせください。

レベルシフタ

NTシリーズNXP Translators

http://www.nxp.com/products/logic/family/NT/#products

NXP 品名	機能名	パッケージ												
		SSOP (DL)	SOP (D)	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	DQFN (BQ)	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)	BGA (EC/EV/UK)	HXSON (GU)
NTB0101	Dual supply translating transceiver; auto direction sensing; 3-state			GW		GV	GM		GF		GS			
NTB0102	Dual supply translating transceiver; auto direction sensing; 3-state								GF					
NTB0104	Dual supply translating transceiver; auto direction sensing; 3-state		D	PW	BQ									GU
NTS0101	Dual supply translating transceiver; open drain; auto direction sensing			GW		GV	GM		GF		GS			
NTS0102	Dual supply translating transceiver; open drain; auto direction sensing			DP				GD	GF			GT		GU
NTS0104	Dual supply translating transceiver; open drain; auto direction sensing		D	PW	BQ									GU

＊リストにないパッケージはお問い合わせください。

コンパレータ

NCXNXP Comparators NC

http://www.nxp.com/products/logic/family/NC/

NXP 品名	機能名	パッケージ												
		出力構成	回路数	TSSOP (PW/ DGG) SOT353 SOT363 (GW) SC-70互換	SOT753 SOT467 (GV) SOT-23 互換	MicroPAK (GM)	VSSOP (DC) US-8互換	VSSOP 同等パッケージ (GD:リードレス)	MicroPAK (GF)	MicroPAK (GN)	MicroPAK (GS)	MicroPAK (GT)	BGA (EC/EV/UK)	HXSON (GU)
NCX2200	Single low voltage comparator	push pull	1	GW		GM			GF					
NCX2202	Single low voltage comparator with open-drain output	open drain	1	GW		GM								
NCX2220	Dual low voltage comparator	push pull	2			GM			GF			GT		GU
NCX2222	Dual low voltage comparator with open-drain output	open drain	2											

＊リストにないパッケージはお問い合わせください。

会社概要

NXPセミコンダクターズについて

NXPセミコンダクターズ N.V.（Nasdaq: NXPI）は、RF（無線）、アナログ、パワーマネジメント、インターフェース、セキュリティ、デジタル処理など、業界をリードする専門技術を活用して、ハイパフォーマンス・ミックスドシグナルとスタンダード製品のソリューションを製造・販売しています。これらのソリューションは自動車、ID認証、無線インフラ、照明、製造、携帯電話、民生、コンピュータといった、幅広い用途に利用されています。

本社所在地……オランダ、アイントホーヘン

社長兼CEO……Rick Clemmer

設立……2006年（旧Philips半導体事業部から分社してNXPに）
半導体分野で50年以上の経験と実績
日本でも30年以上に渡る半導体ビジネスの経験と実績
2011年8月　米NASDAQ市場に上場
（銘柄コード: NXPI）

2011年度売上……42億ドル（4200億円）

研究開発……投資　年間約5.5億ドル
3,200人の技術者
4,000件を超える特許（出願中案件も含め11,000件）
19の研究開発拠点

拠点……世界25ヵ国以上

事業領域……
- ハイパフォーマンス・ミックスドシグナル（HPMS）
- スタンダード製品

主要アプリケーション……
- オートモーティブ（車載）
- アイデンティフィケーション（ID認証）
- 無線インフラ
- 産業用機器
- 民生機器
- 照明
- モバイル
- コンピューティング

NXPの世界的なNFC（近距離無線通信）技術が、2011年5月、ニューヨークで発表されたGoogle Wallet（モバイルトランザクションを実現するオープンフラットフォーム）に採用されています。

半導体製品情報を「携帯」できる注目の無償iPhone アプリ「NXP」日本語対応版提供中！
製品のデータシートの閲覧やスペックのチェックはもちろん、注文もできます。
ダウンロードはこちらから
http://itunes.apple.com/jp/app/nxp/id383827083/

Android向けのNXP Androidアプリケーション日本語対応版も提供中。
ダウンロードはこちらから
https://play.google.com/store/apps/details?id=com.nxp.android.app

日本法人：NXP セミコンダクターズジャパン株式会社

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