



# i.MX 94 Applications Processor — Safe and Secure Industrial and Automotive Connectivity with Real-Time Control

**i.MX94** NEW

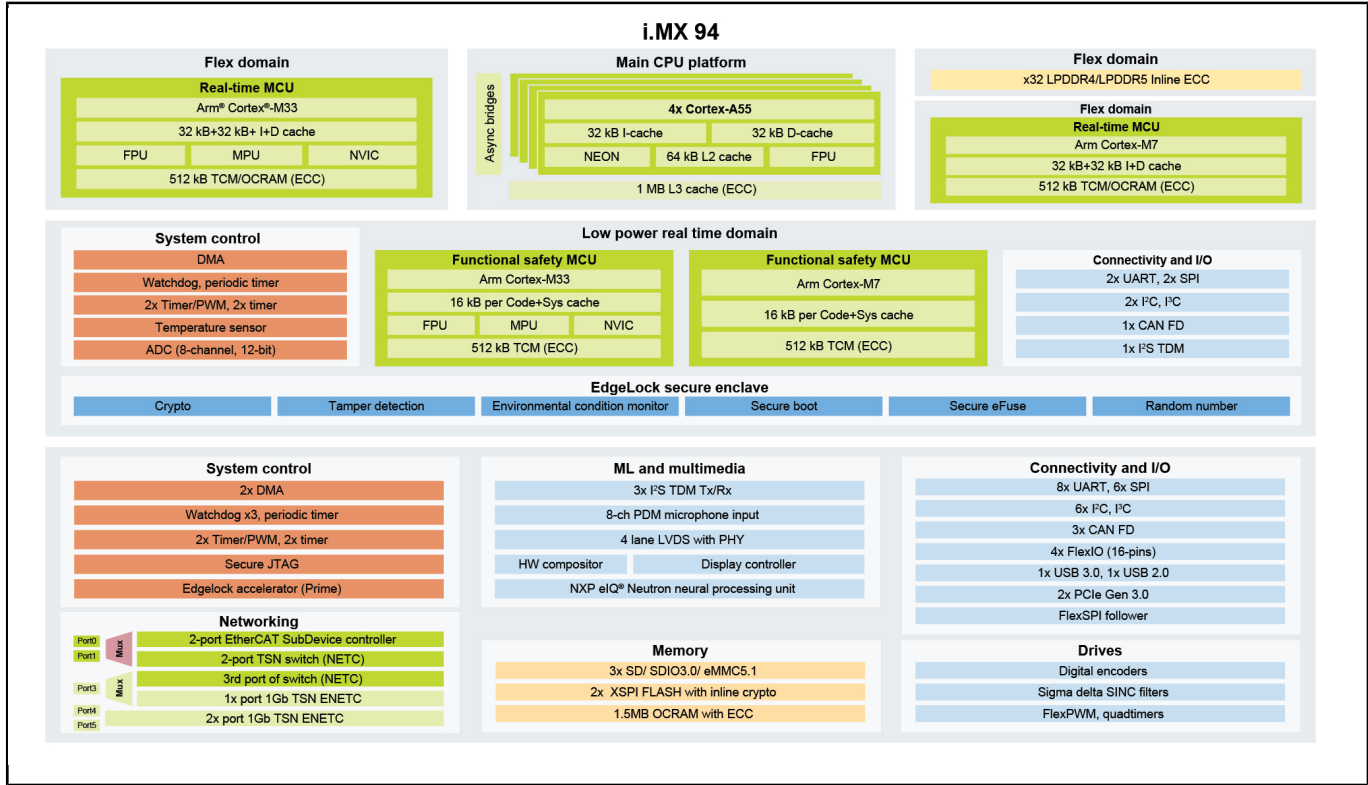
## Preproduction

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The i.MX 94 family is an i.MX MPU with an integrated time-sensitive networking (TSN) switch, enabling configurable, secure communications with rich protocol support in industrial and automotive environments. This is also NXP's first chip supporting future-proof, postquantum cryptography. The multicore design of the i.MX 94 family delivers high-performance and low latency across both application and real-time domains, with an integrated functional safety island.

# i.MX 94 Family Block Diagram Block Diagram



View additional information for [i.MX 94 Applications Processor — Safe and Secure Industrial and Automotive Connectivity with Real-Time Control](#).

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