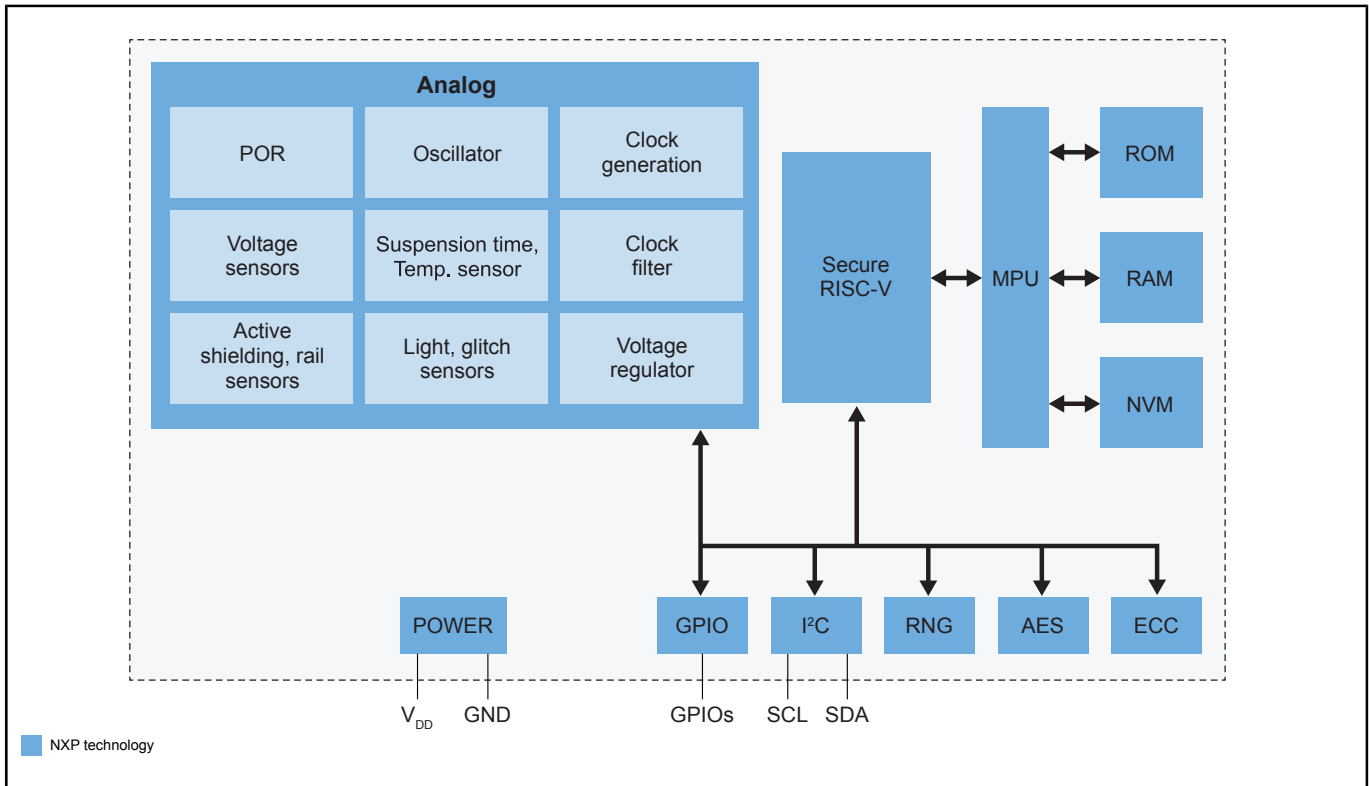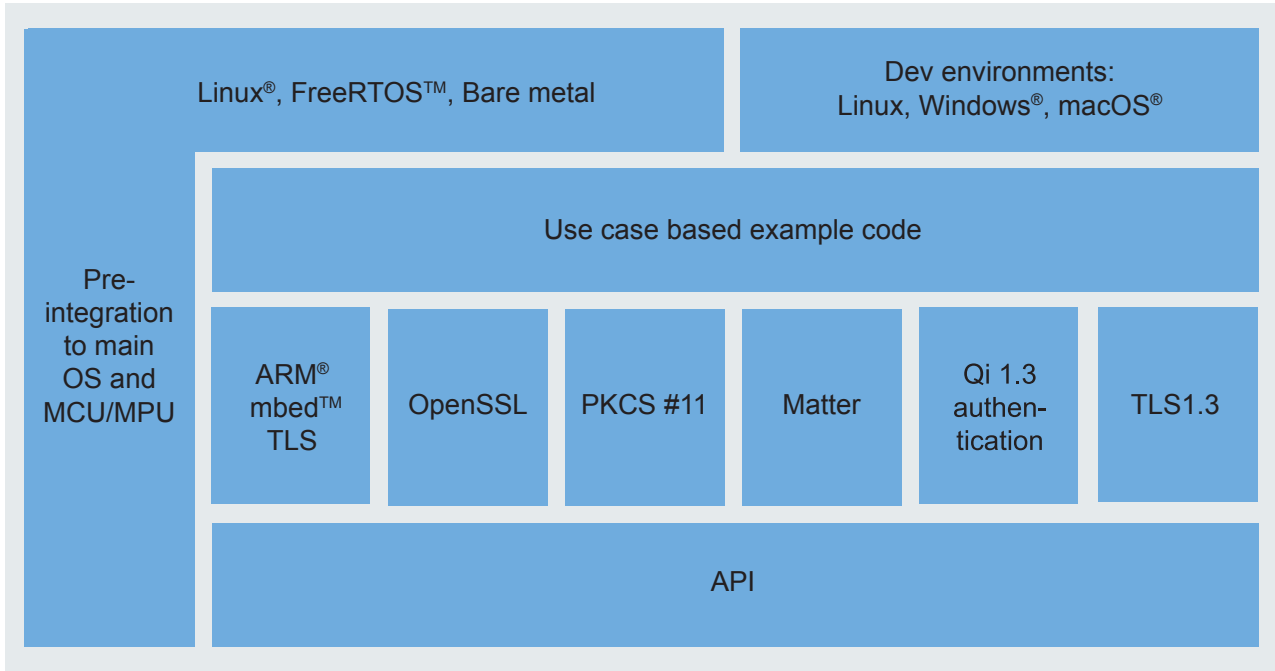# EdgeLock® A30 Secure Authenticator

A30

Last Updated: Feb 21, 2025

The EdgeLock A30 is a ready-to-use, Common Criteria EAL 6+ certified secure authenticator that includes advanced security mechanisms, including AVA_VAN.5, with symmetric and asymmetric crypto. It has an I²C target for easy connection to MCUs/MPUs and scales quickly to high volumes, so manufacturers can quickly deliver IoT devices and accessories. The EdgeLock A30 meets the need for secure accessory and device authentication in a range of markets, while also satisfying new and upcoming regulatory requirements for security, protection and sustainability, including Digital Product Passport.

EdgeLock A30 Block Diagram

Analog

POR	Oscillator	Clock generation
Voltage sensors	Suspension time, Temp. sensor	Clock filter
Active shielding, rail sensors	Light, glitch sensors	Voltage regulator

Secure RISC-V

MPU

ROM

RAM

NVM

POWER

GPIO

I²C

RNG

AES

ECC

V_{DD} GND

GPIOs

SCL SDA

NXP technology

EdgeLock NX Middleware Block Diagram

	Dev environments: Linux, Windows®, macOS®
Linux®, FreeRTOS™, Bare metal	

Pre-integration to main OS and MCU/MPU

Use case based example code

ARM® mbed™ TLS	OpenSSL	PKCS #11	Matter	Qi 1.3 authen-tication	TLS1.3

API

■ NXP Technology   ▢ Non NXP Technology   ⬚ Optional Technology

View additional information for EdgeLock® A30 Secure Authenticator.

Note: The information on this document is subject to change without notice.