

Mask Set Errata

MSE908RK2_1K00J
12/2002

Mask Set Errata for 908RK2,
Mask 1K00J



Introduction

This mask set errata applies to this 908RK2 MCU mask set:

- 1K00J

MCU Device Mask Set Identification

The mask set is identified by a 5-character code consisting of a version number, a letter, two numerical digits, and a letter, for example 1K00J. All standard devices are marked with a mask set number and a date code.

MCU Device Date Codes

Device markings indicate the week of manufacture and the mask set used. The date is coded as four numerical digits where the first two digits indicate the year and the last two digits indicate the work week. For instance, the date code "0201" indicates the first week of the year 2002.

MCU Device Part Number Prefixes

Some MCU samples and devices are marked with an SC, PC, or XC prefix. An SC prefix denotes special/custom device. A PC prefix indicates a prototype device which has undergone basic testing only. An XC prefix denotes that the device is tested but is not fully characterized or qualified over the full range of normal manufacturing process variations. After full characterization and qualification, devices will be marked with the MC or SC prefix.

Low-Voltage Inhibit

SE29-LVI

The low-voltage inhibit (LVI) trip points, low-voltage reset (LVR) and low-voltage detection (LVD), have broader voltage spreads than expected, resulting in a loss of yield. To supply parts, Motorola has relaxed the LVI limits as follows:

Table 1.

	Min	Max
LVR	1.76 V	2.00 V
LVD	1.90 V	2.15 V

The LVD's limits are designed to be higher than those of the LVR. LVR and LVD voltages are taken as a ratio from the same resistor array, and both are compared against the same V_{BREF} (bandgap reference). Then, by nature, LVD has higher values than the LVR.

HOW TO REACH US:**USA/EUROPE/LOCATIONS NOT LISTED:**

Motorola Literature Distribution;
P.O. Box 5405, Denver, Colorado 80217
1-303-675-2140 or 1-800-441-2447

JAPAN:

Motorola Japan Ltd.; SPS, Technical Information Center,
3-20-1, Minami-Azabu Minato-ku, Tokyo 106-8573 Japan
81-3-3440-3569

ASIA/PACIFIC:

Motorola Semiconductors H.K. Ltd.;
Silicon Harbour Centre, 2 Dai King Street,
Tai Po Industrial Estate, Tai Po, N.T., Hong Kong
852-26668334

TECHNICAL INFORMATION CENTER:

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