



S32V2 Processors for Vision, Machine Learning and Sensor Fusion

S32V234

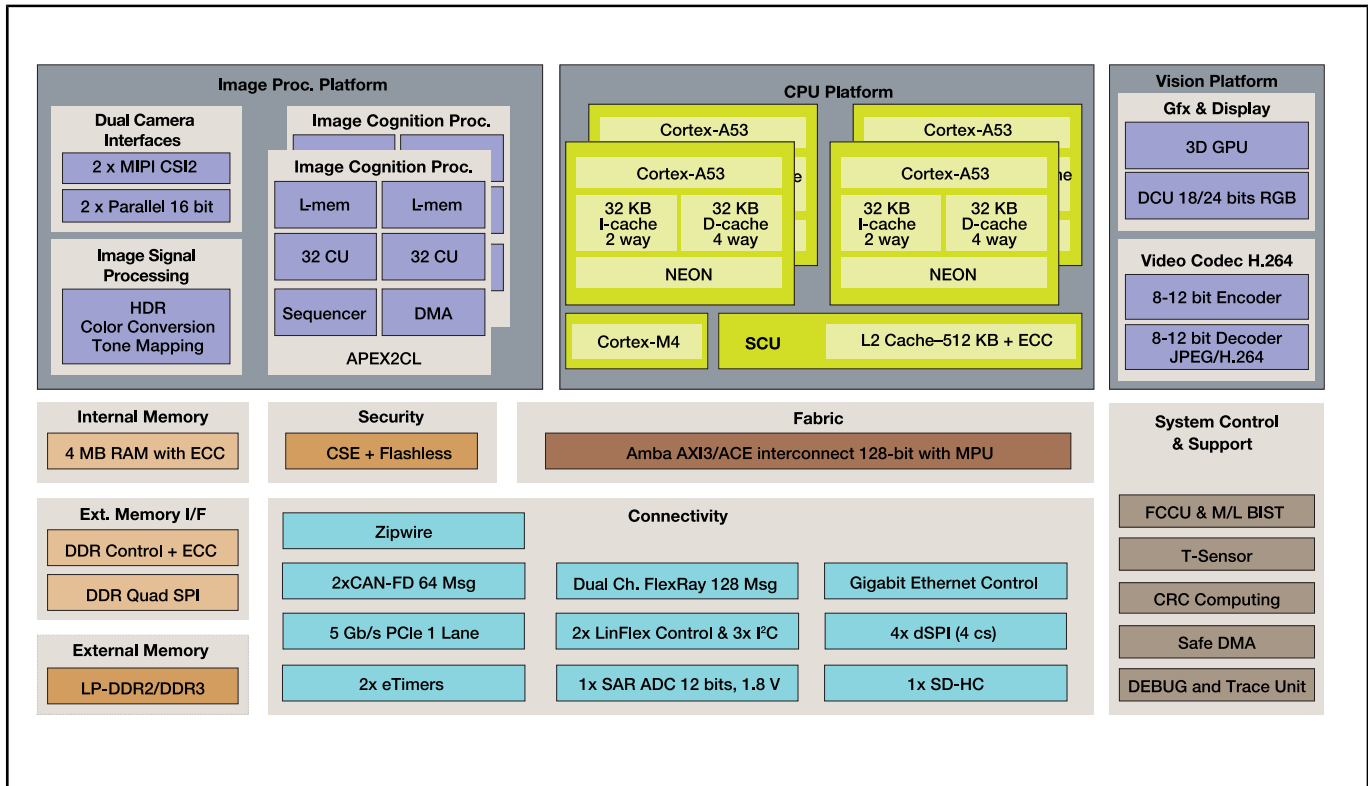
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The S32V234 is a second-generation vision processor family and member of the 64-bit Arm® Cortex®-A53 S32V processors. The S32V234 processor offers an image signal processor (ISP), a powerful 3D graphic processor unit (GPU), dual APEX-2 vision accelerators, automotive-grade reliability, functional safety and security capabilities.

S32V234 processors support computation-intensive ADAS, New Car Assessment Program (NCAP) front camera, object detection and recognition, surround view and automotive and industrial image processing; also, machine learning (ML) and sensor fusion applications.

The S32V234 processor has comprehensive enablement supported by S32 Design Studio IDE for Vision, including a compiler, debugger, Vision SDK, Linux BSP and graph tools.

S32V234 Vision Processor Block Diagram



View additional information for [S32V2 Processors for Vision, Machine Learning and Sensor Fusion](#).

Note: The information on this document is subject to change without notice.

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