



# 8-bit Small Package SG MCUs

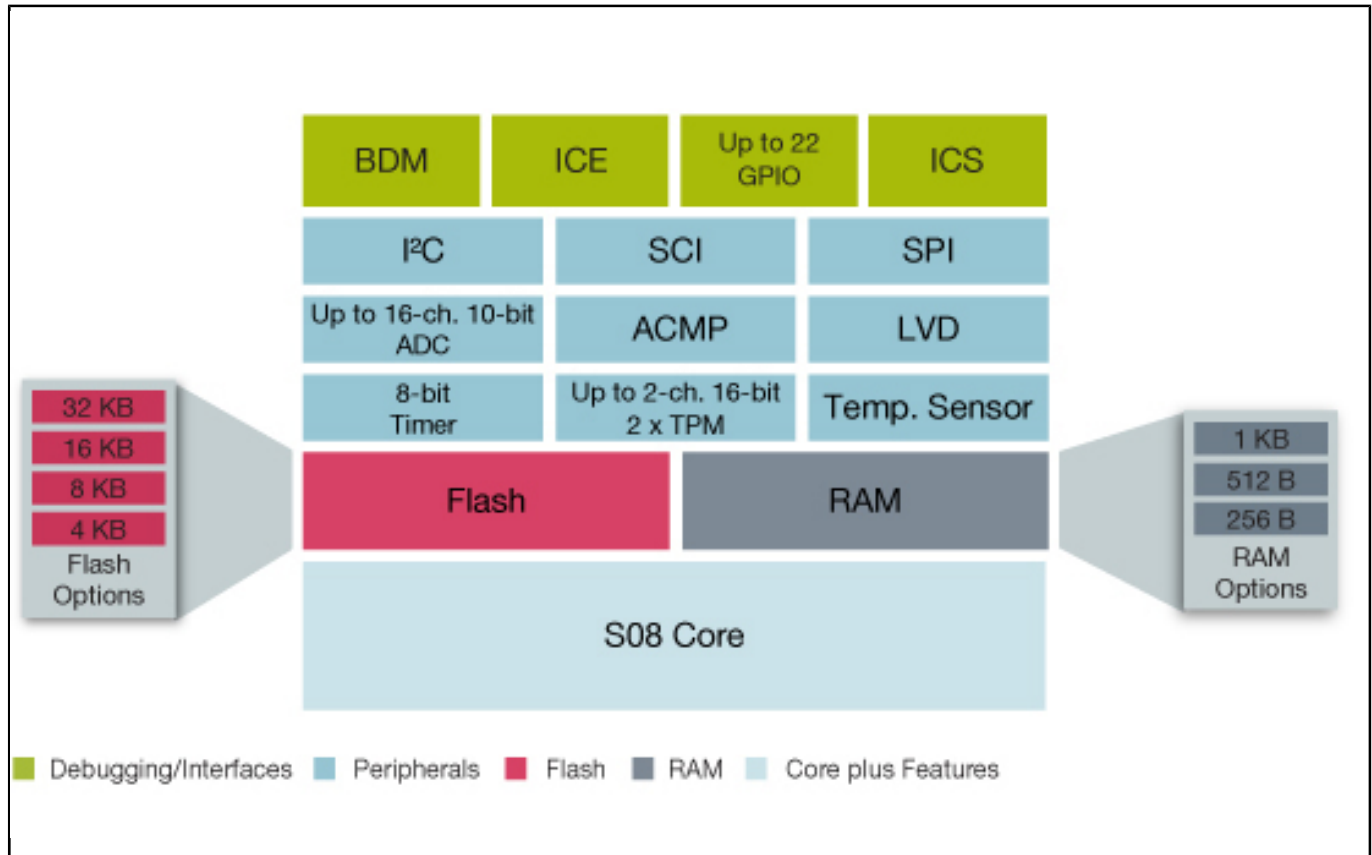
## S08SG

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NXP's 8-bit S08SG family of microcontrollers (MCUs) is an entry-level to mid-range offering designed for LIN follower, general market, high-temperature (up to 150 °C ambient) and space-constrained applications. The SG is easy to design into an application because of its non-intrusive, single-wire background debug module (BDM), which allows you to read registers without stopping execution on the chip. This reduces development time, since changes can be made while in application and in real time. It also allows debug access in constrained locations. The SG helps to lower cost, save board space and improve quality with on-chip components by eliminating the need for external crystal, low-voltage interrupt (LVI) circuit, voltage regulator, I/O mux, watchdog circuit, analog-to-digital converter (ADC) and development tools.

The SG family is also pin-compatible with the [S08EL](#) and [S08SL](#) families. This flexibility allows for upgrading and downgrading without major board redesign or long and expensive development times.

## S08SG Microcontroller Block Diagram Block Diagram



View additional information for [8-bit Small Package SG MCUs](#).

Note: The information on this document is subject to change without notice.

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