



# Kinetis® K11-50 MHz, Anti-Tamper Microcontrollers (MCUs) based on Arm® Cortex®-M4 Core

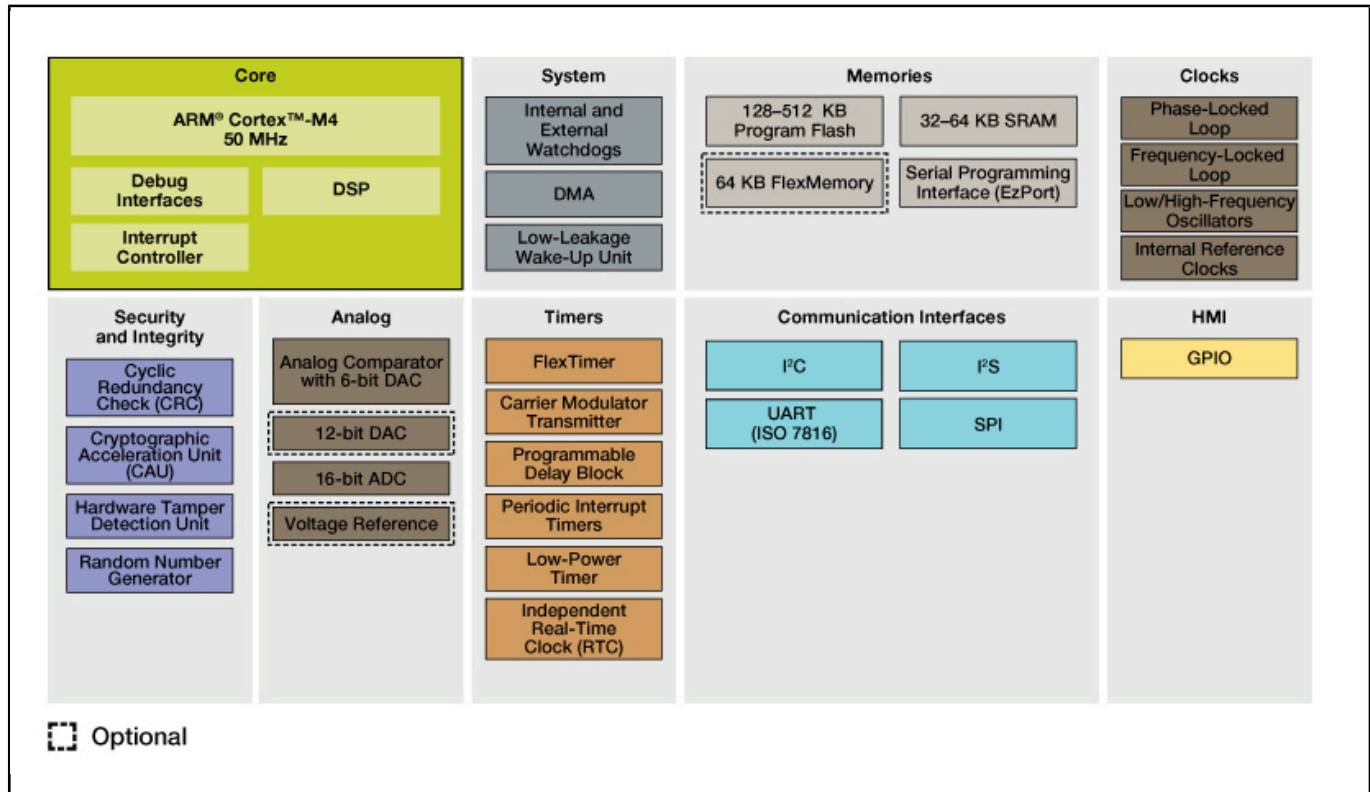
## K11\_50

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The Kinetis® K11 50 MHz baseline MCUs, built on the Arm® Cortex®-M4 core, are optimized for cost-sensitive applications requiring low-power, security encryption, tamper detection and processing efficiency. This family shares the comprehensive enablement and scalability of the Kinetis portfolio.

Please contact your local NXP representative to download the K11 Security Data sheet and Reference Manual documents (under NDA).

# Kinetis K11 50 MHz Baseline MCUs Block Diagram Block Diagram



View additional information for [Kinetis® K11-50 MHz, Anti-Tamper Microcontrollers \(MCUs\) based on Arm® Cortex®-M4 Core.](#)

**Note:** The information on this document is subject to change without notice.

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