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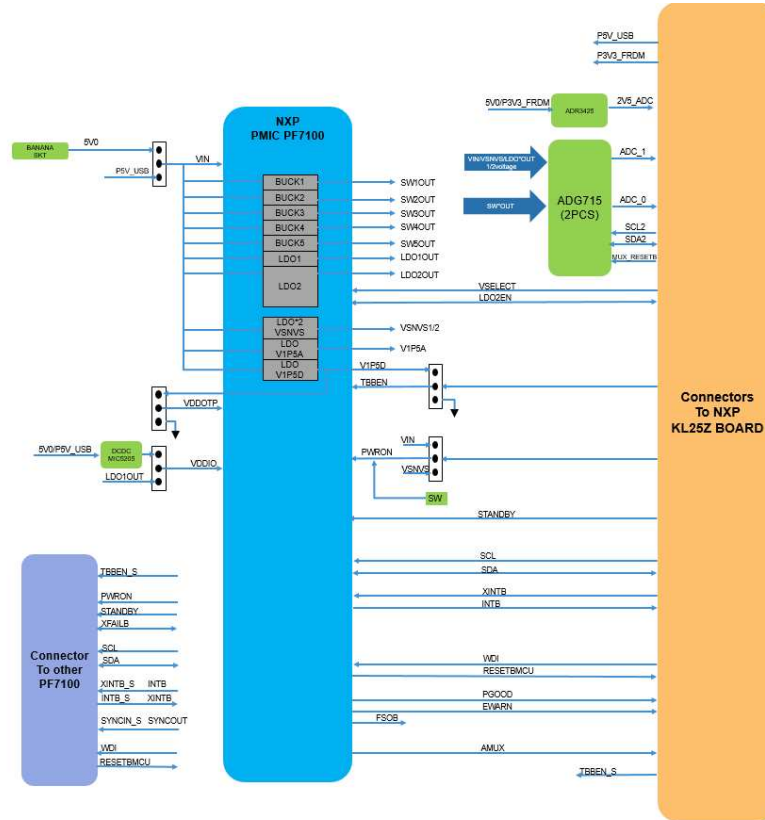
Revisions

Rev	Description	Date
X1	Initial schematic	17-JAN-2019
A	Release A085	4-MAR-2019
B	1. Solved the J1 mirror problem. 2. Changed J27-3, J26-3, R103-1, R78-1, R85-1 pin nets from VIN_PIN to VIN. 3. Changed FSOB and PWRON pull up resistors from 470K to 100K.	31-MAY-2019
B1	Update the board name.	15-Aug-2019
B2	Update U1 part number to PPF7100AVBA0ES.	25-Sep-2019

KITPF7100FRDMEVM

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Designer: <small>Wesluis Chen</small>	Drawing Title: KITPF7100FRDMEVM		
Drawn by: <small>Kate Fick</small>	Page Title: TITLE		
Approved: <small>Wesluis Chen</small>	Size C	Document Number SCH-45772 PDF: SPF-45772	Rev B2
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BLOCK DIAGRAM



REF DES	JUMPER[DEFAULT]	JUMPER[OPTION]	PAGE NAME
J28,J7,J11,J31,J20,J39, J15,J22	1-2		03-PMIC
J27,J30,J14,J26	2-3		03-PMIC
J29,J25,J21	OPEN		03-PMIC
J12	1-2: VREG=3V3	2-3: VERG=1V8	03-PMIC
J33,J32	1-2		04-KL25Z I/O, CONNECTOR, AMUX

REF DES	ASSY. OPT	PAGE NAME
C72,C91,C80,C66,C2,C68,C90, R102,R72,C76	DNP	03-PMIC
R11,R106,R10	DNP	04-KL25Z I/O, CONNECTOR, AMUX



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