
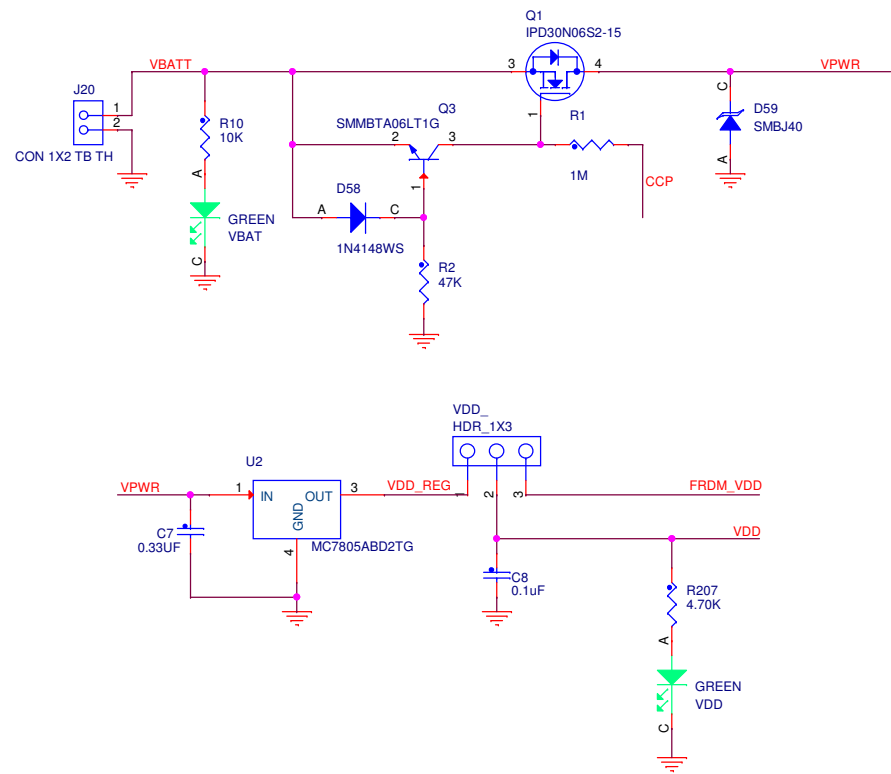


Table of Contents	
1	Title, TOC & Rev History
2	FRDM-HB2002ESEVM SCHEMATIC

Revisions			
Rev	Description	Date	Approved
A	Agile release	22 NOV 17	Wayne A

FRDM-HB2002ESEVM

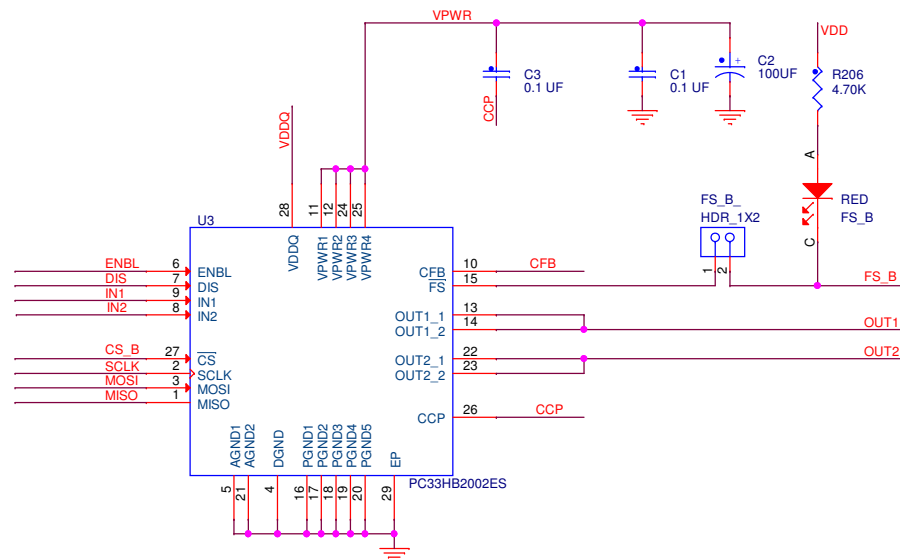
		Analog Sensor Product Group 6501 William Cannon Drive West Austin, TX 78735-8598	
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ICAP Classification: CP: IUQ: X PUB:			
Designer: Wayne Anderson		Drawing Title: FRDM-HB2002ESEVM	
Drawn by: Catalin Neacsu		Page Title: Title Page, TOC, Rev. History	
Approved: Wayne Anderson	Size C	Document Number SCH-30177 PDF: SPF-30177	Rev A
Date: Wednesday, November 22, 2017		Sheet 1 of 2	



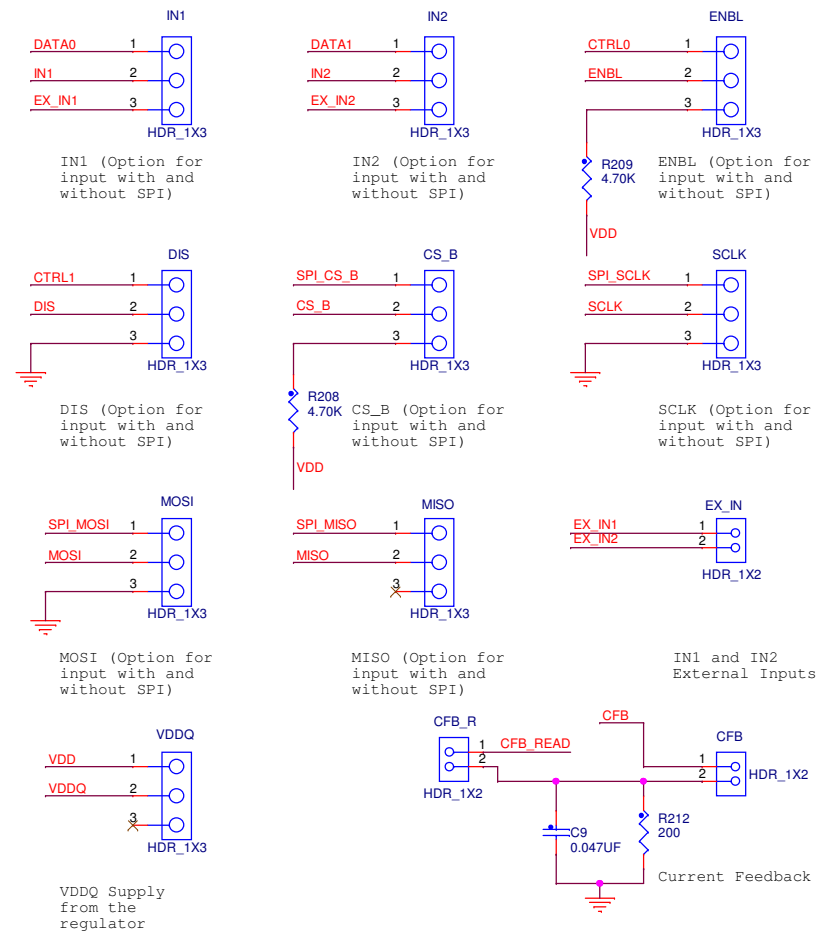
Jumper setting 1-2 for VDD through regulator and 2-3 for VDD supply through FRDM board.

Power Supply

FRDM-HB2002ESEVM

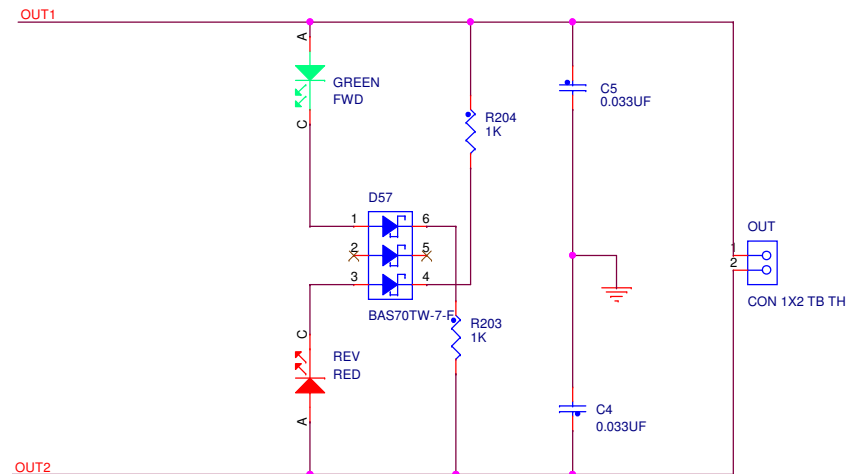


Main Circuit

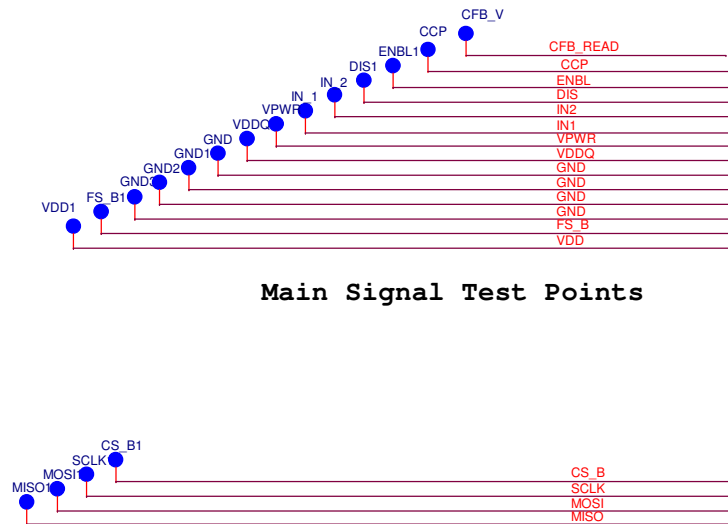


Jumpers

All settings jumper position 1-2 for operation with MCU and 2-3 for manual operation.

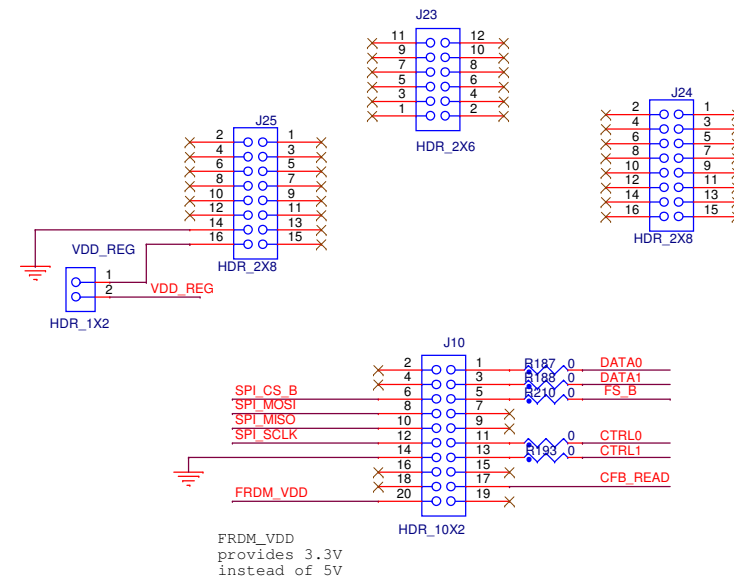


Outputs



Main Signal Test Points

SPI Signal Test Points



SPI Connector

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Drawing Title: FRDM-HB2002ESEVM		
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