

SOT1092-2

plastic thermal enhanced very thin quad flat package; no leads; 36 terminals; body 6 mm x 6 mm x 0.85 mm

21 June 2017

Package information

1. Package summary

Terminal position code	Q (quad)
Package type descriptive code	HVQFN36
Package type industry code	HVQFN36
Package style descriptive code	HVQFN (thermal enhanced very thin quad flatpack; no leads)
Package body material type	P (plastic)
Mounting method type	S (surface mount)
Issue date	24-2-2009
Manufacturer package code	SOT1092

Table 1. Package summary

Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	5.9	-	6	6.1	mm
E	package width	5.9	-	6	6.1	mm
A	seated height	0.8	-	0.85	1	mm
A ₂	package height	-	-	-	-	mm
e	nominal pitch	-	-	-	-	mm
n ₂	actual quantity of termination	-	-	36	-	



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2. Package outline

HVQFN36: plastic thermal enhanced very thin quad flat package; no leads; 36 terminals; body 6 mm x 6 mm x 0.85 mm

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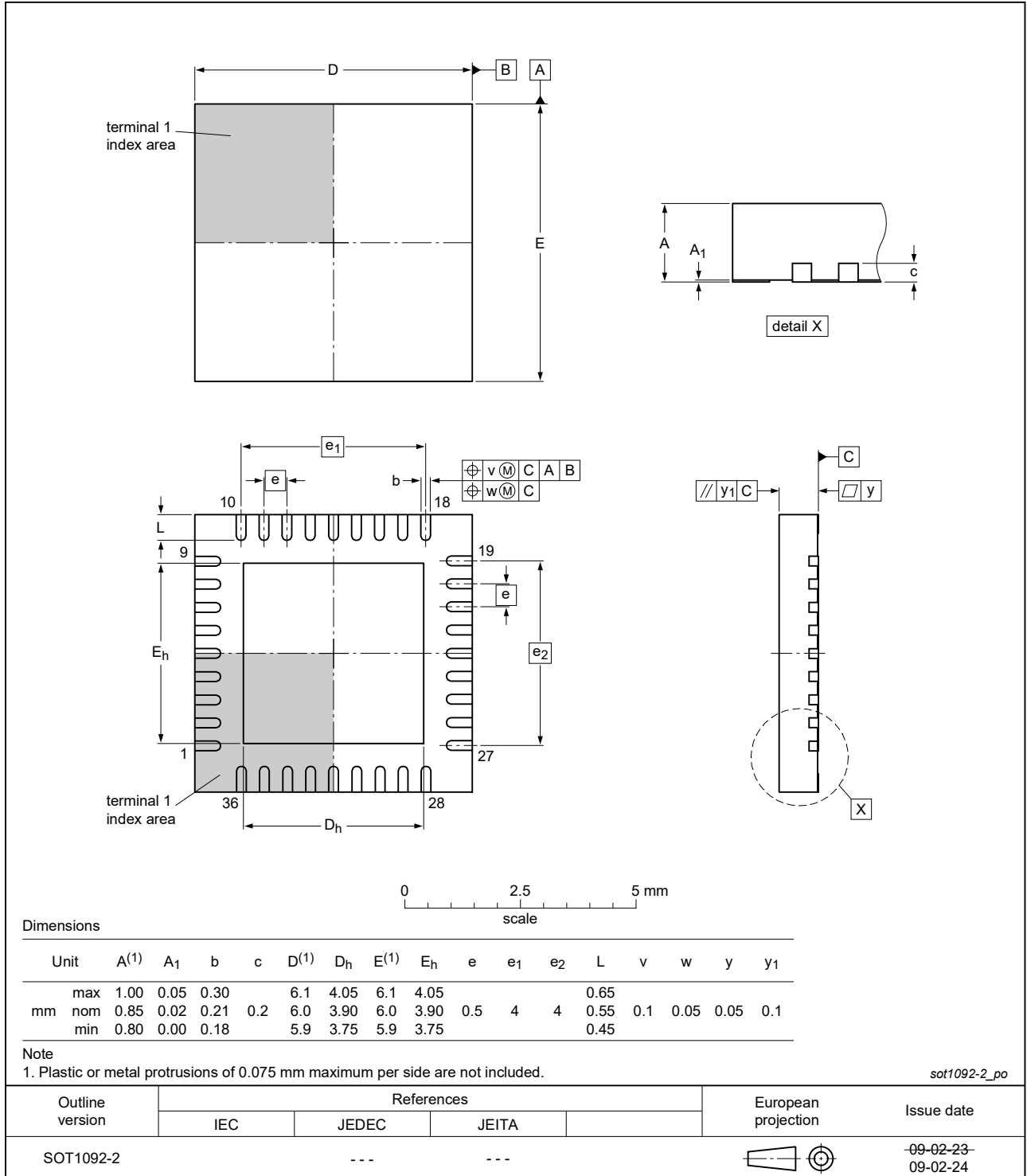
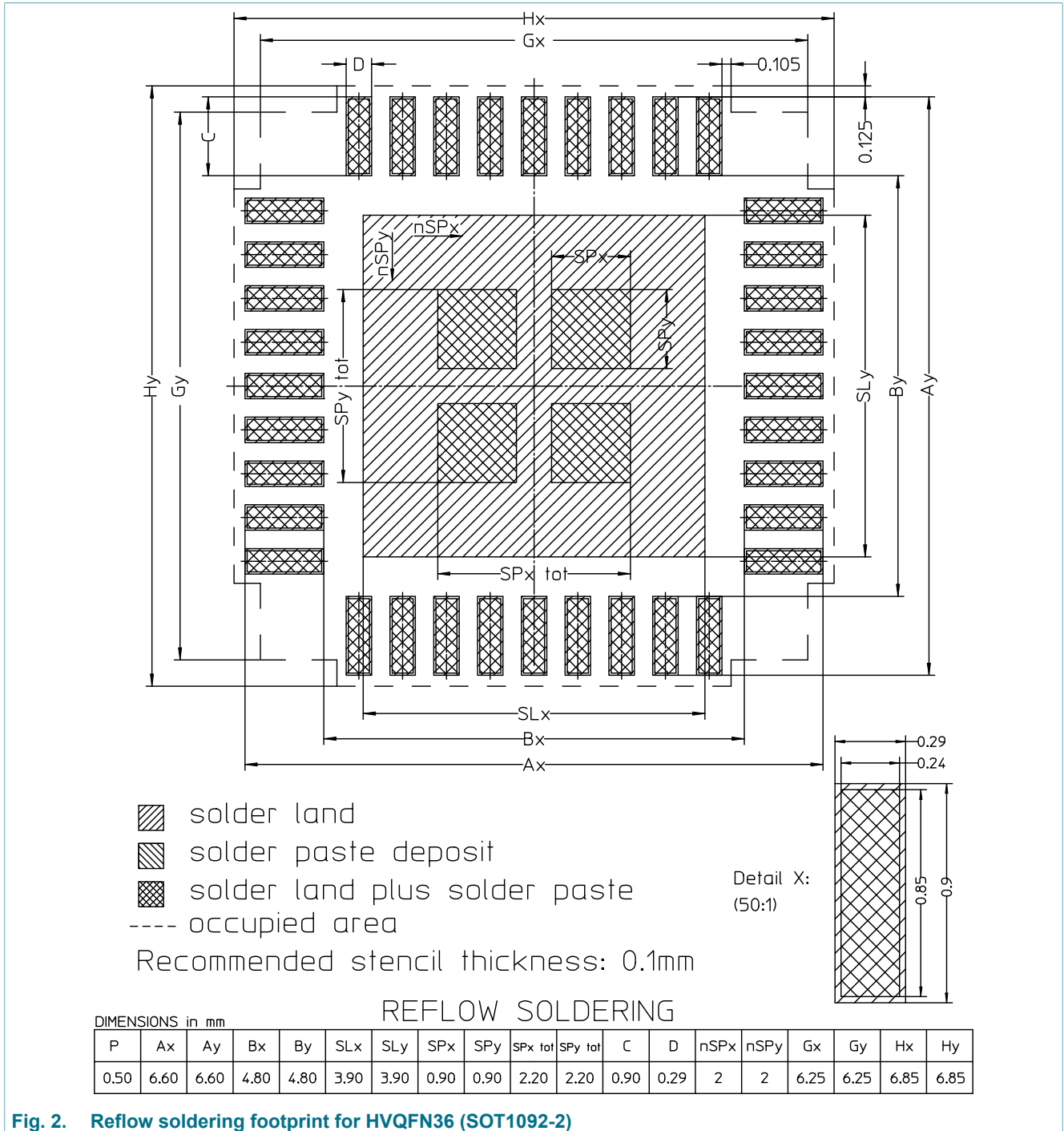


Fig. 1. Package outline HVQFN36 (SOT1092-2)

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3. Soldering



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4. Legal information

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Date of release: 21 June 2017
