Table 1:

MPC860	**Monitor Debugger (mpc8bug)**
Rev 0.x 0.2 = Mask 2E64C 0.3 = Mask 3E64C	mpc8bug - Any Version
Rev A.x A.2 = Mask 2F84C	mpc8bug V1.2 or later
Flash in MPC860 ADS Board & Part #	**ADS board & Monitor Debugger (mpc8bug)**
2 MB - 55132T9DX (*Southland Microsystems)	Pilot, ENG, & Rev A boards, Any Version mpc8bug
4 MB - 50132T9DX (*Southland Microsystems) 4 MB - SM73218XV1JAVS2 (Smart)	Rev A boards or later, mpc8bug V1.2 or later
8 MB - 50232T2DX (*Southland Microsystems)	Rev A boards or later, mpc8bug V1.2 or later
Flash in MPC860 ADS Board & Part #	**SDS Debugger**
2 MB - 55132T9DX (*Southland Microsystems)	V0.7 Working
4 MB - 50132T9DX (*Southland Microsystems) 4 MB - SM73218XV1JAVS2 (Smart)	Not Supported as of March 1997
8 MB - 50232T2DX	Not Supported as of March 1997
MPC860 ADS Board	**** Memory Limitations**
REV B or later	All O.K. 4MB DRAM Part # - 30136G6 (*Southland Microsystems)
ENG, PILOT, Rev A	No Burst to EDO
ENG, PILOT, Rev A (Shipped before June 25, 1996)	Require additon of damp ing resistors
*To order through Southland Microsys-tems contact: Steve Marconi (714)380-1958 x270	

Table 1:

** All boards (used with mpc8bug monitor debugger) default to memory initialization for 50MHz Fast Page Mode DRAM (non-EDO), only SPEED (60ns, 70ns) is automatically sensed. EDO/Non-EDO is NOT! ** On the MPC860ADS if EDO is initialized as Non-EDO (as above) it will run all modes (Bursting OK). It will just be treated like normal DRAM. ** Any MPC860 chip will work with any MPC860 ADS board	

Rev. 1 1/15/98 Changed Southland Microsystems contact.