S32R41 Evaluation Board

S32R41-EVB

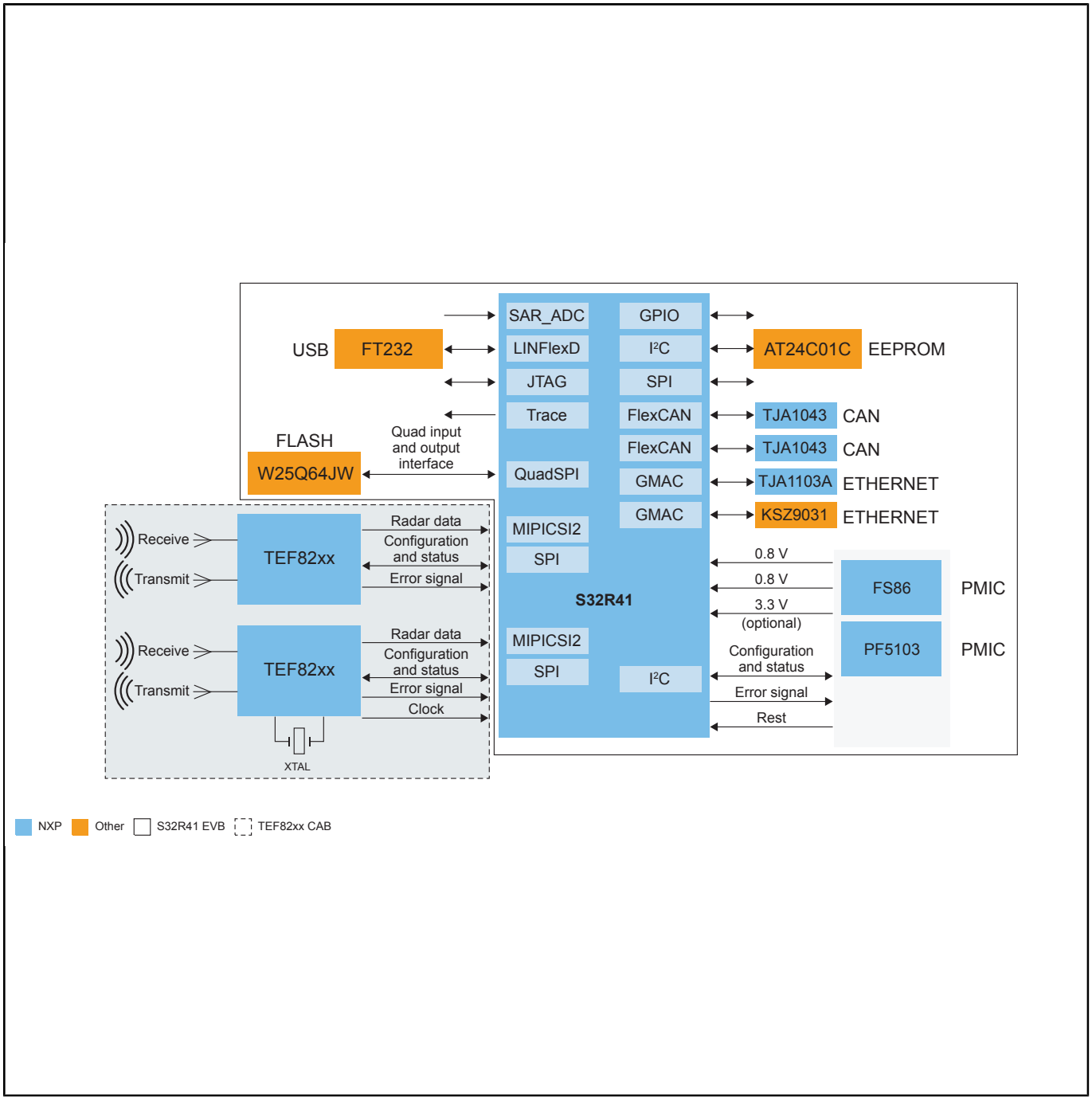
Last Updated: Jan 18, 2024

The S32R41 Evaluation Board S32R41-EVB supports the development of high-performance 77GHz radar applications targeted for automotive use cases, such as advanced high resolution corner and front radar applications. The Arm® Cortex® A53 core and triple Arm Cortex M7 lockstep cores in the S32R41 MPU together with dedicated radar processing accelerators enable the S32R41 EVB to target high resolution radar systems required for L2 Autonomous driving applications and beyond.

S32R41 EVB offers a wide variety of features and interfaces to facilitate HW and SW development and reduce customer's time to market, e.g., 2x MIPI-CSI2 to connect NXP's TEF82xx RFCMOS transceivers, CAN FD, LIN and Ethernet (1Gbps and 100Mbps).

The S32R41 EVB consists of a processor board suitable for development of radar processing for the S32R41 processor. Together with the TEF82xx CAB, the S32R41-EVB can be used as a radar development platform.

S32R41 Evaluation Board Block Diagram



View additional information for S32R41 Evaluation Board.

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.