



# 7-Channel Power Management Integrated Circuit for High Performance Applications, Fit for ASIL B Safety Level

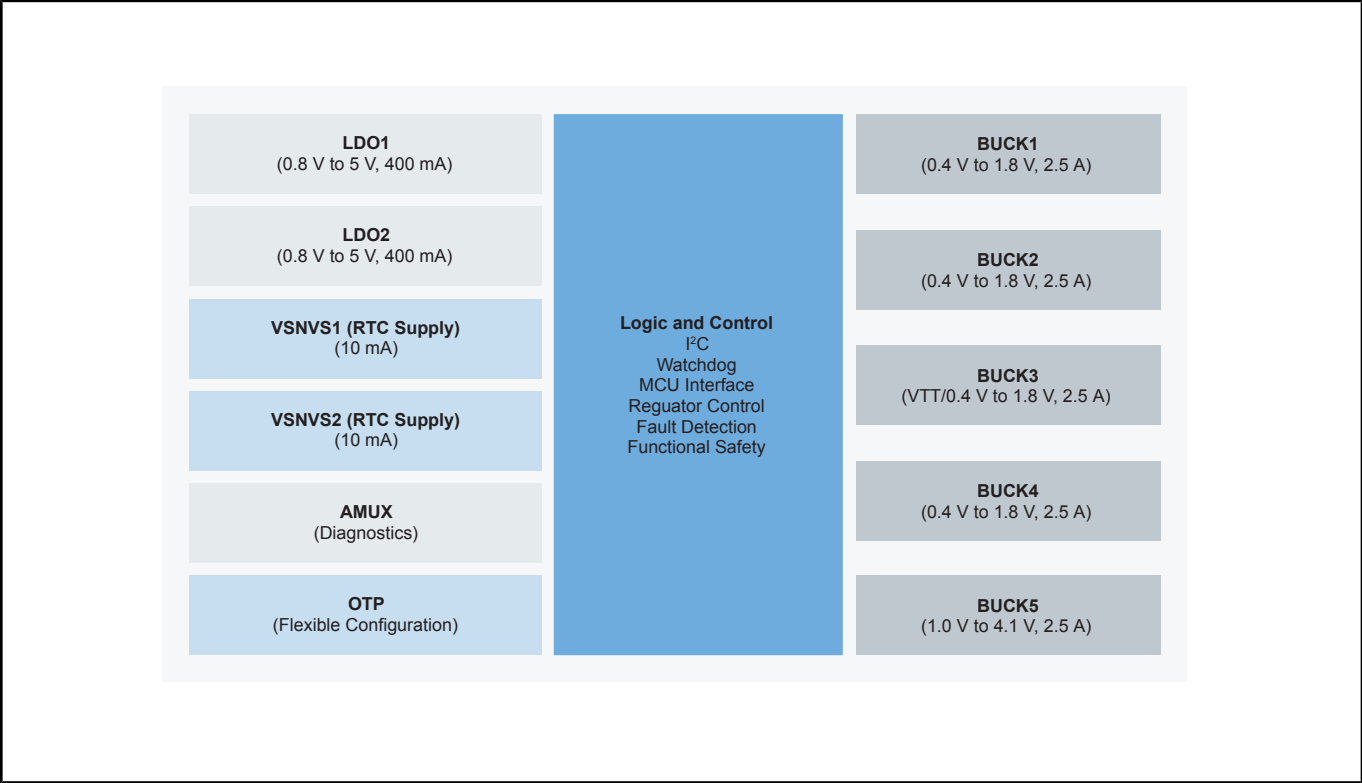
## PF7100

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The PF71 is a power management integrated circuit (PMIC) designed for high performance [i.MX 8 processors](#). It features five high efficiency buck converters and two linear regulators for powering the processor, memory and miscellaneous peripherals.

Built-in one-time programmable memory stores key startup configurations, drastically reducing external components typically used to set output voltage and sequence of external regulators. Regulator parameters are adjustable through high-speed I<sup>2</sup>C after startup, offering flexibility for different system states.

# PF7100 7-Channel PMIC Block Diagram



View additional information for [7-Channel Power Management Integrated Circuit for High Performance Applications, Fit for ASIL B Safety Level](#).

**Note:** The information on this document is subject to change without notice.

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