MPC860 PowerQUICC® Processor
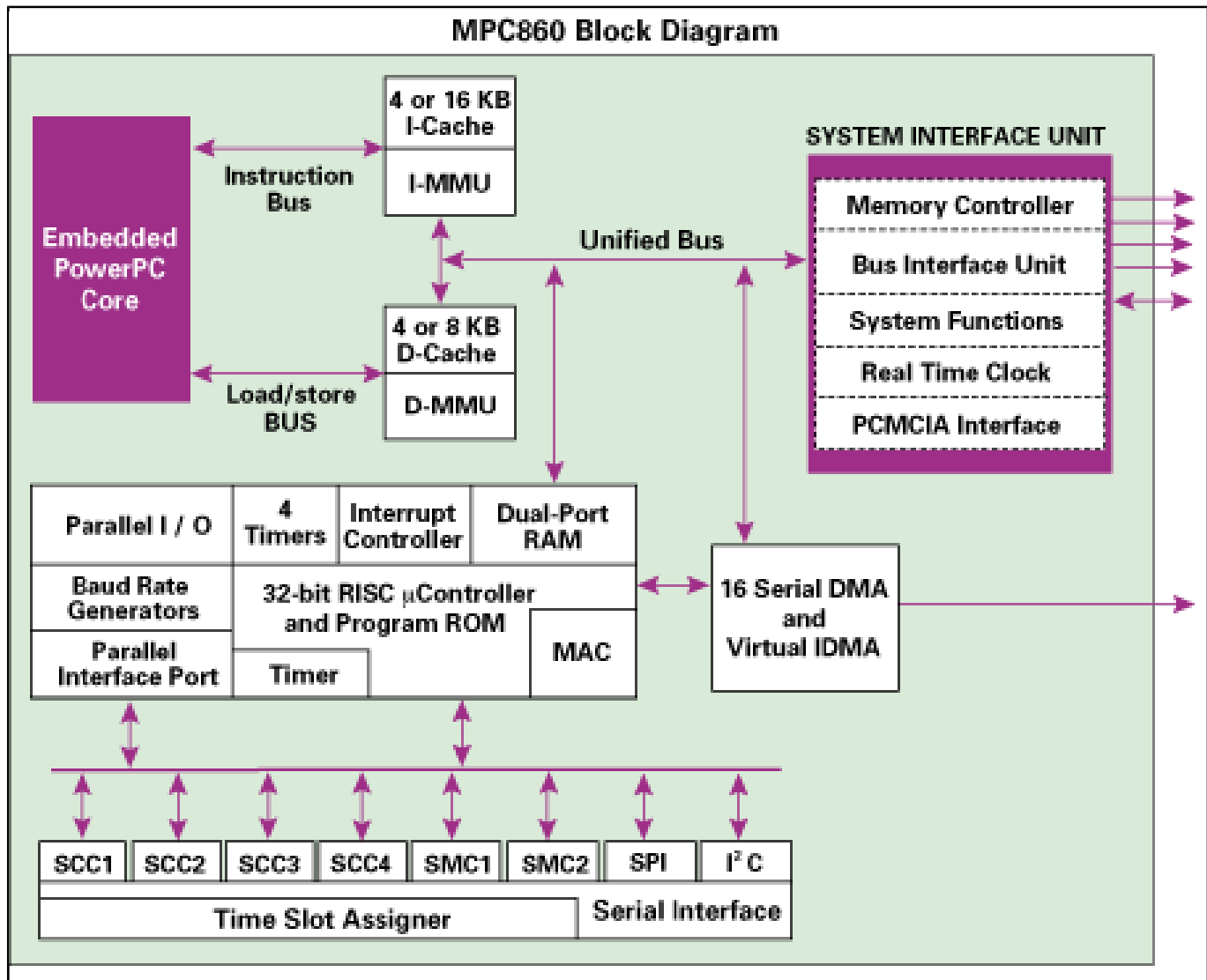
MPC860

Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

Last Updated: Apr 8, 2022

The MPC860 PowerQUICC® offers an integrated processor and peripheral solution that can be used in a variety of communications and networking controller applications.

- Delivers higher performance in all areas of device operation including flexibility, extensions in capability, and integration
- Provides lower power consumption than traditional architectures
- Backed by strong third-party tool support through Our Design Alliance Program

MPC860_BLKDIAG Block Diagram

MPC860 Block Diagram

Embedded PowerPC Core

Instruction Bus → **4 or 16 KB I-Cache** / **I-MMU**

Load/store BUS → **4 or 8 KB D-Cache** / **D-MMU**

Unified Bus

SYSTEM INTERFACE UNIT
- Memory Controller
- Bus Interface Unit
- System Functions
- Real Time Clock
- PCMCIA Interface

Parallel I / O | 4 Timers | Interrupt Controller | Dual-Port RAM

Baud Rate Generators | 32-bit RISC μController and Program ROM

Parallel Interface Port | Timer | MAC

16 Serial DMA and Virtual IDMA

SCC1 | SCC2 | SCC3 | SCC4 | SMC1 | SMC2 | SPI | I²C

Time Slot Assigner | **Serial Interface**

View additional information for MPC860 PowerQUICC® Processor.

Note: The information on this document is subject to change without notice.