Integrated ColdFire® V2 External Memory and Ethernet Microprocessors

MCF520X

Not Recommended for New Designs

This page contains information on a product that is not recommended for new designs.

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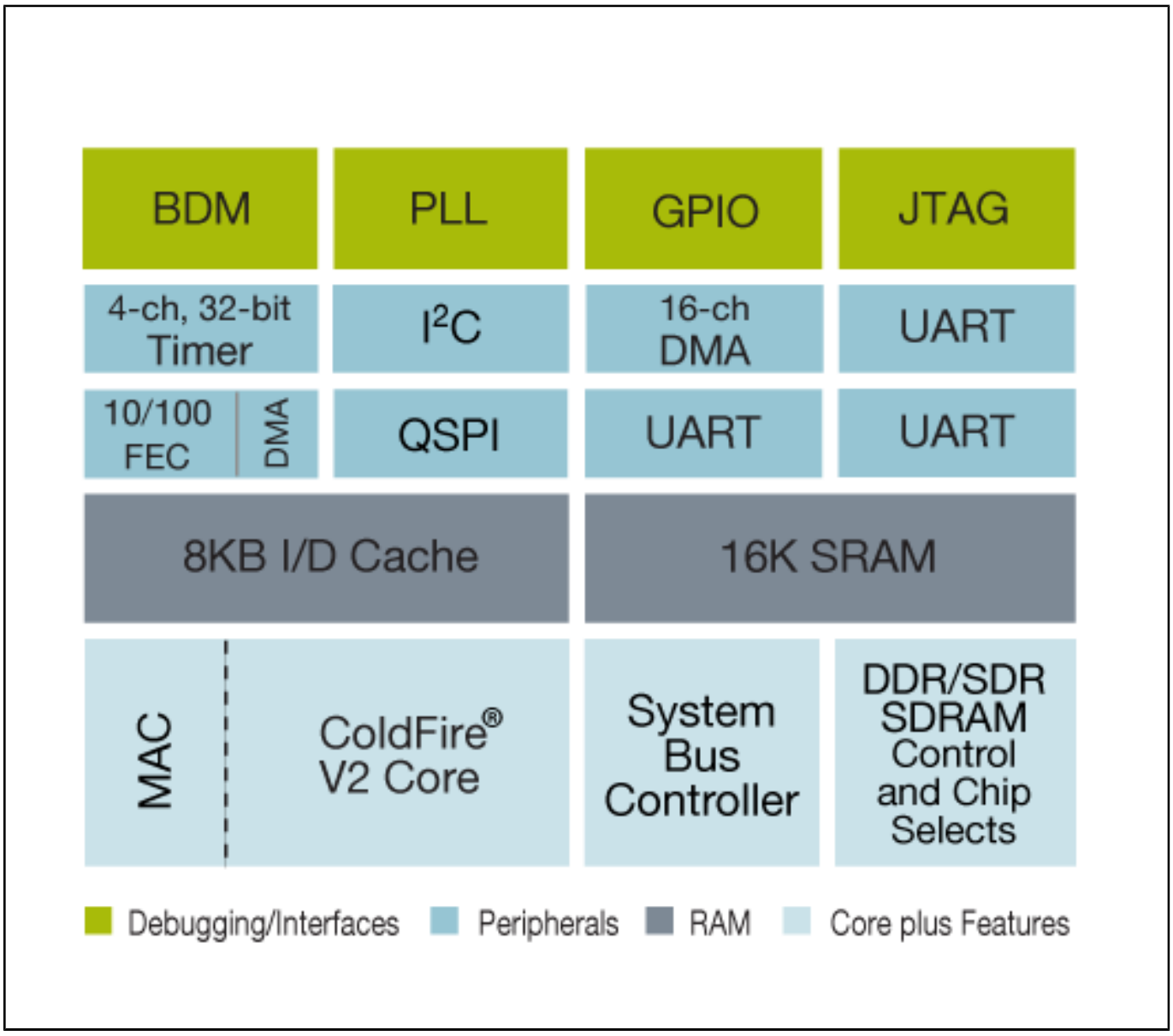
The MCF520X family of ColdFire® V2 embedded controllers offers low system cost, flexible memory controller supporting a combination of external static random access memory (SRAM), flash memory and a choice of single-data rate (SDR), double-data rate (DDR) or mobile-double-data rate (M-DDR) synchronous dynamic random access memory (SDRAM). The MCF5207 and MCF5208 embedded controllers integrate standard ColdFire communications peripherals, including three universal asynchronous receiver/transmitters (UARTs) for medium-and long-distance connections, an inter-integrated circuit (I²C) and queued serial peripheral interface (QSPI) for in-system communication to connected peripherals. In addition, the MCF5208 features a 10/100 fast Ethernet controller. The devices are optimized for low-power applications, featuring several low-power modes and a low-frequency clock divider.

The cost-effective and easy-to-use M5208EVB kit simplifies MCF5207 and MCF5208 product development and speeds time to market. The M5208EVB features full access to the 196-pin fully integrated MCF5208 embedded controller and integrates the MC13192 ZigBee®-ready transceiver and antenna.

Integrated ColdFire MCF520X Ethernet Microprocessor Block Diagram Block Diagram



View additional information for Integrated ColdFire® V2 External Memory and Ethernet Microprocessors.

Note: The information on this document is subject to change without notice.