



System Basis Chip with CAN High-Speed and LIN Interface

MC34904

Not Recommended for New Designs

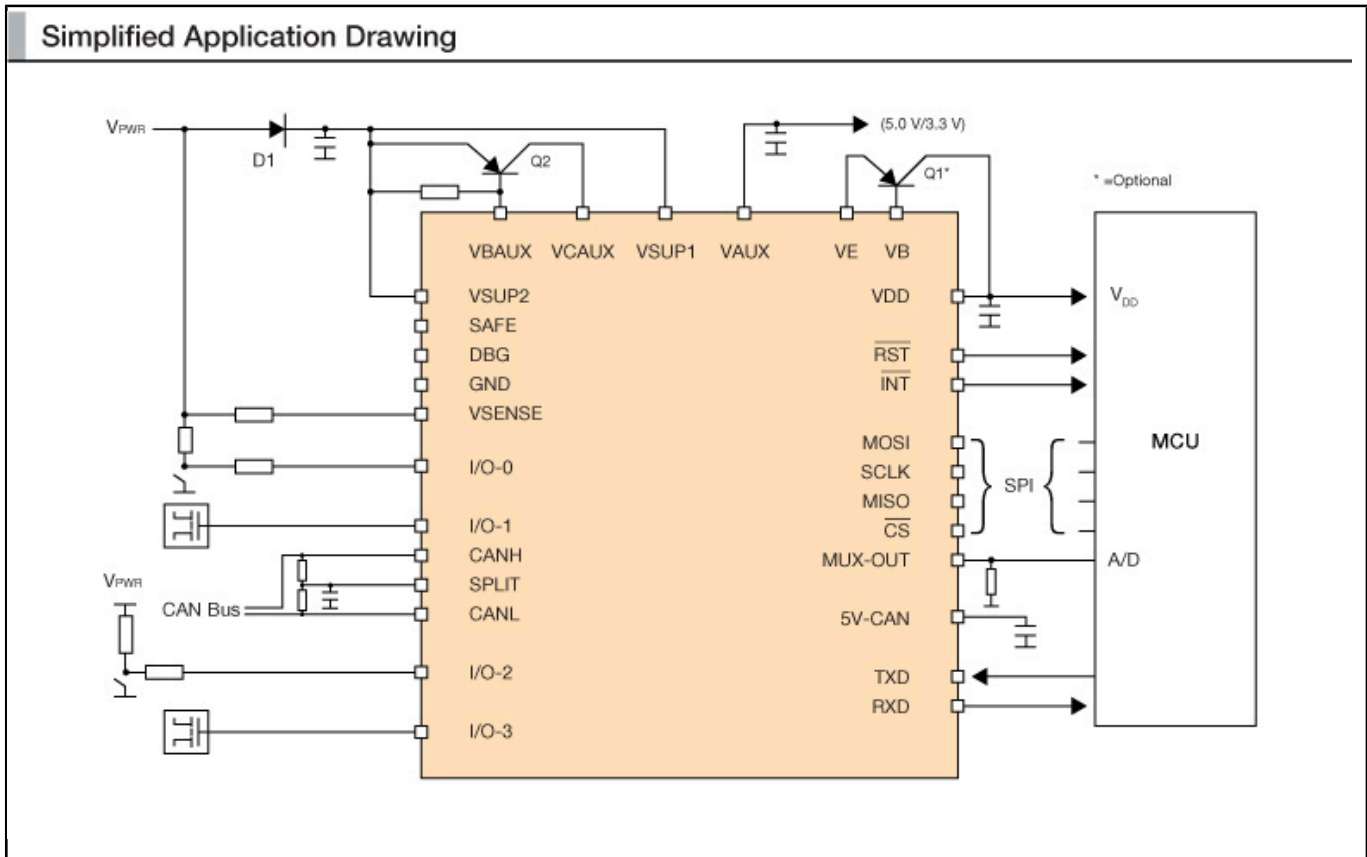
This page contains information on a product that is not recommended for new designs.

Last Updated: Jan 19, 2024

The NXP® MC34904 is an analog companion chip for the MCU and combines several voltage regulators, interface, monitoring and control, and other features to enhance present module designs targeting the industrial market.

- Built-in enhanced high-speed CAN interface (ISO11898-2 and -5) with local and bus failure diagnostics, protection, and fail-safe operation modes
- Includes up to four wake-up input pins that can also be configured as output drivers for flexibility
- Powered by SMARTMOS® technology

29400_MC34904_BD Block Diagram



View additional information for [System Basis Chip with CAN High-Speed and LIN Interface](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.