



# Layerscape 2084A and 2044A Multicore Processors

## LS2084A

Last Updated: Apr 11, 2024

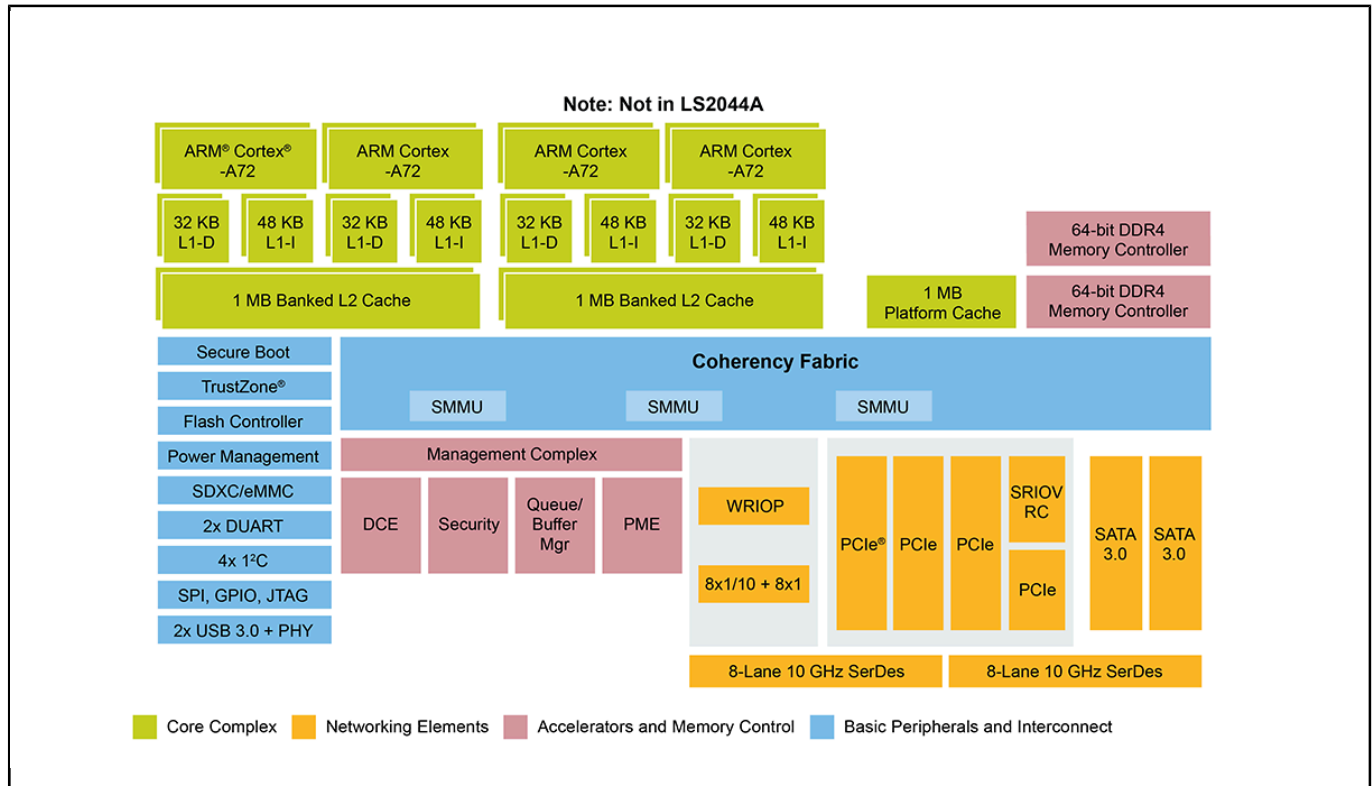
The LS2 family of processors delivers unprecedented performance and integration for tomorrow's smarter, more capable networks. The eight-core LS2084A and the four-core LS2044A multicore processors offer Arm® Cortex®- A72 cores with the advanced, high-performance datapath and network peripheral interfaces required for networking, telecom/ datacom, wireless infrastructure, military, aerospace and automotive applications.

The next-generation datapath architecture, combined with a powerful software toolkit, provides a higher level of hardware abstraction and makes software development quick and simple.

This combination balances ease-of-use with high-performance processing in a Linux® environment that is familiar to any software programmer. Customers can fully exploit the underlying hardware and easily adapt to network changes for real-time 'soft' control over the network.

Layerscape processors are part of NXP's EdgeVerse™ [edge computing](#) platform.

# LS2084A Processor Block Diagram Block Diagram



View additional information for [Layerscape 2084A and 2044A Multicore Processors](#).

**Note:** The information on this document is subject to change without notice.

[www.nxp.com](http://www.nxp.com)

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.