



NXP's Energy Efficient Cortex-M4 MCU with Cortex-M0+ and Advanced Security

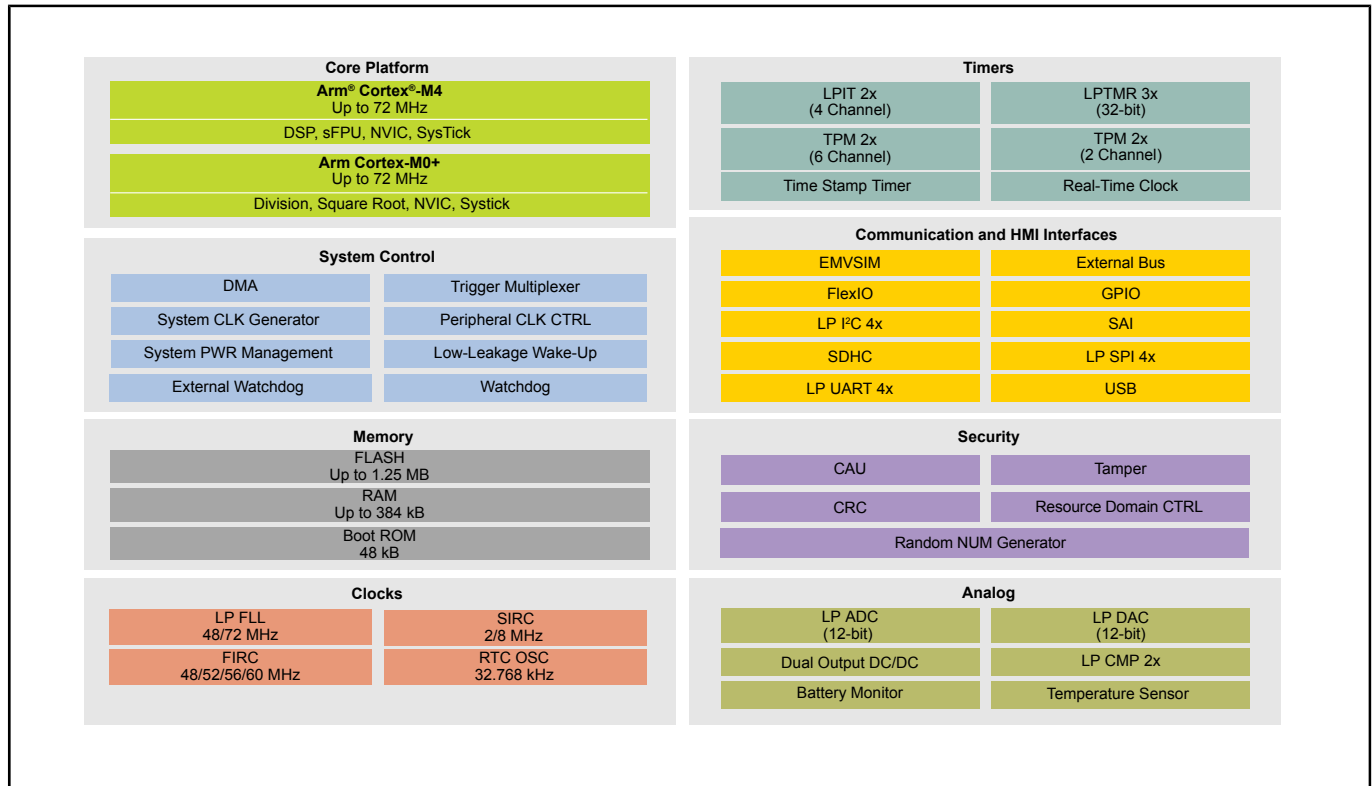
K32-L3

Last Updated: Apr 10, 2024

Building on the successful Kinetis K Series (K22 and K24), the K32 L3 family of MCUs delivers a 50% improvement in power optimization and security advancements over the previous generation, to address a wide range of industrial and IoT applications. The K32 L3 MCU family is based on the power-efficient Arm® Cortex® -M4 core and offers a Cortex-M0+, providing new enhancements such as low-leakage power-optimized peripherals, a DC-DC converter, and security features like authenticated boot, secure update and tamper detection pins.

The introduction of the K32 L3 MCU family is the start of a long line of MCUs, which will further advance NXP's security capabilities and power optimization features to lead the market in the next generation of low-leakage applications. The K32 L3 MCU family is complemented by a comprehensive ecosystem including MCUXpresso software and tools and a Freedom development board for easy prototyping.

K32 L3 MCU Block Diagram



View additional information for [NXP's Energy Efficient Cortex-M4 MCU with Cortex-M0+ and Advanced Security](#).

Note: The information on this document is subject to change without notice.

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