

Automotive Dual Buck Regulator and Controller with Voltage Monitors and Watchdog Timer

FS5600

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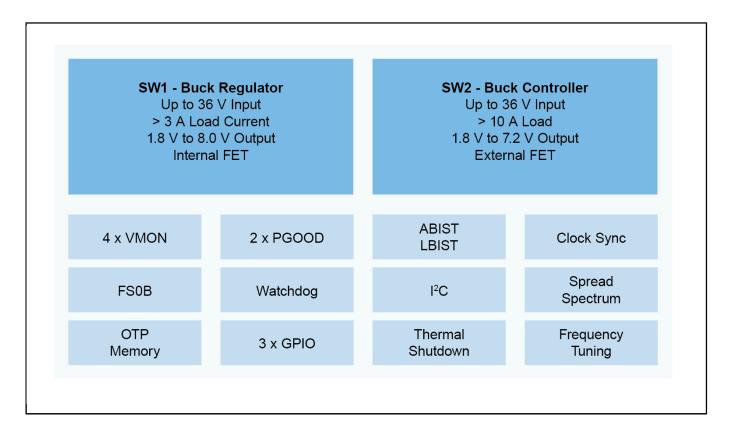
The FS56 System Basis Chip (SBC) for Automotive features a battery connected DC-DC buck controller with external FET and a battery connected DC-DC buck controller with internal FET.

In addition, it offers functional safety features such as independent voltage monitors, windowed watchdog timer, I/O monitoring via ERRMON and FCCU and built-in-self test. The FS56 is offered in QM, ASIL B and Enhanced ASIL B versions.

The FS56 offers highly configurable General Purpose Input/Outputs for seamless operation with PF PMICs as PF8x, PF7x and PF5x families.

The FS56 presents an optimized architecture for automotive with low-power modes and EMC minimizing as spread spectrum, frequency tuning and synchronization.

FS56 Block Diagram Block Diagram



View additional information for Automotive Dual Buck Regulator and Controller with Voltage Monitors and Watchdog Timer.

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